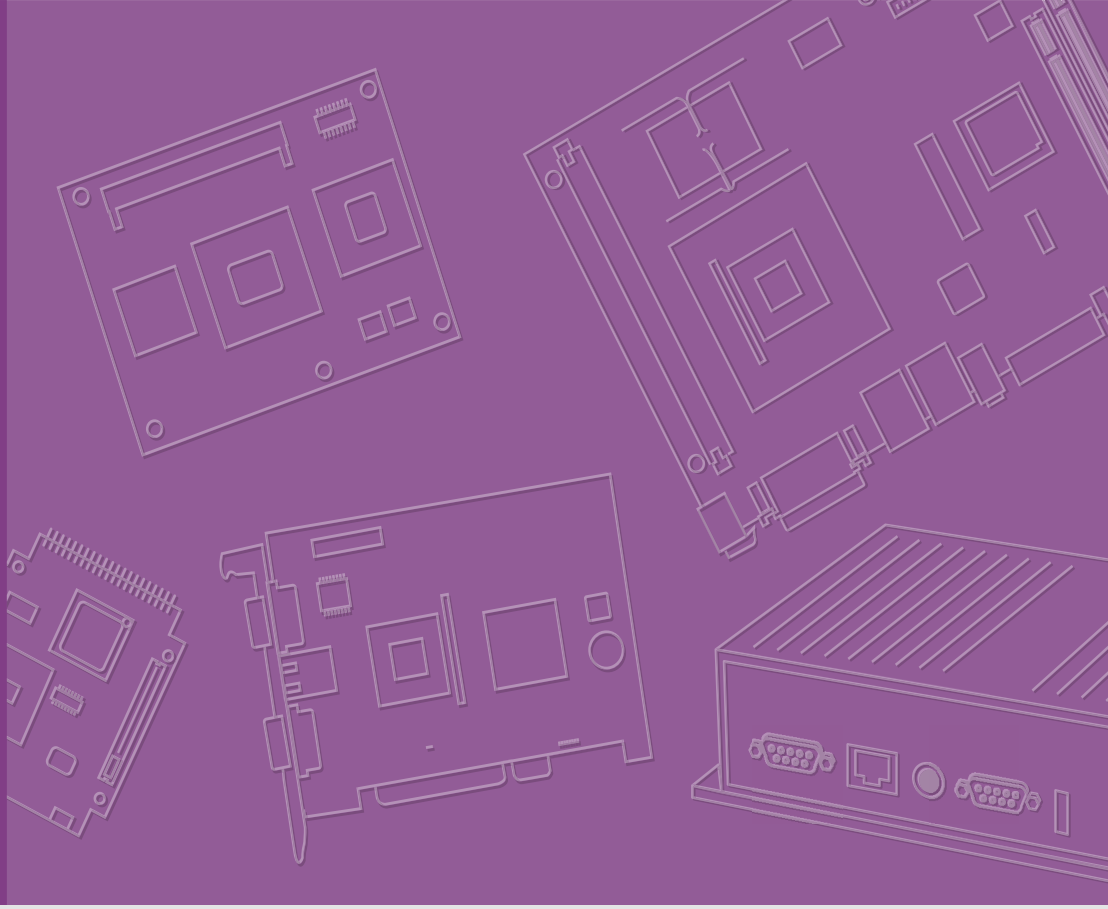

User Manual

ASMB-975 Series

Dual LGA 3647-P0 Intel Xeon® Server Board with 12 DDR4, 4 PCIe x16, 14 SATA3, 8 USB3.0, Dual 10GbE, IPMI

ADVANTECH

Enabling an Intelligent Planet

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Part No. 2001097511 Edition 2

August 2018

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FCC

This device complies with the requirements in part 15 of the FCC rules:

Operation is subject to the following two conditions:

- This device may not cause harmful interference
- This device must accept any interference received, including interference that may cause undesired operation.

Note! *This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.*

Initial Inspection

Before installing motherboard, please make sure that the following materials have been shipped:

- 1 x ASMB-975 Server Board
- 1 x ASMB-975 Startup Manual
- 1 x Driver CD
- 2 x Serial ATA HDD data cables
- 1 x I/O port bracket
- 2 x CPU power cable (8P)
- 2 x SATA power cable
- 1 x Warranty card
- 2 x Heatsink clip for CPU

If any of these items are missing or damaged, contact distributor or sales representative immediately. We have carefully inspected the ASMB-975 mechanically and electrically before shipment. It should be free of marks and scratches and in perfect working order upon receipt. When unpacking the ASMB-975, check it for signs of shipping damage. (For example, damaged box, scratches, dents, etc.) If it is damaged or it fails to meet the specifications, notify our service department or local sales representative immediately. Also notify the carrier. Retain the shipping carton and packing material for inspection by the carrier. After inspection, we will make arrangements to repair or replace the unit.

Order Information

Part Number	Chipset	Memory	GbE/10GbE LAN	IPMI	VGA Chip
ASMB-975-00A1	C621	DDR4 RDIMM	2/-	-	AST 2510
ASMB-975I-00A1	C621	DDR4 RDIMM	2/-	Yes	AST 2500
ASMB-975T2-00A1	C622	DDR4 RDIMM	2/2	Yes	AST 2500

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Chapter 1

Overview

1.1 Introduction

The ASMB-975 serverboard is the most advanced Intel Xeon Processor Scalable Family series board for server-grade IPC applications that require high-performance computing power & multi-expansion slots. This serverboard supports Intel Xeon Processor Scalable Family series processor and DDR4 2133/2400/2666 MHz memory up to 384 GB. ASMB-975 provides four PCIe x16, one PCIe x8 and four PCIe x4 slots in Gen3.0 high speed. In addition, the ASMB-975T2 has dual Gigabit and dual 10GbE Ethernet LAN ports that eliminate network bottlenecks. One GbE RJ-45 LAN and one 10GbE RJ-45 LAN jack are shared with IPMI function that allows remote control management. High reliability and outstanding performance makes ASMB-975 the ideal platform for industrial server/networking applications.

By using the Intel C622/C621 chipset, the ASMB-975 offers a variety of features such as 8 x USB3.0 and 3x USB 2.0 connectivity, 12 x onboard SATA III and 2 x M.2 (SATA only) interface. The 12 x SATA ports support software RAID 0, 1, 10 and 5 (Windows only*), and with the latest Intel RSTe (Rapid Storage Technology Enterprise) it provides compelling RAID solution for NVMe SSDs via Intel VROC (Virtual RAID on Chip) HW key.

These powerful I/O capabilities ensure even more reliable data storage capabilities and high-speed I/O peripheral connectivity.

Note!

1. *IPMI module will be included in ASMB-975I and ASMB-975T2 SKUs.*
2. *Three USB 2.0 ports (1*Type- A) and eight USB 3.0 ports (4 ports from on-board 20-pin header)*
3. *Please refer to the release note of each Linux OS for Intel's C622/ C621 chipset SATA RAID function support.*

1.2 Features

General

- **Intel Xeon Processor Scalable Family support:** ASMB-975 supports two Intel Xeon Platinum/Gold/Silver/Bronze series up to 28-core processors.
- **High performance I/O capability:** 2 x 10GbE (T2 SKU Only) + 2 x GbE LAN, 4 x PCIe x16 slot (x16 link) + 1 x PCIe x8 slot (x8 link), 12 x SATA and 2 x M.2 connectors,8 x USB 3.0 and 3 x USB 2.0 (incl. 1 x Type-A) ports.
- **Outstanding industrial features:** ASMB-975 provides industrial features like long product lifecycle, reliable operation under wide temperature range, watchdog timer, etc.
- **IPMI 2.0 support:** ASMB-975I/ASMB-975T2 equipped with ASPEED 2500 BMC chip supports IPMI 2.0 (Intelligent Platform Management Interface 2.0) via sharing LAN port.
- **KVM over IP:** KVM over IP function allows BIOS level remote control of ASMB-975I/ASMB-975T2 sku system through your own computer.

1.3 Specifications

Table 1.1: Specifications	
Processor	
CPU	■ Dual Intel LGA3647-P0 Xeon processor sockets ■ Supports Intel Processor Scalable Family, up to 28 cores ■ Supports the TDP of processor up to 205 W
System Memory	
Memory Capacity	■ Xeon processor supports DDR4 memory bus ■ Total 12 memory slots provided ■ Supports up to 384 GB memory ■ 1 DIMM slot per channel, 6 channels per processor
Memory Type	Supports DDR4 2133/2400/2666 MHz ECC-REG DIMM modules
DIMM Sizes	Each memory slot supports 4GB, 8GB, 16GB and 32GB memory modules
Memory Voltage	1.2 V
Error Detection	■ Corrects single-bit errors (Using ECC memory) ■ Detects double-bit errors (Using ECC memory)
On-Board Devices	
Chipsets	Intel C622/C621 PCH provide 20 PCIe Gen3 lanes (8 GT/s)
Network controllers	■ 2 x Intel I210-AT Gigabit Ethernet controller connected to C621/C622 through PCIe x1 Gen1. ■ Intel X557-AT2 10 Gigabit Ethernet controller connected to C622 through PCIe Gen3 lane.
VGA	ASPEED AST2500/2510 controller with 64 MB VGA memory provides basic 2D VGA function.
EC	ITE IT8528E chip provide motherboard keyboard mouse, RS-232, parallel port and hardware monitor functions
BMC (975I/975T2 SKUs)	One of Intel I210 Gigabit Ethernet connected to AST2500 for sharing IPMI / iKVM
Input / Output	
Storage	■ Total 12 x SATA ports and 2 x M.2 (SATA only) provide 6 Gb/s bandwidth ■ RAID 0, 1, 5, 10 support (Windows only. For Linux support please refer to note item 3 of chapter 1.1)
LAN	■ 4 x RJ-45 LAN ports (2 x 10GbE + 2 x 10/100/1000 Base-T LAN)
USB	■ 4 x USB 3.0 ports at rear window ■ 2 x USB 3.0 internal header (4 ports) ■ 1 x USB 2.0 internal headers (2 ports) ■ 1 x internal Type-A USB port.
Graphics	■ 1 x VGA port.
Serial Port / Header	■ 1 x RS232 port at rear window, 1 x internal header (2 x 5P pitch: 2.50 mm), both ports are RS-232 (5V)
Keyboard / Mouse	■ PS/2 keyboard and mouse connector (onboard).

Table 1.1: Specifications	
Power Connector	
System Power	1 x 24-pin SSI EPS 12V power connector (Input 12V, 5V, 3.3V, 5Vsb)
CPU Power	2 x 8-pin SSI EPS 12V power connector for CPU & Memory power (12V)
PCIe slot power	1 x 8-pin power connector for PCIe slot 12V input
Expansion Slots	
PCI-express	■ 4 x PCIe x16 slot (Gen3 x16 link) – PCIEX16_SLOT2 (from CPU 0) – PCIEX16_SLOT4 (from CPU 1) – PCIEX16_SLOT6 (from CPU 1) – PCIEX16_SLOT8 (from CPU 0) ■ 1 x PCIe x8 slot (Gen3 x8 link) – PCIEX8_SLOT9 (from CPU 1) ■ 4 x PCIe x4 slot (Gen3 x4 link) – PCIEX4_SLOT1 (from CPU1) – PCIEX4_SLOT3 (from CPU1) – PCIEX4_SLOT5 (from CPU0) – PCIEX4_SLOT7 (from CPU0)
System BIOS	
BIOS Type	128 Mb SPI Flash EEPROM with AMI BIOS
PC Health Monitoring	
Voltage	Monitors for CPU Cores, +3.3V, +5V, +12V, +5V Standby, VBAT
FAN	■ Two 4-pin headers for CPU cooler and eight 4-pin headers for system fans ■ All fans with tachometer status monitoring (except SYSFAN6 and SYSFAN7) ■ Thermal control for all fan connectors
Temperature	■ Monitoring for CPU (PECI) ■ Monitoring for System (EC)
Other Features (Case Open)	■ Chassis intrusion detection ■ Chassis intrusion header
Operating Environment / Compliance	
RoHS	RoHS Compliant 6/6 Pb Free
Environmental Spec.	■ Operating Temperature: 0 to 40° C ■ Non-operating Temperature: -40 to 85° C ■ Operating Relative Humidity: 10% to 90% (non-condensing) ■ Non-operating Relative Humidity: 10% to 95% (non-condensing)

1.4 Board Layout, Jumpers and Connectors

Connectors on the ASMB-975 are linked to external devices such as hard disk drives. In addition, ASMB-975 has a number of jumpers that are used to configure the system for specific applications.

The tables below list the functions of each jumper and connector. Later sections in this chapter give instructions for setting jumpers. Chapter 2 gives instructions for connecting external devices to ASMB-975.

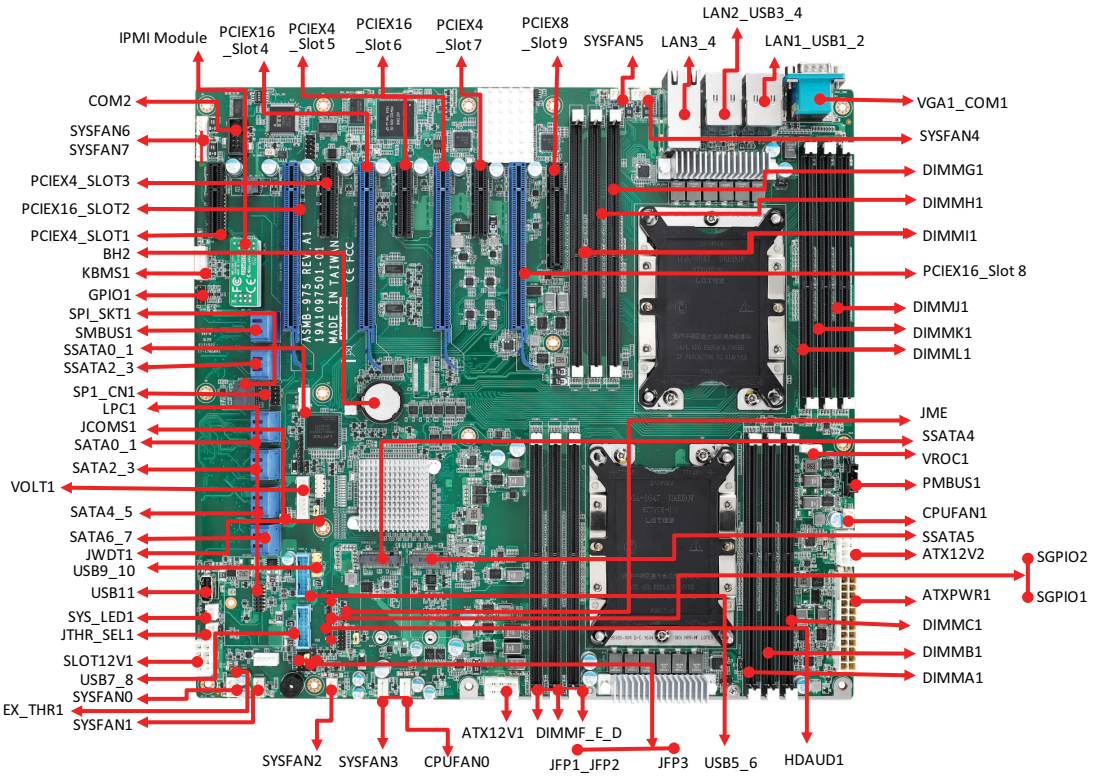


Figure 1.1 Board Layout

ASMB-975-00A1

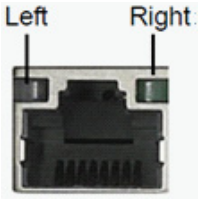
ASMB-975I-00A1

ASMB-975T2-00A1

Figure 1.2 Rear I/O of full SKU

Table 1.2: Onboard LAN LED Color Definition

10/100/1000 Mbps LAN Mbps LAN Link/Activity LED Scheme

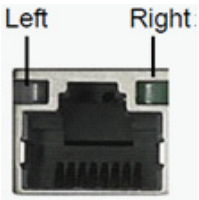


LAN1 & LAN2 (1G)

		Left LED	Right LED
10 Mbps	Link Active	Off Off	Green Blinking green
100 Mbps	Link Active	Amber Amber	Green Blinking green
1000 Mbps	Link Active	Green Green	Green Blinking green
No Link		Off	Off

Table 1.3: Onboard LAN LED Color Definition

100/1000 & 10G bps LAN Mbps LAN Link/Activity LED Scheme

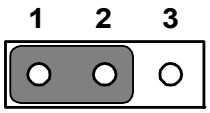


LAN3 & LAN4 (10G)

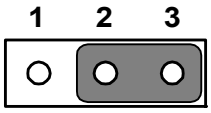
		Left LED	Right LED
100M bps	Link Active	Off Off	Green Blinking green
1G bps	Link Active	Amber Amber	Green Blinking green
10G bps	Link Active	Green Green	Green Blinking green
No Link		Off	Off

Table 1.4: Jumpers

Label	Function	Default
JCMOS1	CMOS Clear	1-2
JME1	ME update	1-2
JWDT1	Watch Dog Reset	1-2
PSON1	AT(1-2) / ATX(2-3)	2-3
JCASE1	Chassis case open alarm	1-2
JTHR_SEL	On board(1-2)/external thermistor(2-3)	1-2



Keep CMOS data/
disable ME update/



Clear CMOS data/
Enable ME update/

Table 1.5: Connectors

Label	Function
ATX12V1	SSI EPS 12V auxiliary power connector (for CPU0) and memory
ATX12V2	SSI EPS 12V auxiliary power connector (for CPU1) and memory
ATXPWR1	SSI EPS 24-pin main power connector (for system)
BH2	For optional battery kit
BIOS_SKT1	BIOS SPI ROM
BMC_CN1, BMC_CN2	IPMI connector
COM2	Serial port: RS-232
CPU0	Intel LGA3647 CPU0 socket
CPU1	Intel LGA3647 CPU1 socket
CPUFAN0	CPU0 fan connector (8-pin)
CPUFAN1	CPU1 fan connector (8-pin)
DIMMA1	Channel A1 DIMMA1 of CPU0
DIMMB1	Channel B1 DIMMB1 of CPU0
DIMMC1	Channel C1 DIMMC1 of CPU0
DIMMD1	Channel D1 DIMMD1 of CPU0
DIMME1	Channel E1 DIMME1 of CPU0
DIMMF1	Channel F1 DIMMF1 of CPU0
DIMMG1	Channel G1 DIMMG1 of CPU1
DIMMH1	Channel H1 DIMMH1 of CPU1
DIMMI1	Channel I1 DIMMI1 of CPU1
DIMMJ1	Channel J1 DIMMJ1 of CPU1
DIMMK1	Channel K1 DIMMK1 of CPU1
DIMML1	Channel L1 DIMML1 of CPU1
EC_SMBUS1	For EC debug
EX_THR1	Connector for external thermistor
GPIO1	GPIO function for customize usage
HDAUD1	Audio header
JFP1/JFP2/JFP3	Front panel pin header
KBMS1	For additional keyboard/mouse
LAN1_2, LAN3_4	RJ-45 LAN connector
LANLED1	LAN LED extension connector
LPC1	LPC port for debug & TPM module
SSATA4, SSATA5	SATA port 4/5 for M.2 2242 SATA SSD
PMBUS1	PMBUS connector to communicate with power supply
PEHP1	NVMe RAID LED control
SATA0~SATA7	Serial ATA0~7
SSATA0~SSATA3	sSATA port 0~3
SGPIO1	Supports Serial_Link interface for onboard SATA connections

Table 1.5: Connectors	
SLOT1	PCIE x 4 slot of CPU1
SLOT2	PCIE x 16 slot of CPU0
SLOT3	PCIE x 4 slot of CPU1
SLOT4	PCIE x 16 slot of CPU1
SLOT5	PCIE x 4 slot of CPU0
SLOT6	PCIE x 16 slot of CPU1
SLOT7	PCIE x 4 slot of CPU0
SLOT8	PCIE x 16 slot of CPU0
SLOT9	PCIE x 8 slot of CPU1
SLOT12V1	For PCIe slot 12V input only
SMBUS1	Front panel SMBus header
SPI_CN1	Connector for BIOS update tool
SPI_SKT1	EC EEPROM
SYSFAN0-SYSFAN7	System FAN connector (4-pin)
SYS_LED1	System LED connector
USB5_6,USB7_8	USB 3.0 port 5,6,7,8 (20 pin header)
USB9_10	USB 2.0 pin header 9, 10
USB11	USB 2.0 port 11 (Type-A)
LAN1_USB1_2	RJ-45 LAN port 1 + USB 3.0 port 1/2 connector
LAN2_USB3_4	RJ-45 LAN port 2 + USB 3.0 port 3/4 connector
VOLT1	Voltage display
VROC1	Intel Virtual RAID (VROC) key
VGA1_COM1	VGA and COM connector
COM2	Serial port: RS-232

Table 1.6: Onboard LED			
LED	**Description**	**LED Definition**	
5V_LED1	Power on LED	Off: Power off	On (Green): System is On
5VSB_LED1	Standby LED	Off: No input AC Power	On (Green): System is ON, in sleep mode, or in soft-off mode
LED3	BMC heartbeat LED (ASMB-975I & ASMB-975T2 SKU Only)	Blinking (Green): controller is working normally	

1.5 Block Diagram

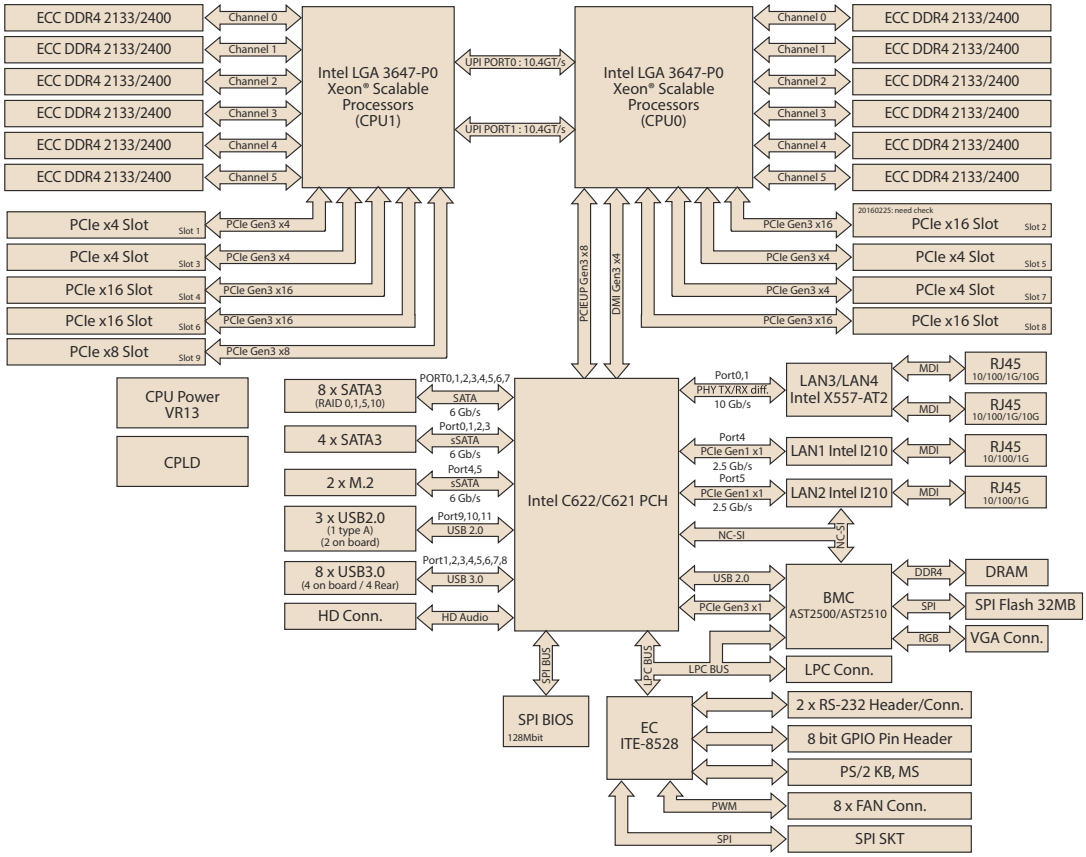


Figure 1.3 Block Diagram

1.6 System Memory

ASMB-975 has twelve 288-pin memory slots for DDR4 2133/2400/2666 MHz memory modules with maximum capacity of 384 GB (Maximum 32 GB for each DIMM). ASMB-975 supports registered DIMMs memory module.

1.7 Memory Installation Procedures

	Single CPU Installed (CPU0)					
Quantity of memory installed	1	2	3	4	5	6
DIMMA1	v	v	v	v	v	v
DIMMB1		v	v	v	v	v
DIMMC1			v	v	v	v
DIMMD1				v	v	v
DIMME1					v	v
DIMMF1						v
DIMMG1						
DIMMH1						
DIMMI1						
DIMMJ1						
DIMMK1						
DIMML1						

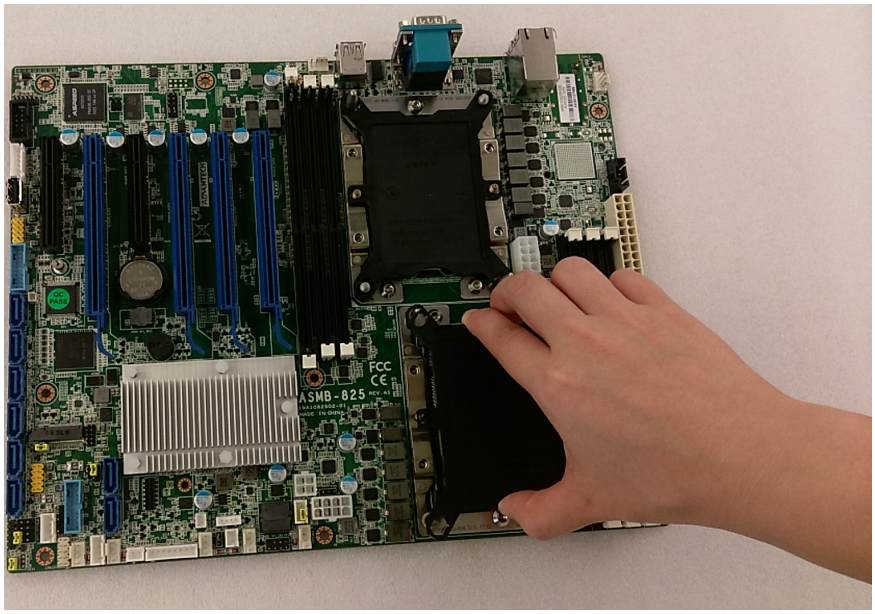
	Dual CPU Installed (CPU0 & CPU1)										
Quantity of memory installed	2	3	4	5	6	7	8	9	10	11	12
DIMMA1	v	v	v	v	v	v	v	v	v	v	v
DIMMB1		v	v	v	v	v	v	v	v	v	v
DIMMC1				v	v	v	v	v	v	v	v
DIMMD1						v	v	v	v	v	v
DIMME1								v	v	v	v
DIMMF1										v	v
DIMMG1	v	v	v	v	v	v	v	v	v	v	v
DIMMH1			v	v	v	v	v	v	v	v	v
DIMMI1					v	v	v	v	v	v	v
DIMMJ1							v	v	v	v	v
DIMMK1									v	v	v
DIMML1											v

1.8 Processor Installation

The ASMB-975 is designed for Intel Xeon processor scalable family.

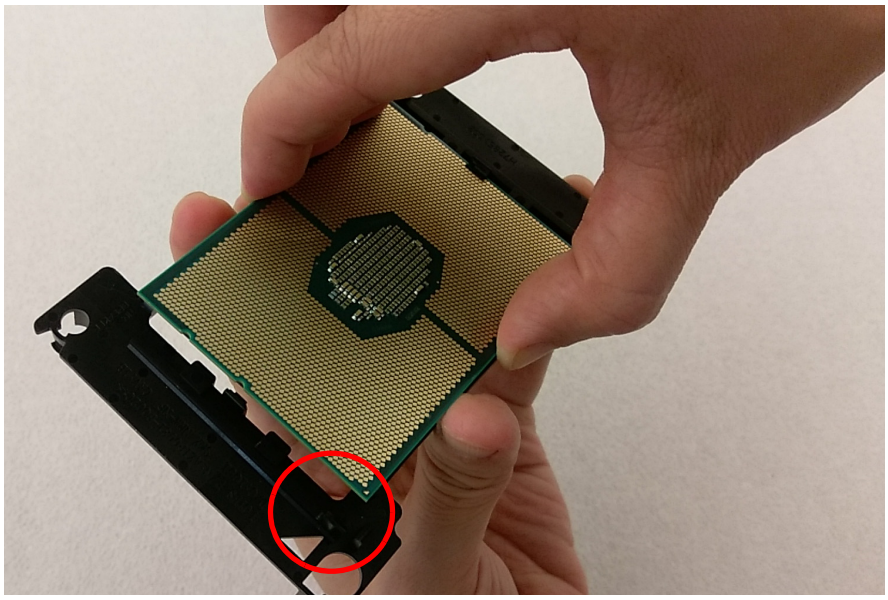
Step 1

Remove dust cover.



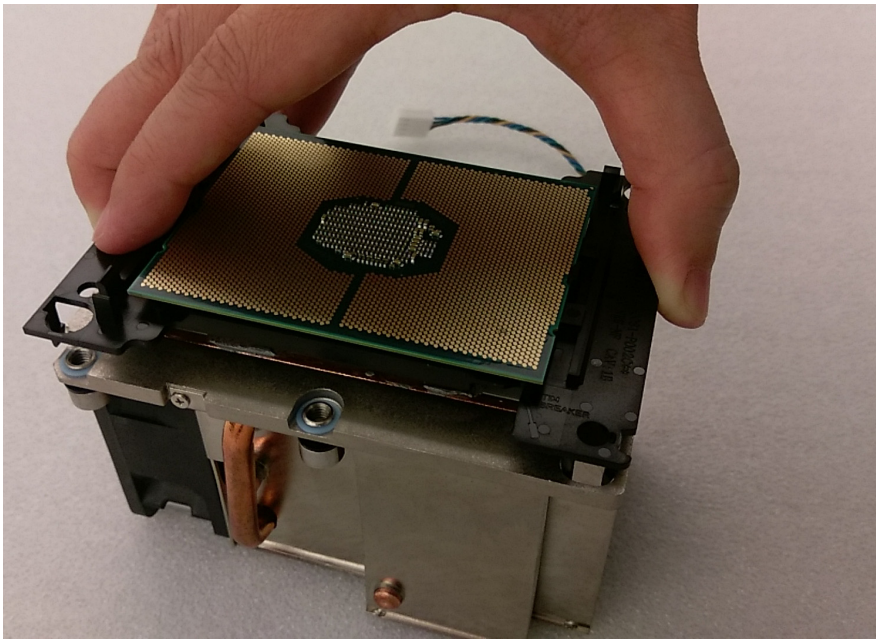
Step 2

Install CPU on CPU clip and align pin 1 mark.



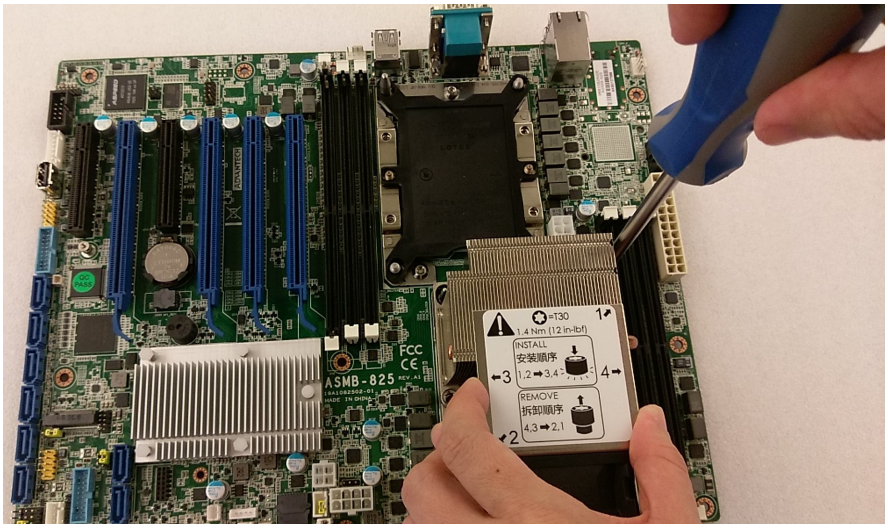
Step 3

Install the CPU clip assembly on the heatsink as a processor + heatsink module.



Step 4

Put the processor heatsink module into the motherboard bolster plate by using a T-30 screw driver (follow heatsink label direction 1-2-3-4).



Chapter 2

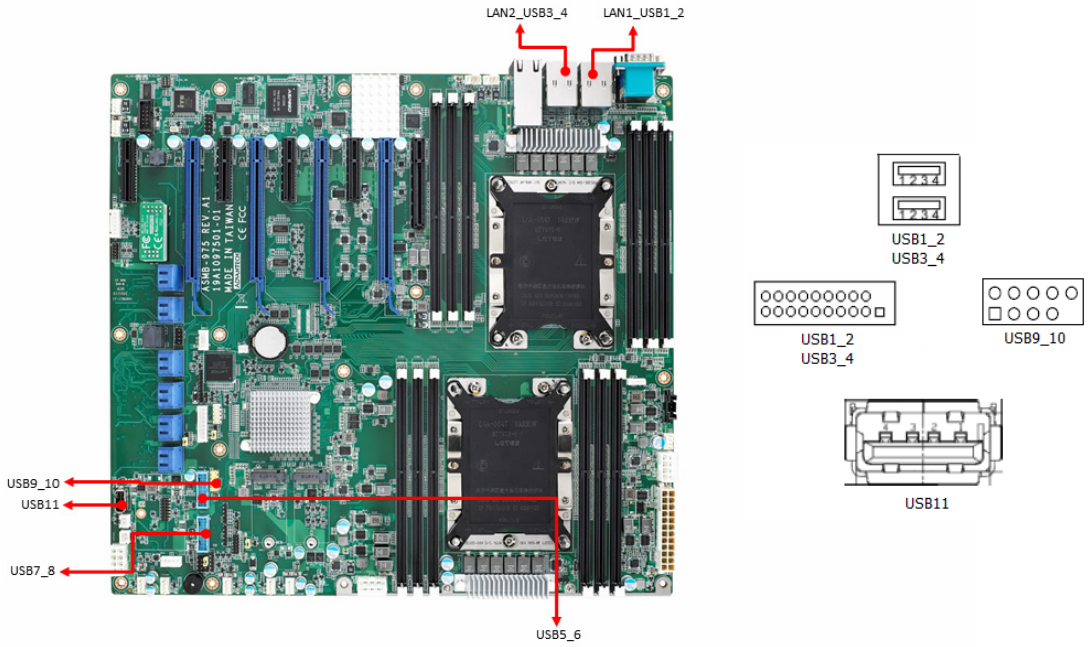
Connections

2.1 Introduction

You can access most of the connectors from the top of the board as it is being installed in the chassis. If you have a number of cards installed, you may need to partially remove a card to make all the connections.

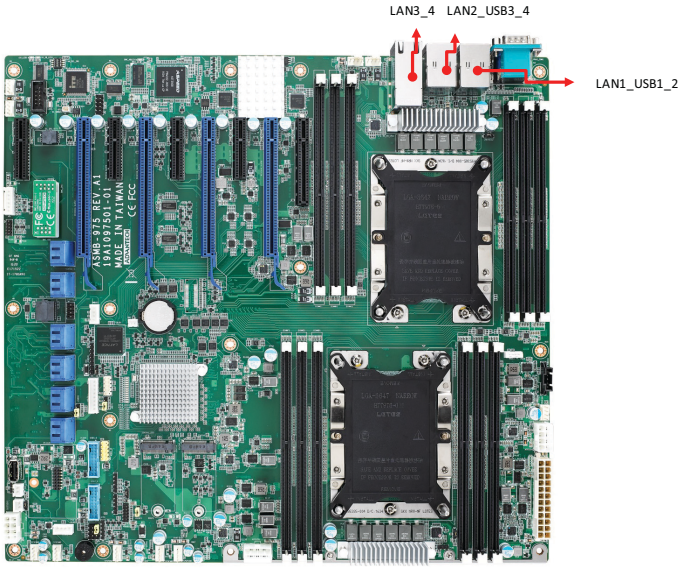
2.2 USB Ports (USB1~11)

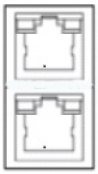
The two USB 3.0 ports on the rear plate and four USB3.0 on-board ports could reach transmission rates up to 5Gbps, besides the two USB2.0 on-board ports and one TypeA USB2.0 ports with 480 Mbps speed. Fuse protection are supported. The USB interface can be disabled in the system BIOS setup.



2.3 LAN Ports (LAN1_2, LAN3_4)

The ASMB-975T2 is equipped with two 10GbE ports and two up to 1000 Mbps Ethernet LANs. They are all with RJ-45 jacks and supported by all major network operating systems. One of GbE and one of 10GbE are shared with IPMI for system management.

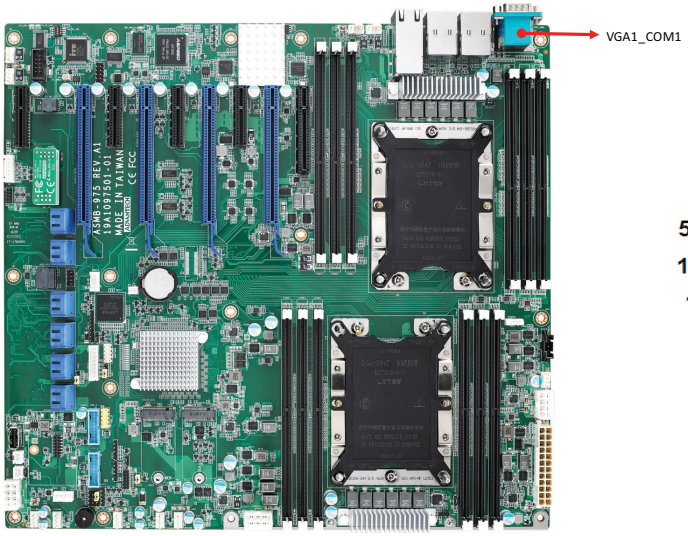


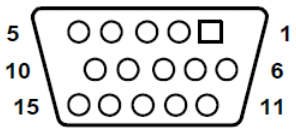


LAN1_2
LAN3_4

2.4 VGA Connector (VGA1)

The ASMB-975 includes a VGA interface that can drive conventional CRT and LCD displays.

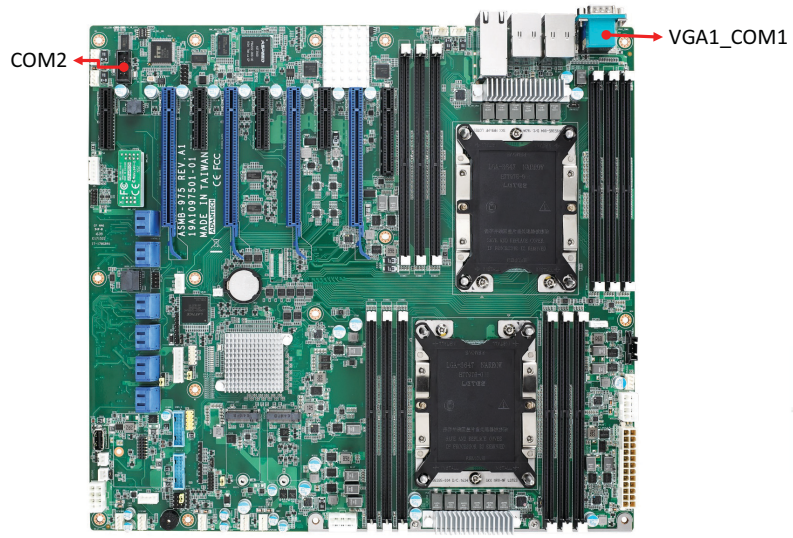


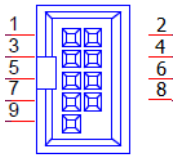


VGA1

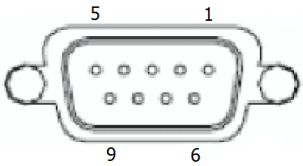
2.5 Serial Ports (COM1~2)

The ASMB-975 offers one serial port on the rear plate and one 2.54mm pitch 9-pin header onboard.





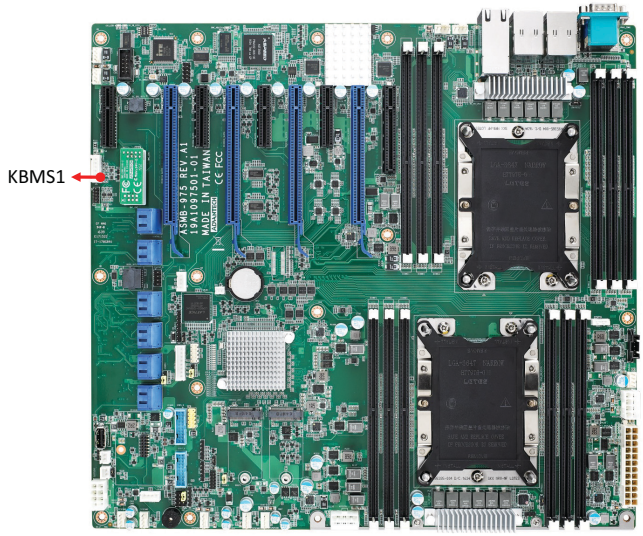
COM2

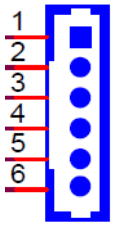


COM1

2.6 PS2 Keyboard and Mouse Connectors (KBMS2)

The 6-pin KBMS2 connector is for additional keyboard & mouse device usage.





KBMS2

2.7 CPU Fan Connector (CPUFAN0~1)

If a fan is used, this connector supports cooling fans that draw up to 1.5A (18W).

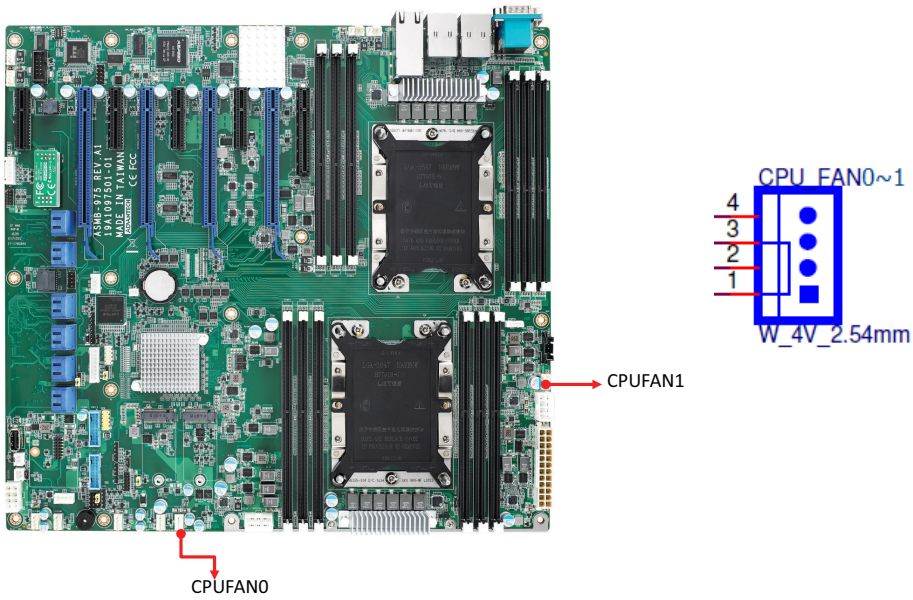
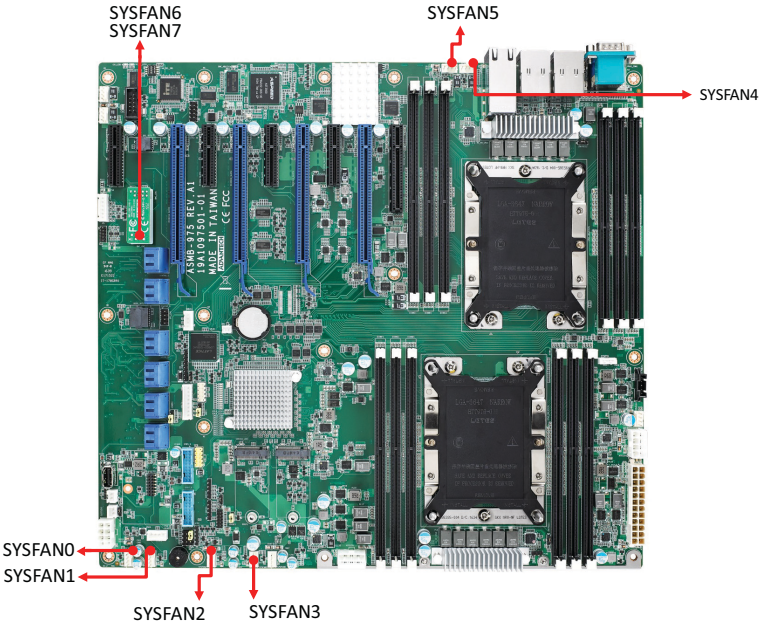


Table 2.1: CPU FAN Pin Definition

	CPUFAN0	CPUFAN1
1	GND	GND
2	+12V	+12V
3	CPU0_TACH	CPU1_TACH
4	CPU0_PWM	CPU1_PWM

2.8 System Fan Connector (SYSFAN0~7)



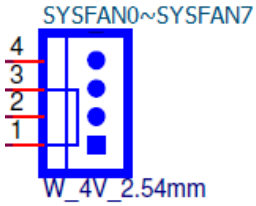
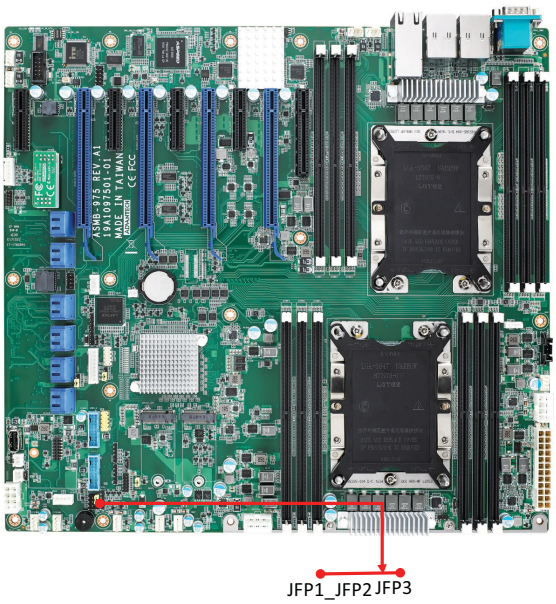


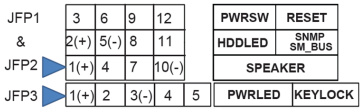
Table 2.2: SYS FAN Pin Definition

	SYSFAN 0	SYSFAN 1	SYSFAN 2	SYSFAN 3	SYSFAN 4	SYSFAN 5	SYSFAN 6	SYSFAN 7
1	GND	GND	GND	GND	GND	GND	GND	GND
2	+12V	+12V	+12V	+12V	+12V	+12V	+12V	+12V
3	FAN0_ TACH	FAN1_ TACH	FAN2_ TACH	FAN3_ TACH	FAN4_ TACH	FAN5_ TACH	FAN6_ TACH	FAN7_ TACH
4	FAN2_ PWM	FAN2_ PWM	FAN1_ PWM	FAN1_ PWM	FAN3_ PWM	FAN3_ PWM	FAN3_ PWM	FAN3_ PWM

2.9 Front Panel Connector (JFP1)

There are several external switches and LEDs to monitor and control the ASMB-975.



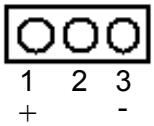


2.9.1 Power LED (JFP3)

JFP3 pin 1 and pin 3 are for the power LED. Refer to Appendix B for detailed information on the pin assignments. If an ATX power supply is used, the system's power LED status will be as indicated as follows.

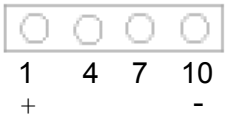
Table 2.3: ATX Power Supply LED Status

ACPI Power Mode	LED (ATX power)
System On (S0)	On
System Standby (S1)	Fast flashes
System Hibernation(S4)	Slow flashes
System Off (S5)	Off



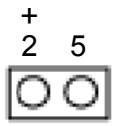
2.9.2 External Speaker (JFP2 pins 1, 4, 7, 10)

JFP2 pins 1, 4, 7, 10 connect to an external speaker. The ASMB-975 provides an onboard buzzer as an alternative. To enable the buzzer, set pins 7-10 closed.



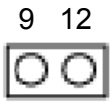
2.9.3 HDD LED Connector (JFP1 Pins 2 & 5)

You can connect an LED to connector JFP1 to indicate when the HDD is active.



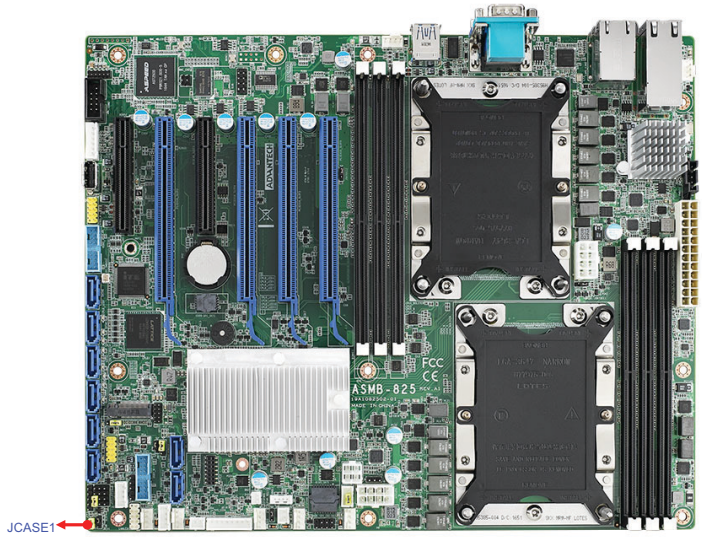
2.9.4 Reset Connector (JFP1 Pins 9 & 12)

Many computer cases offer the convenience of a reset button.

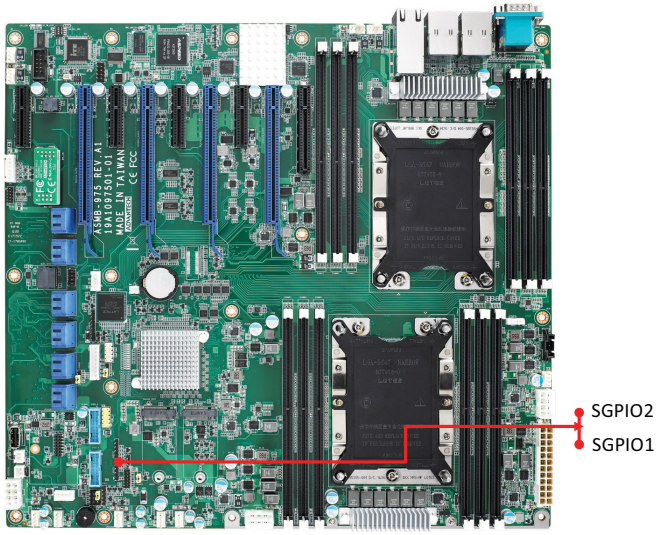


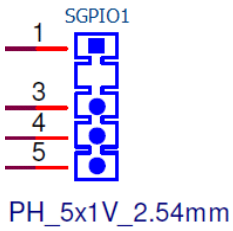
2.9.5 Case Open (JCASE1)

A chassis Intrusion header is located at JCASE1 on the motherboard. Attach the appropriate cable from the chassis to be informed of a chassis intrusion when the chassis is opened. The default function is disabled and Pin 1-2 is bridged by a jumper cap.



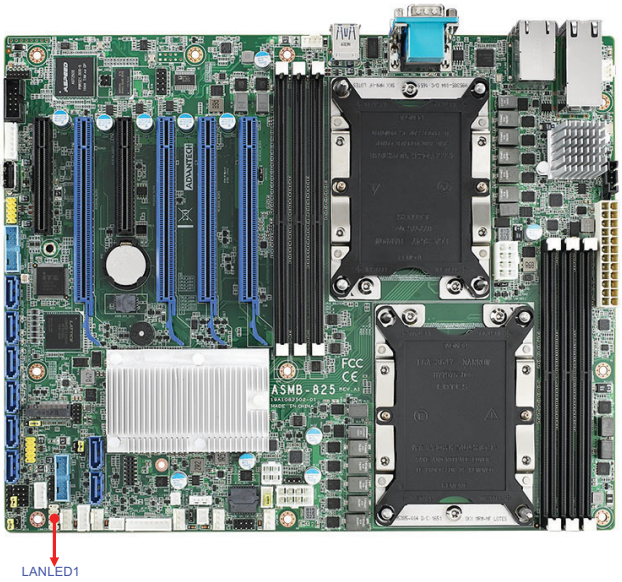
2.10 SATA SGPIO (SGPIO1)

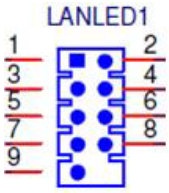




1	SCLOCK_PCH
2	NC
3	SLOAD_PCH
4	SDATAOUT0_PCH
5	SDATAOUT1_PCH

2.11 Front Panel LAN Indicator Connector (LANLED1)

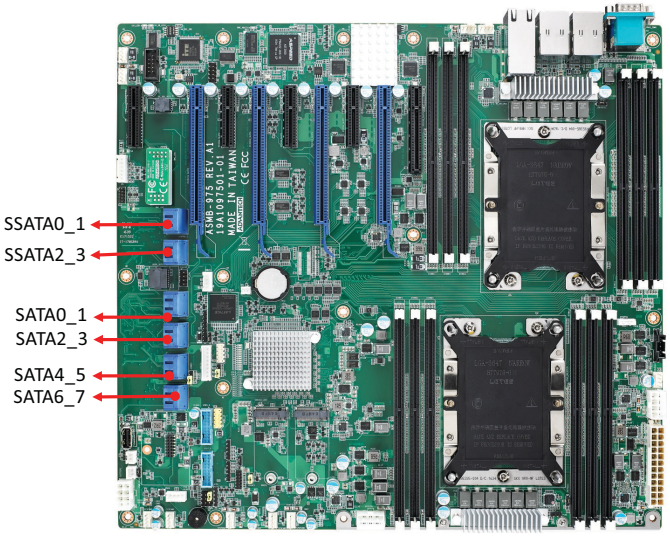




1	LAN1_LED_ACT#	2	LAN2_LED_ACT#
3	+V3.3_AUX	4	+V3.3_AUX
5	LAN3_LED_ACT#	6	LAN4_LED_ACT#
7	+V3.3_AUX	8	+V3.3_AUX
9	NC		

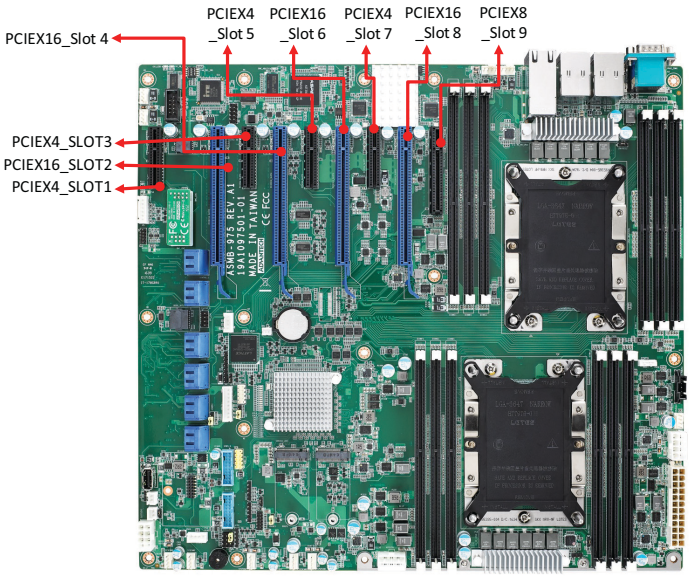
2.12 SATA and M.2 Connector (SATA0~7,SSATA0~3,SSATA4~5 as M2_2242)

ASMB-975 features 12 SATA III interface ports (up to 600 MB/s) which ease cabling to hard drives with thin and long cables. In addition, there are two M.2 2242 connectors supporting SATAIII as well.



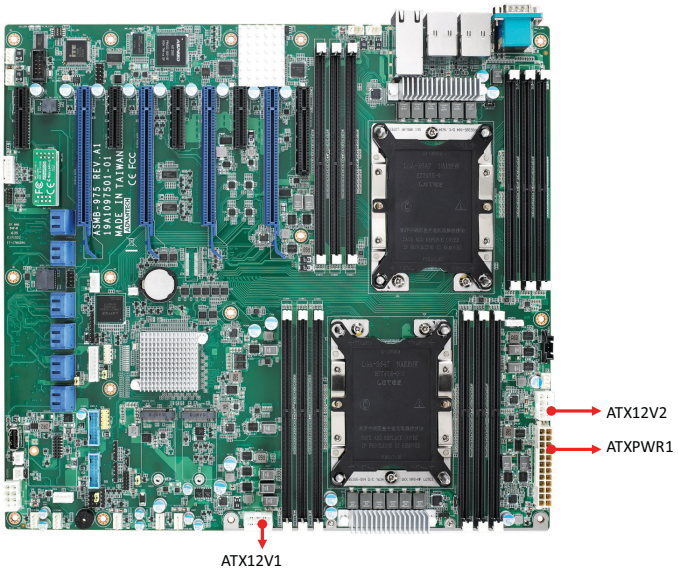
2.13 PCIe Expansion Slots

The ASMB-975 provides nine expansion slots that can support four double-deck cards, one PCIe x 8 card and one PCIe x4 card.



	Slot Length	Link	PCI-E Generation	PCIe link provide from
SLOT1	PCIE x4	PCIE x4	3	CPU1
SLOT2	PCIE x16	PCIE x16	3	CPU0
SLOT3	PCIE x4	PCIE x4	3	CPU1
SLOT4	PCIE x16	PCIE x16	3	CPU1
SLOT5	PCIE x4	PCIE x4	3	CPU0
SLOT6	PCIE x16	PCIE x16	3	CPU1
SLOT7	PCIE x4	PCIE x4	3	CPU0
SLOT8	PCIE x16	PCIE x16	3	CPU0
SLOT9	PCIE x8	PCIE x8	3	CPU1

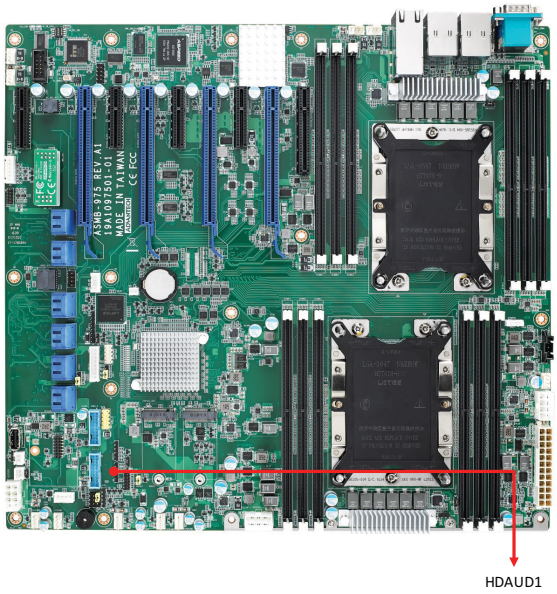
2.14 Auxiliary Power Connector (ATXPWR1/ ATX12V1/ATX12V2)

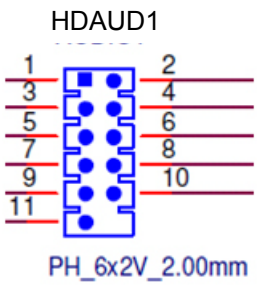


Note!

1. *Please use a power supply which is of SSI type; minimum output should be at least 500 W.*
2. *ATXPWR1 & ATX12V1 & ATX12V2 sockets should be all connected with power supply, otherwise ASMB-975 will not boot up normally.*

2.15 HD Audio Interface Connector (HDAUD1)

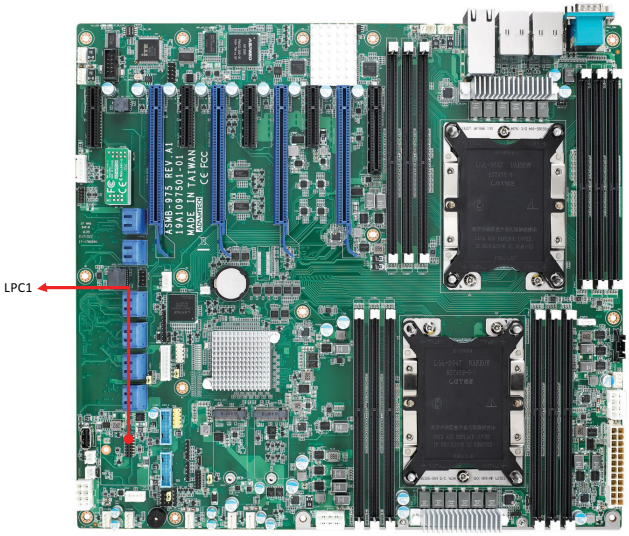


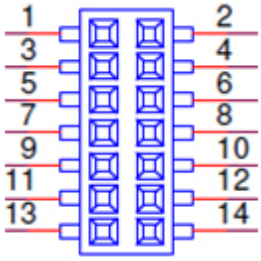


1	+5V_AUDIO	2	GND
3	ACZ_SYNC	4	ACZ_BITCLK
5	ACZ_SDOUT	6	ACZ_SDIN0
7	ACZ_SDIN1	8	ACZ_RST#
9	+12V_AUDIO	10	GND
11	GND		

2.16 LPC Connector (LPC1) for Optional TPM Module

ASMB-975 has one LPC connector that can be installed Advantech's TPM Module for security management.



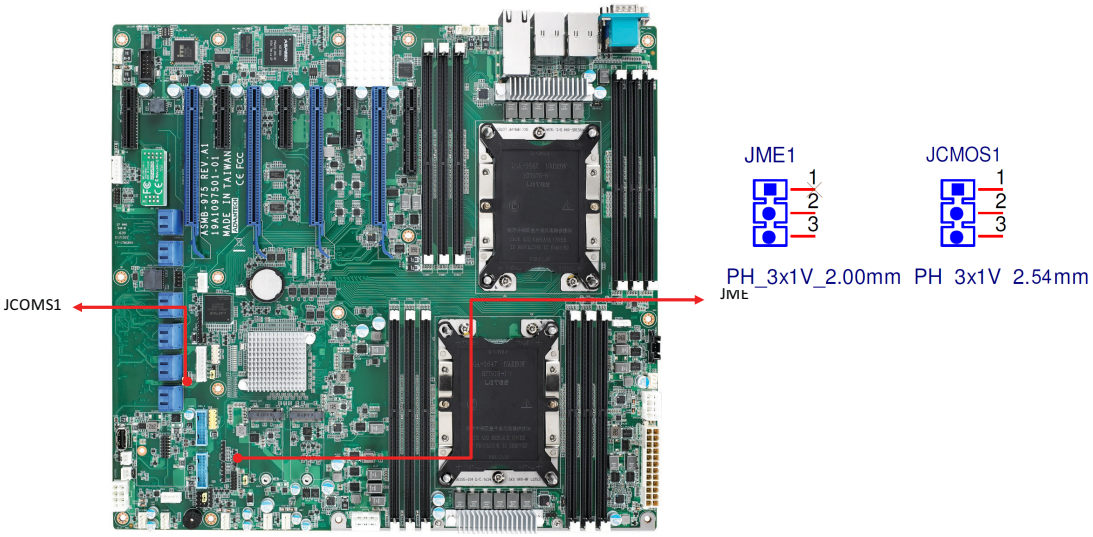


LPC1

1	LPC_CLK_24M	2	LPC_AD1
3	PLTRST_LPC	4	LPC_AD0
5	LPC_FRAME#	6	+3.3V
7	LPC_AD3	8	GND
9	LPC_AD2	10	SMB_SCL_LPC
11	SERIRQ_PCH	12	SMB_SDA_LPC
13	+5V_AUX	14	+5V

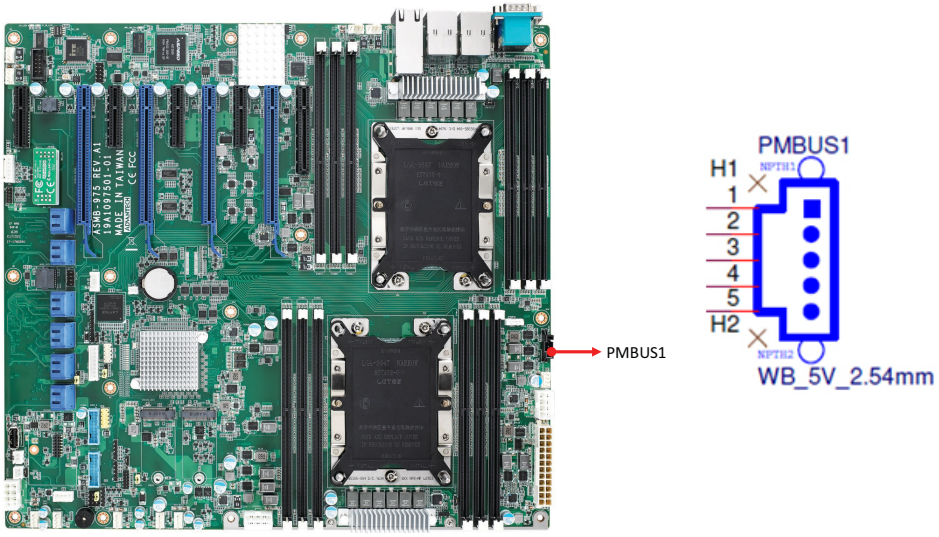
2.17 Clear CMOS Connector (JCMOS1, JME1)

Setting jumper of JCMOS1 from pin 1-2 to pin 2-3,then back to pin 1-2 to reset CMOS data. Default setting of JME1 jumper is pin 1-2. Setting jumper of JME1 from pin 1-2 to pin 2-3 to enable ME update.



	JCMOS1	JME1
1	NC	NC
2	RTC_RST_PCH#	HDA_SDOUT_PCH
3	GND	+3.3V_AUX

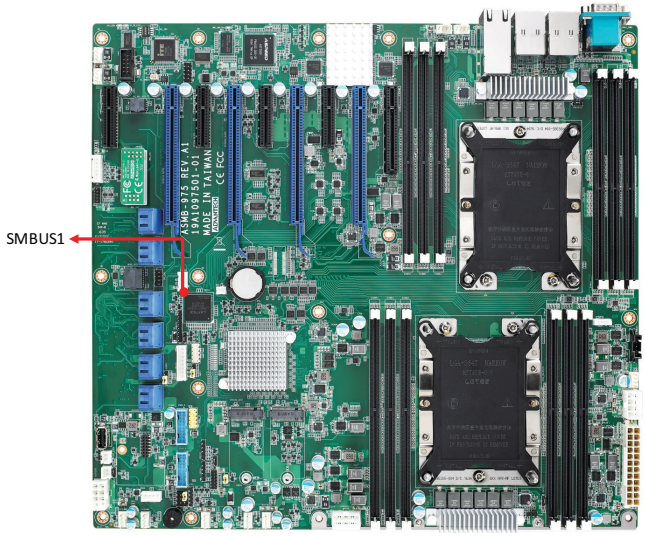
2.18 PMBUS Connector (PMBUS1)

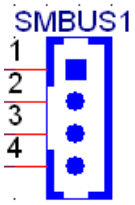


1	SMB_SCL_PM
2	SMB_SDA_PM
3	SMB_ALERT_PM#
4	GND
5	+3.3V

2.19 Front Panel SMBUS Connector (SMBUS1)

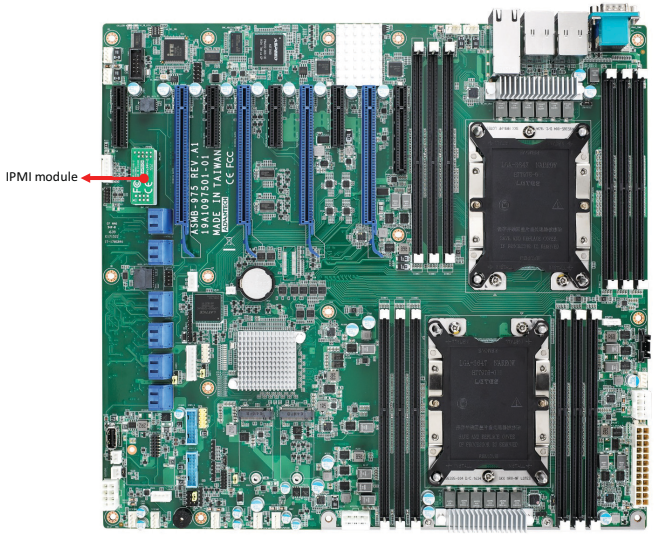
For ASMB-975I/ASMB-975T2 sku, this connector is connected to BMC chip and BMC is the mater in this bus. For ASMB-975 SKU, this connector is connected to PCH, and PCH is the mater in this bus.

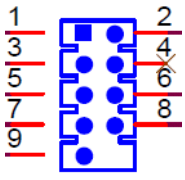




2.20 IPMI Module Connector (BMC_CN1 & BMC_CN2)

This connector only fits to ASMB-BMC-00A1E that will exist in ASMB-975I & ASMB-975T2 SKUs for enabling IPMI function.

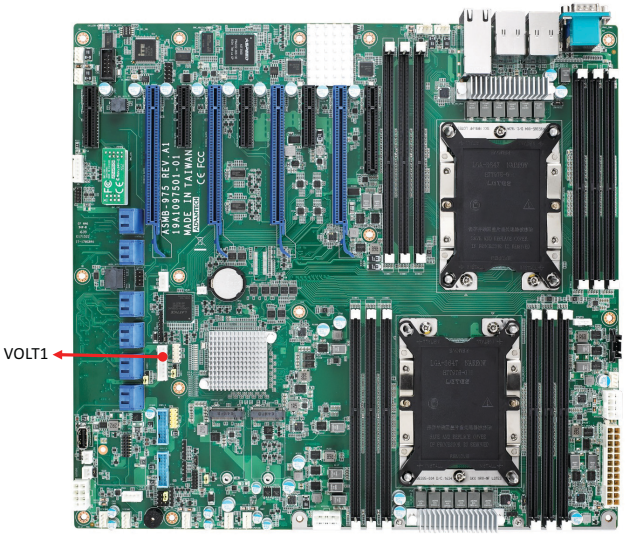


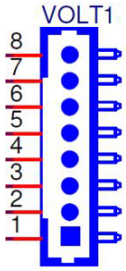


BMC_CN1

2.21 VOLT1 Connector (VOLT1)

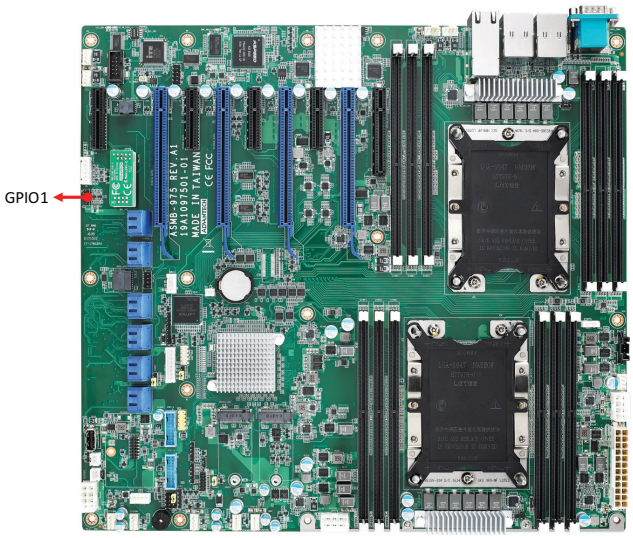
VOLT1 connects to the alarm board on the Advantech chassis. These alarm boards give warnings if a power supply or fan fails, if the chassis overheats, or if the back-plane malfunctions.

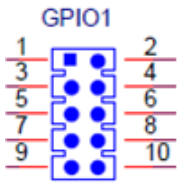




1	5VSB	5	+5V
2	GND	6	+3.3V
3	GND	7	-12V
4	-5V	8	+12V

2.22 GPIO Connector (GPIO1)





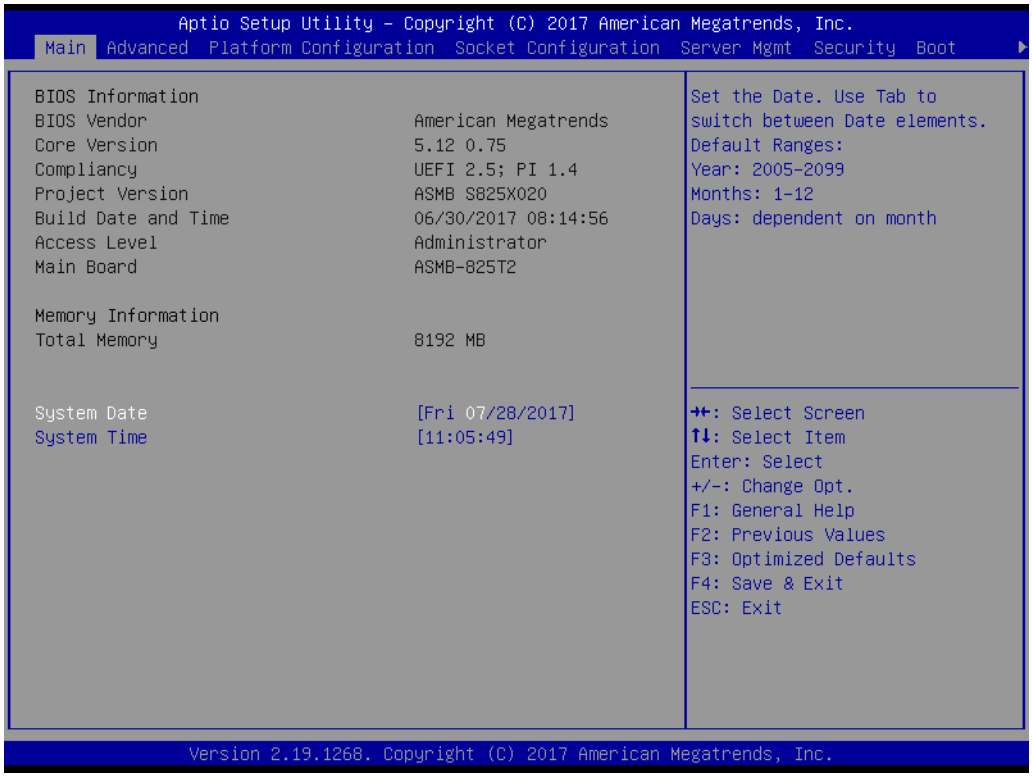
1	EC_GOIP 0	2	EC_GOIP 4
3	EC_GOIP 1	4	EC_GOIP 5
5	EC_GOIP 2	6	EC_GOIP 6
7	EC_GOIP 3	8	EC_GOIP 7
9	+VCC_GPIO	10	GND

Chapter 3

AMI BIOS

3.1 Introduction

With the AMI BIOS Setup program, you can modify BIOS settings and control the special features of your computer. The Setup program uses a number of menus for making changes and turning the special features on or off. This chapter describes the basic navigation of the ASMB-825 setup screens.



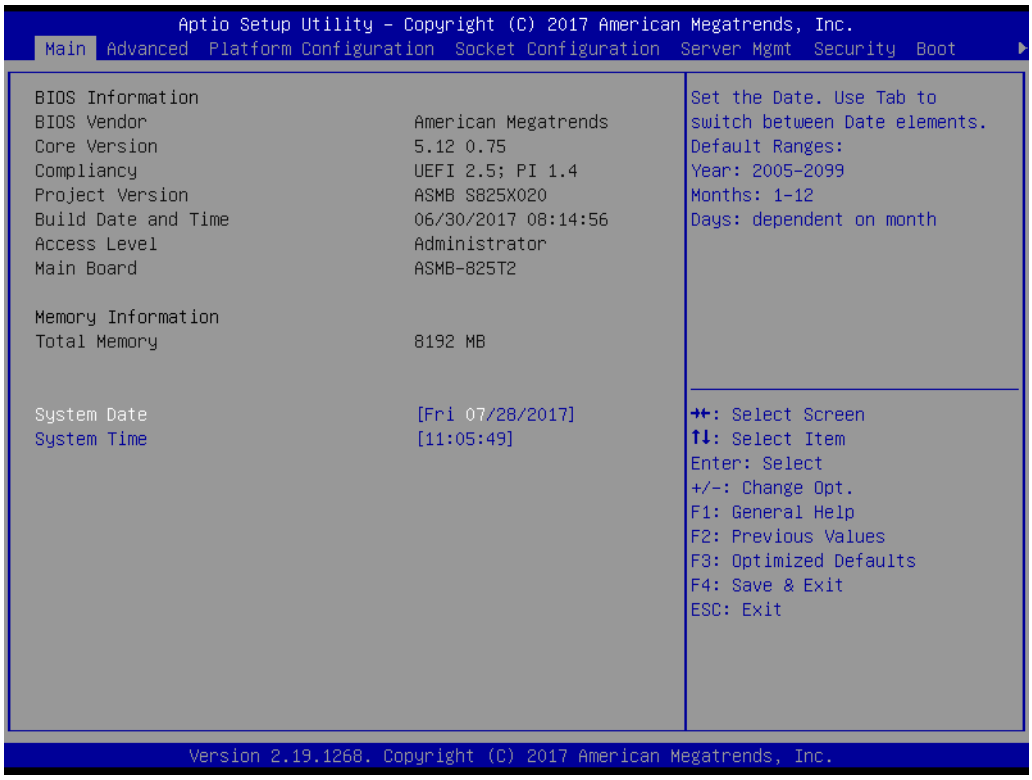
AMI's BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed up CMOS so it retains the Setup information when the power is turned off.

Note! *The BIOS setup screens shown in this chapter are for reference only, they may not exactly match what you see on your display devices.*

3.2 BIOS Setup

3.2.1 Main Menu

Press <Del> during bootup to enter AMI BIOS CMOS Setup Utility; the Main Menu will appear on the screen. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.



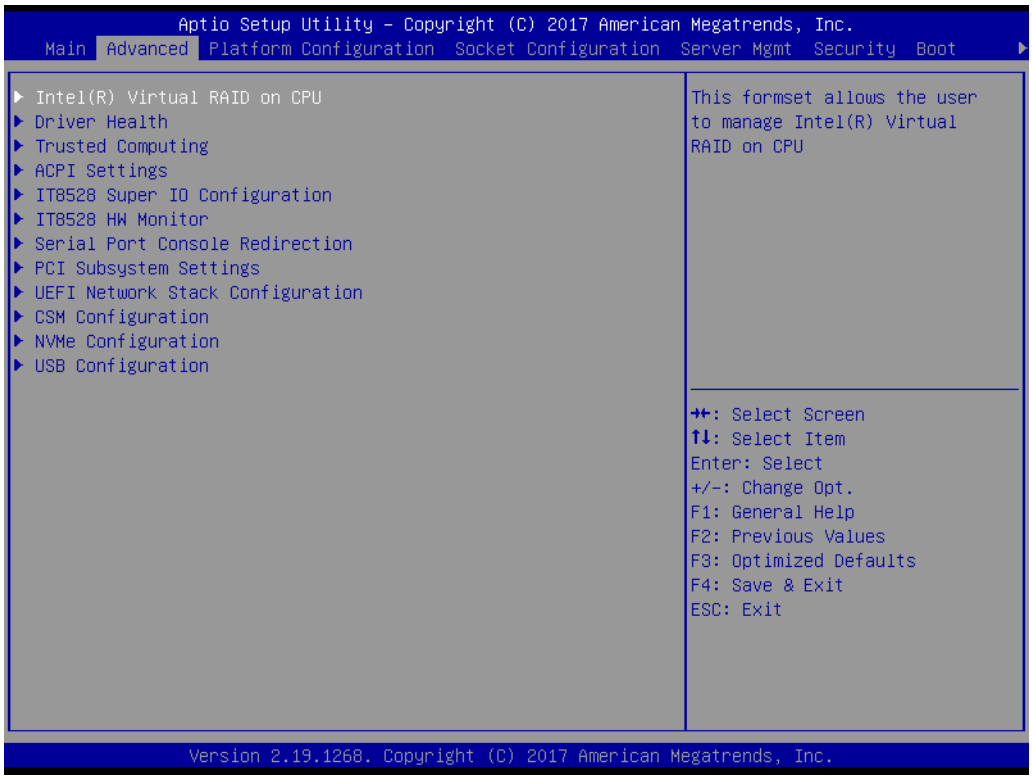
The Main BIOS setup screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured; options in blue can be. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

- **System Date / System Time**

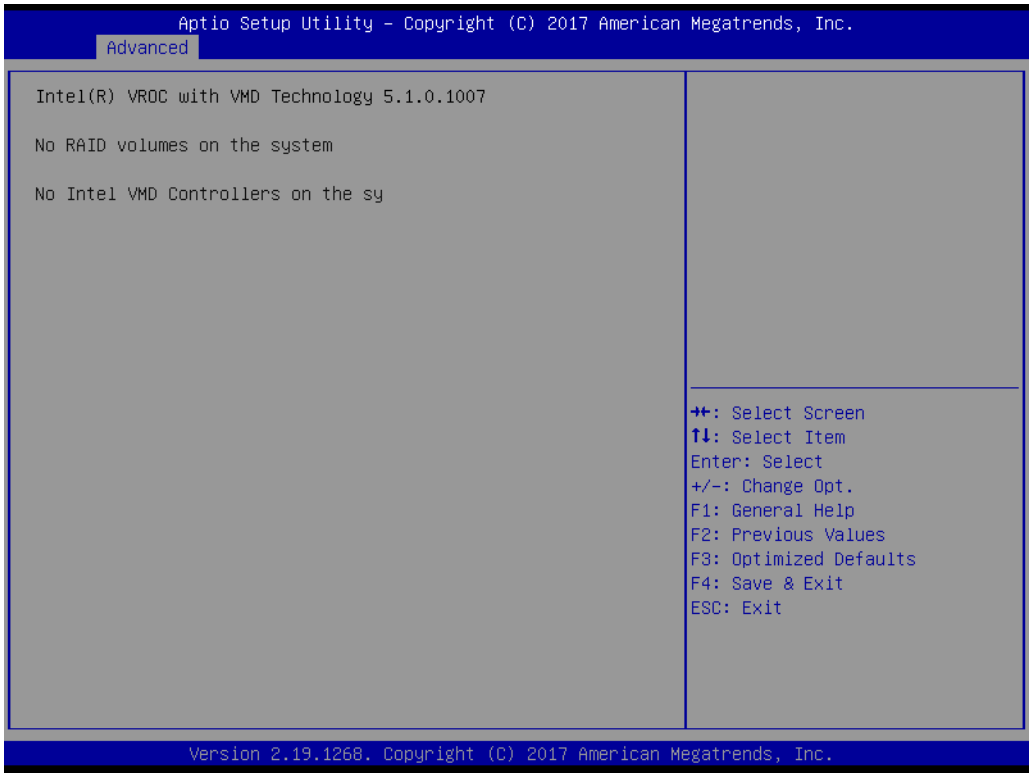
  Use this option to change the system time and date. Highlight System Time or System Date using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in MM/DD/YY format. The time must be entered in HH:MM:SS format.

3.2.2 Advanced BIOS Features Setup

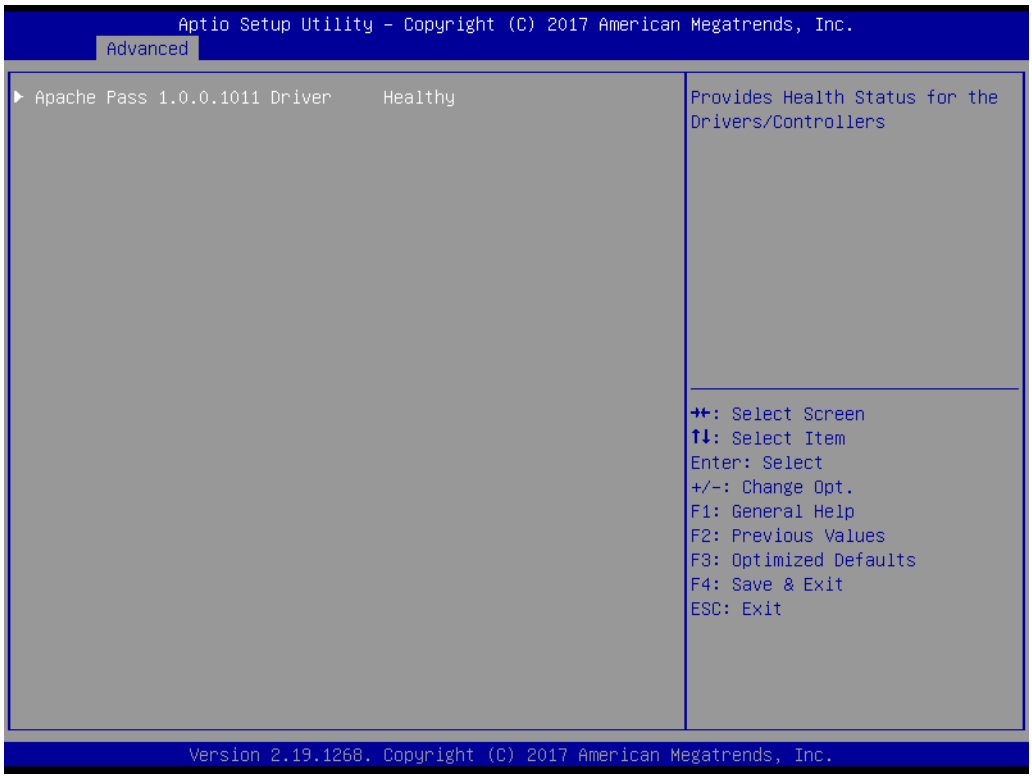
Select the Advanced tab from the ASMB-825 setup screen to enter the Advanced BIOS setup screen. You can select any of the items in the left frame of the screen, such as CPU configuration, to go to the sub menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced BIOS Setup options are described in this section. The Advanced BIOS Setup screens are shown below. The sub menus are described on the following pages.



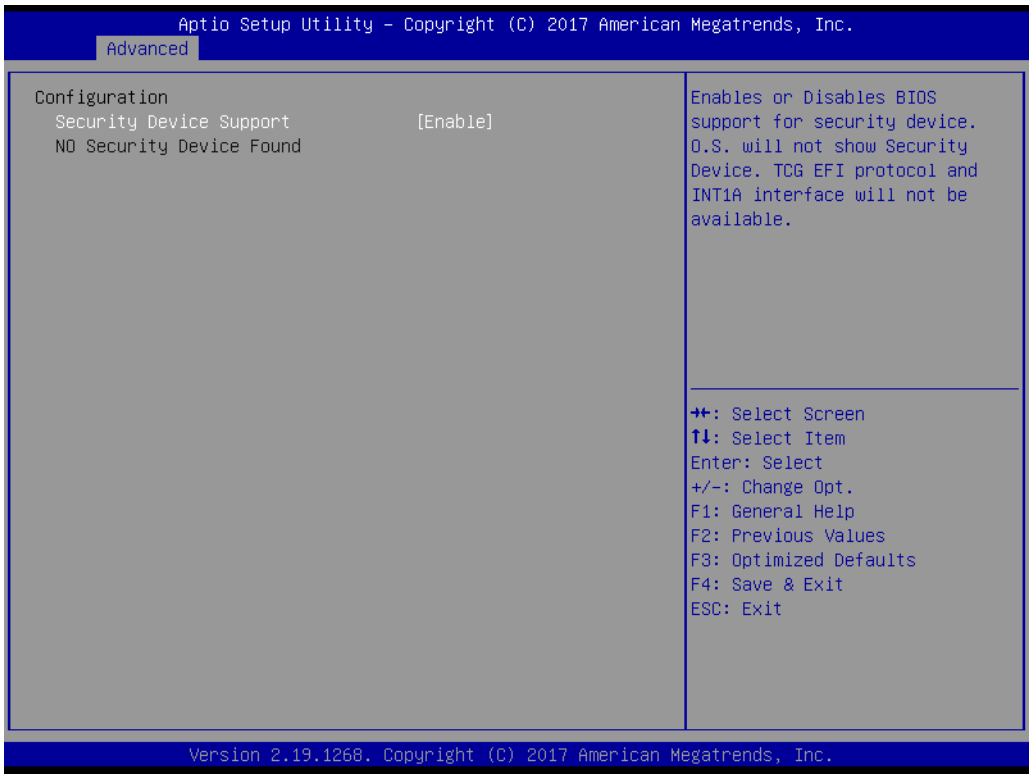
3.2.2.1 Intel Virtual RAID on CPU



3.2.2.2 Driver Health



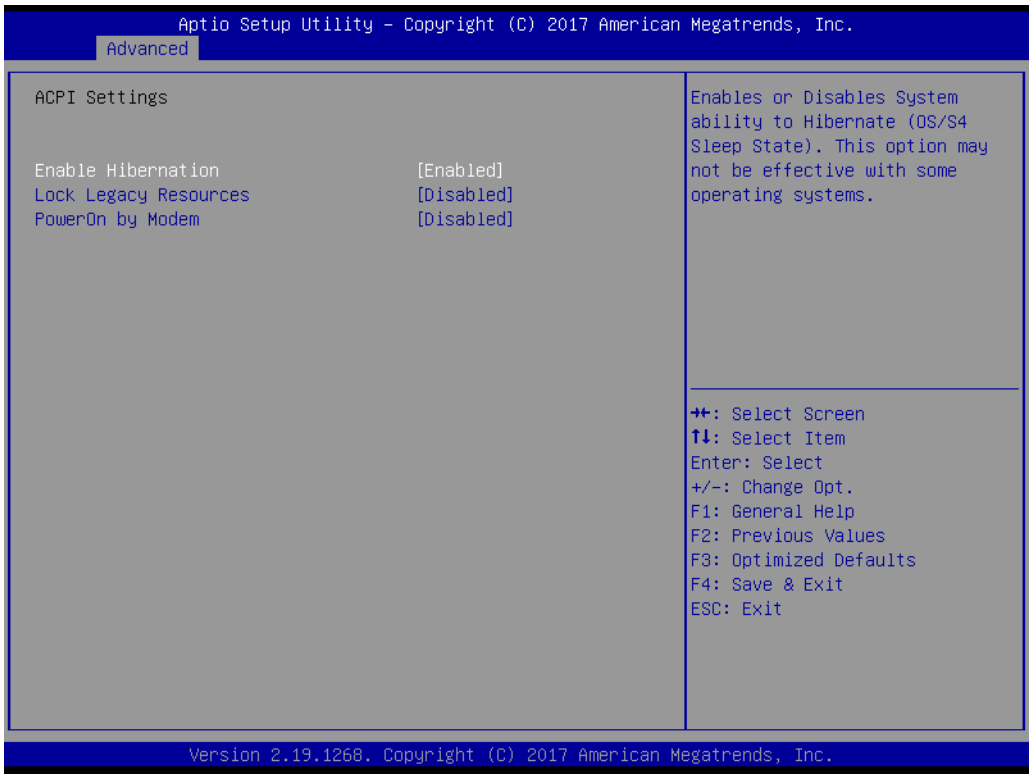
3.2.2.3 Trusted Computing



- **Security Device Support**
  Enables or disables BIOS support for security devices.

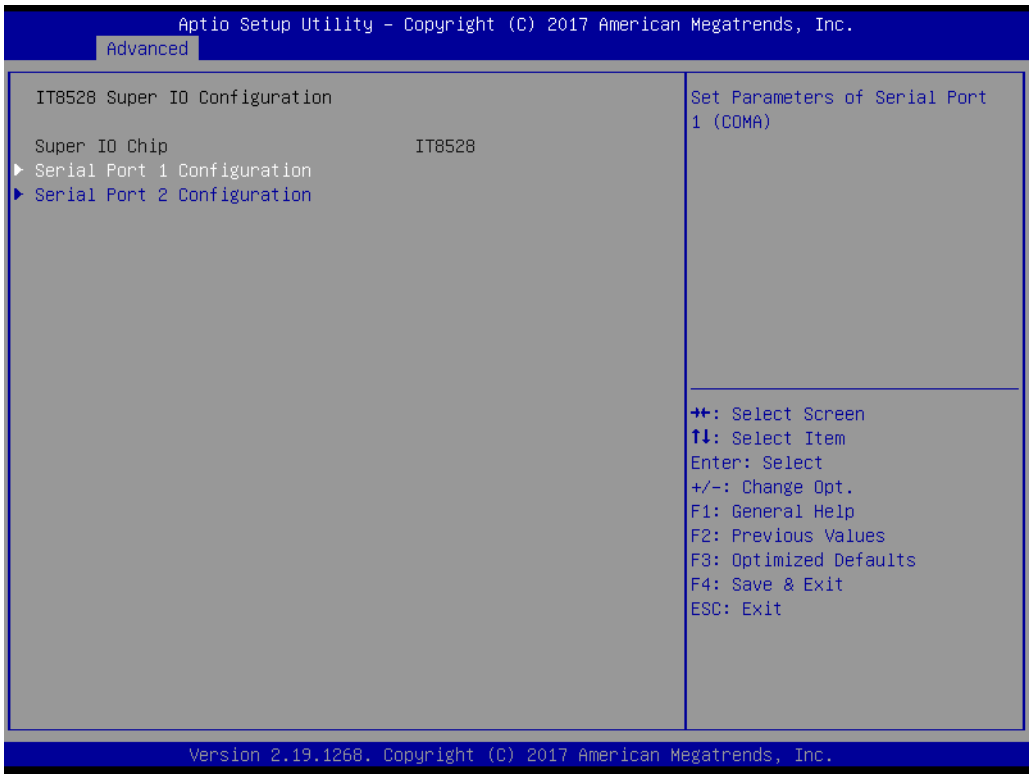
Note! *Purchase Advantech LPC TPM module to enable TPM function. P/N: PCA-TPM-00A1E_B1E.*

3.2.2.4 ACPI Settings

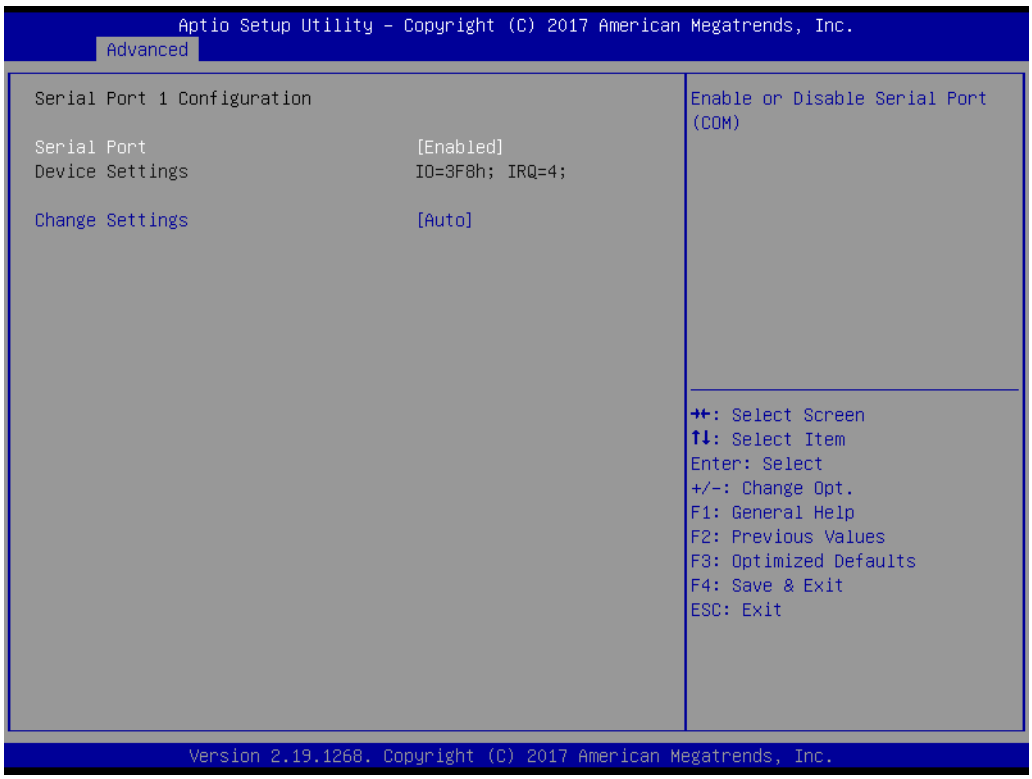


- **Enable Hibernation**
  Enable or Disable hibernation feature.
- **Lock Legacy Resources**
  Enable or disable lock legacy resources feature.
- **PowerOn By Modem**
  Enable or disable power on by modem feature.

3.2.2.5 IT8528 EC Super IO Configuration

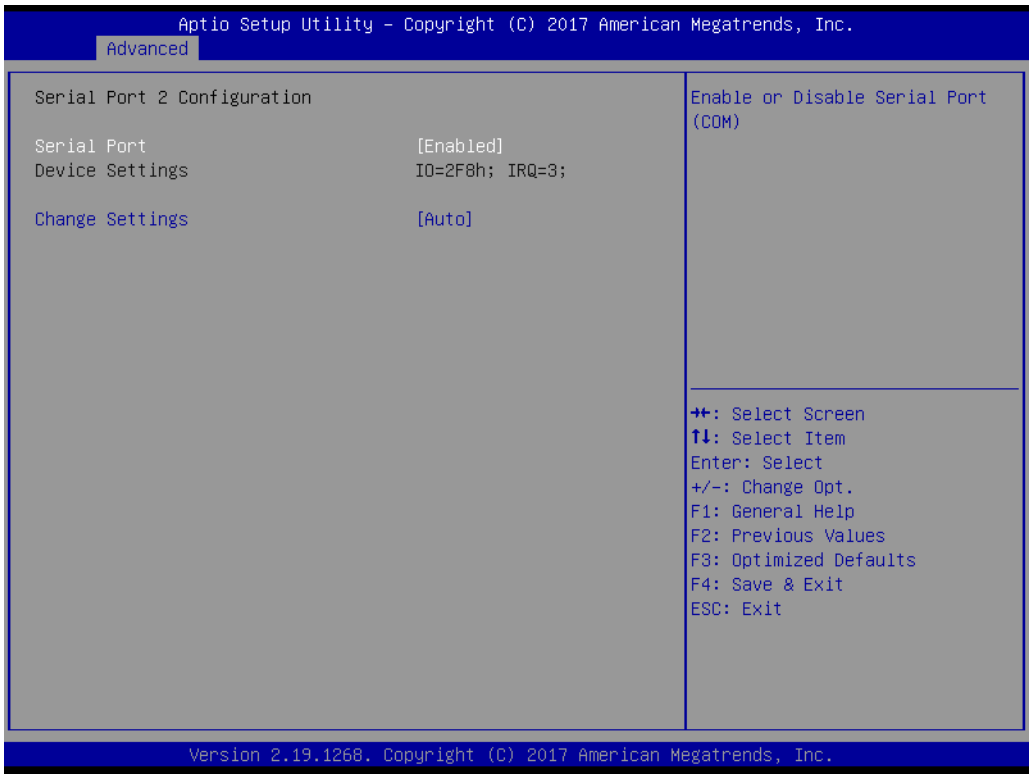


- **Serial Port 1 Configuration**



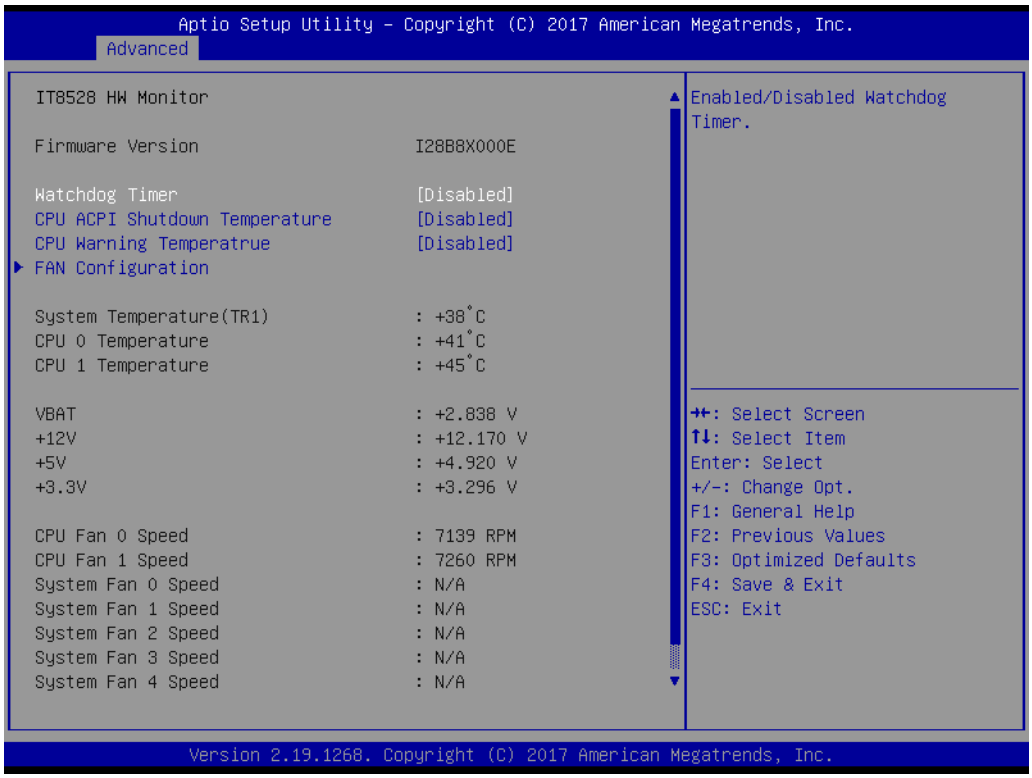
  - **Serial Port**
    Enable or disable serial port 1.
  - **Change Settings**
    To select an optimal setting for serial port 1.

Serial Port 2 Configuration



- **Serial Port**
  Enable or disable serial Port 2.
- **Change Settings**
  To select an optimal setting for serial port 2.

3.2.2.6 IT8528 HW Monitor



- **Watchdog Timer**

  Enable or disable the watchdog timer function.

- **CPU ACPI Shutdown Temperature**

  Enable or disable the ACPI shutdown temperature threshold. When the system reaches the shutdown temperature, it will be automatically shut down by ACPI OS to protect the system from overheating damage.

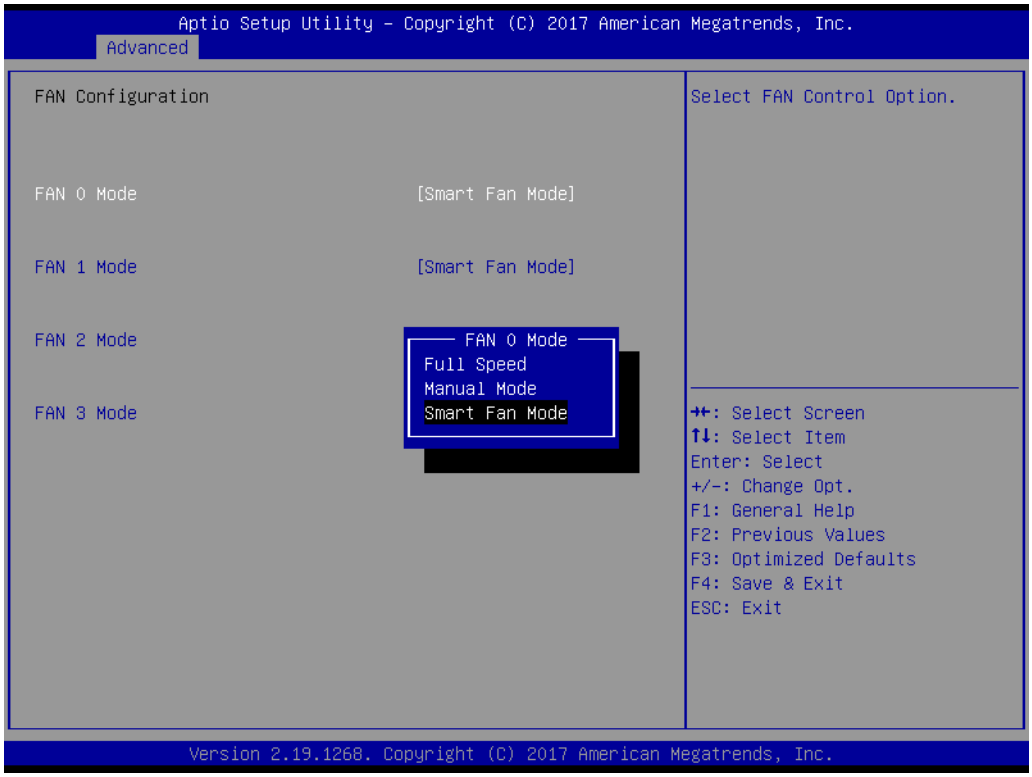
- **CPU Warning Temperature**

  Enable or disable the CPU warning temperature threshold. When the system reaches the warning temperature, the speaker will beep.

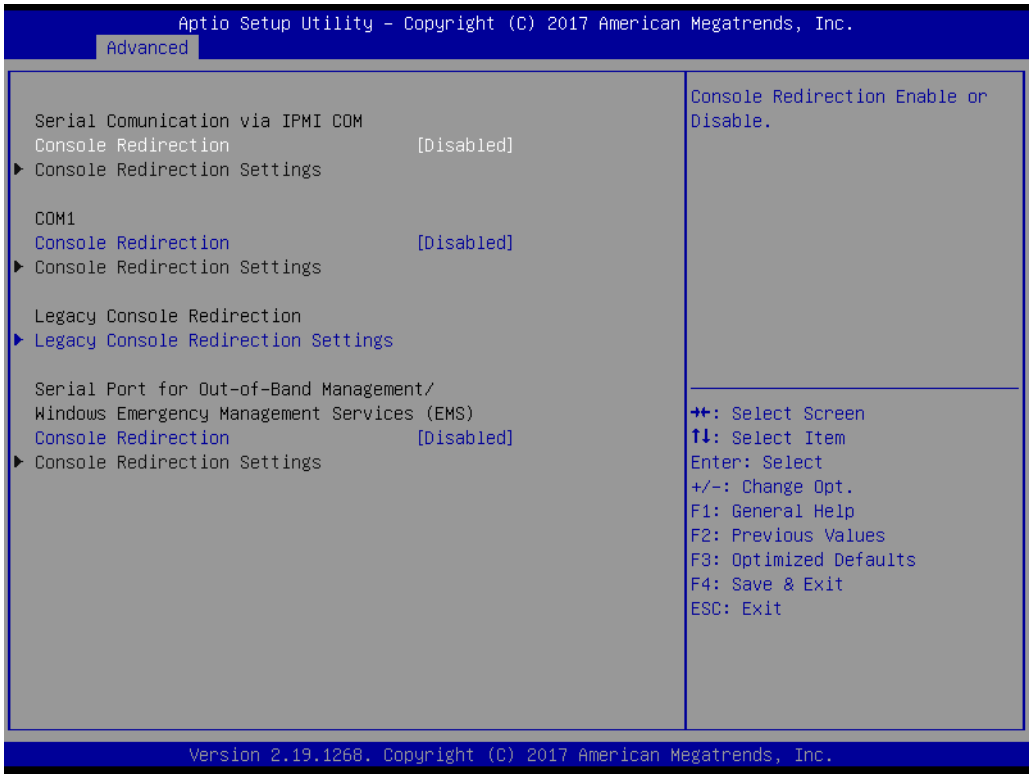
- **Fan Configuration**

  Fan 0 mode controls CPUFAN0 and CPUFAN1, Fan 1 mode controls SYSFAN0 and SYSFAN1, FAN 2 mode controls SYSFAN2 and SYSFAN3; and FAN 3 mode controls SYSFAN4.
  The default of CPU/System FAN is Smart FAN mode and the BIOS will automatically control the FAN speed by CPU temperature.

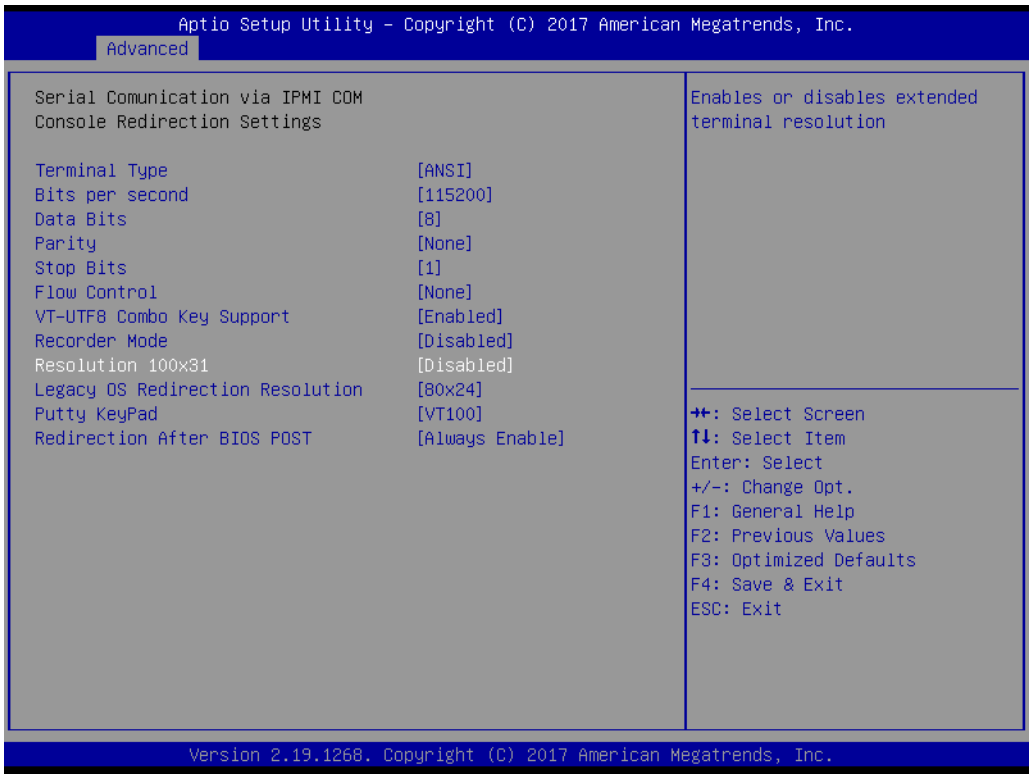
  When set to manual mode, fan duty setting can be changed; the range is from 30%~100%, default setting is 50%.

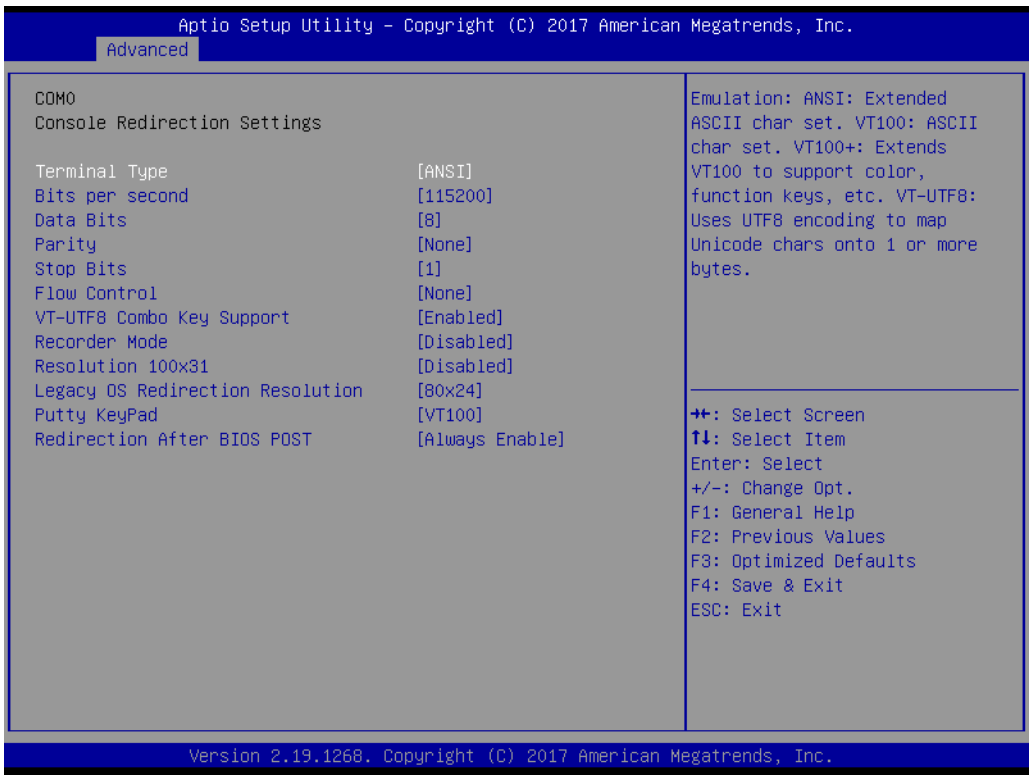


3.2.2.7 Serial Port Console Redirection



COM1 Console Redirection Settings





– **Terminal Type**
  Select a terminal type to be used for console redirection.
  Options available: VT100/VT100+/ANSI /VT-UTF8.
– **Bits Per Second**
  Select the baud rate for console redirection.

Options available: 9600/19200/57600/115200.

- **Data Bits**
- **Parity**

  A parity bit can be sent with the data bits to detect some transmission errors.

  Even: parity bit is 0 if the number of 1's in the data bits is even.

  Odd: parity bit is 0 if number of 1's the data bits is odd.

  Mark: parity bit is always 1. Space: Parity bit is always 0.

  Mark and Space Parity do not allow for error detection.

  Options available: None/Even/Odd/Mark/Space.

- **Stop Bits**

  Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.

  Options available: 1/2.

- **Flow Control**

  Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.

  Options available: None/Hardware RTS/CTS.

- **VT-UTF8 Combo Key Support**

  Enable VT-UTF8 combination key support for ANSI/VT100 terminals.

- **Recorder Mode**

  When this mode enabled, only text will be send. This is to capture Terminal data.

  Options available: Enabled/Disabled.

- **Resolution 100x31**

  Enables or disables extended terminal resolution.

- **Legacy OS Redirection Resolution**

  On Legacy OS, the number of Rows and Columns supporting redirection.

  Options available: 80x24/80X25.

- **Putty Keypad**

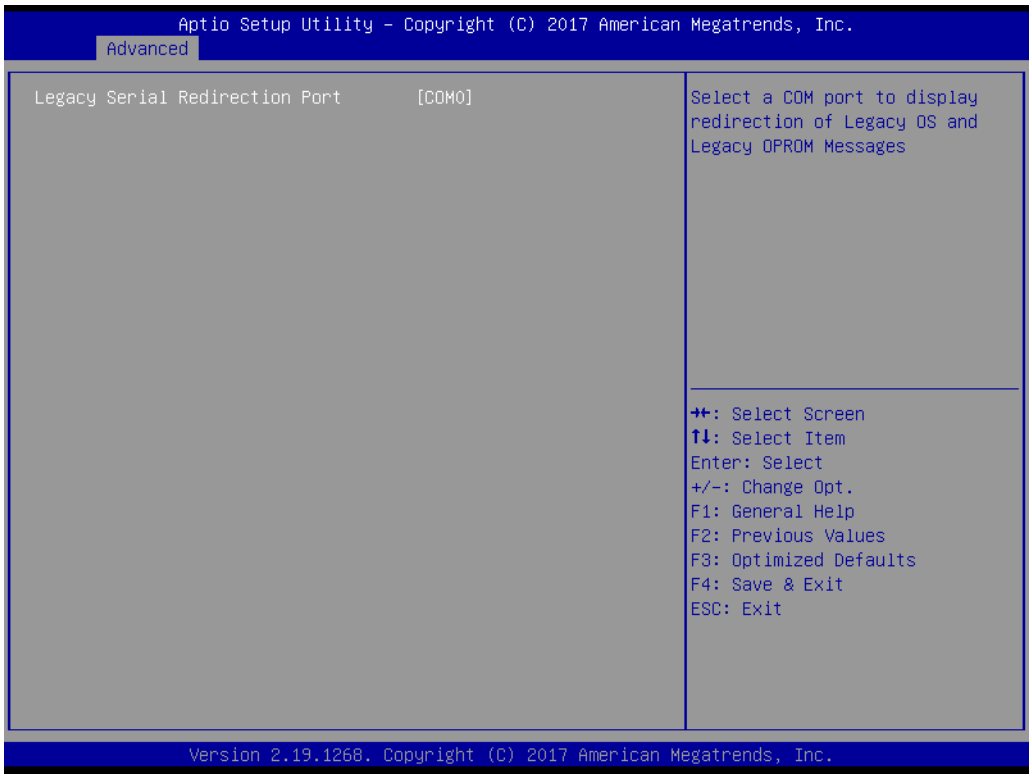
  Select function key and keypad on putty.

- **Redirection After BIOS Post**

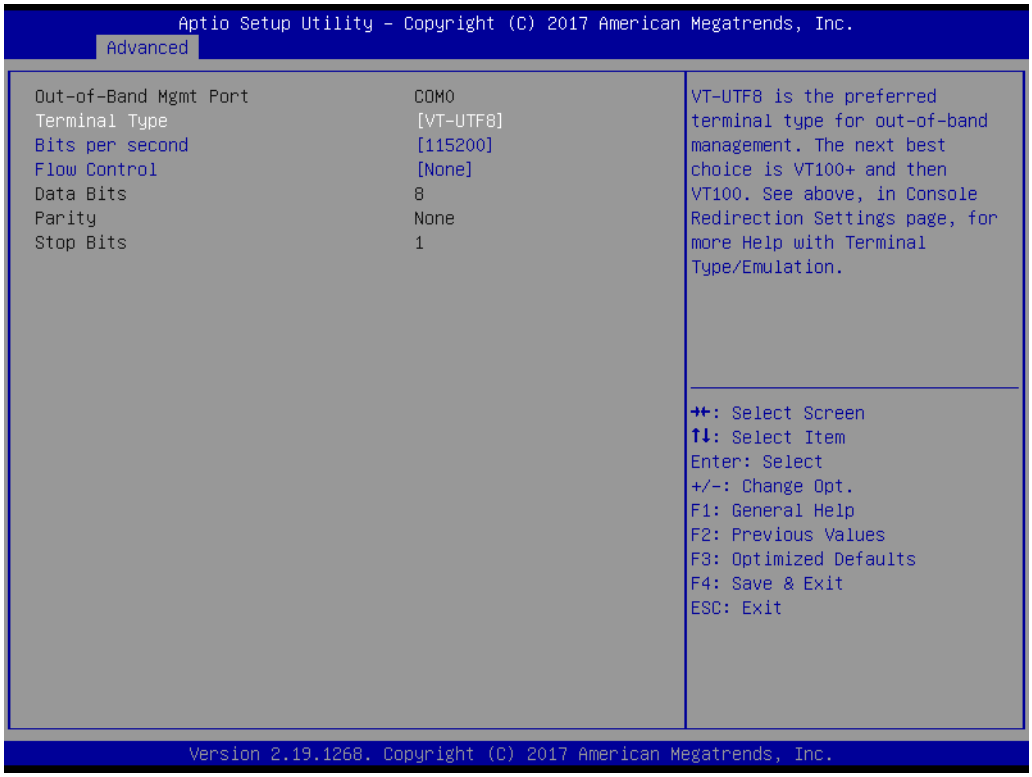
  When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to Always Enable.

Legacy Console Redirection Settings

Select a COM port to display redirection of Legacy OS and Legacy OPROM Messages.



Console Redirection Settings

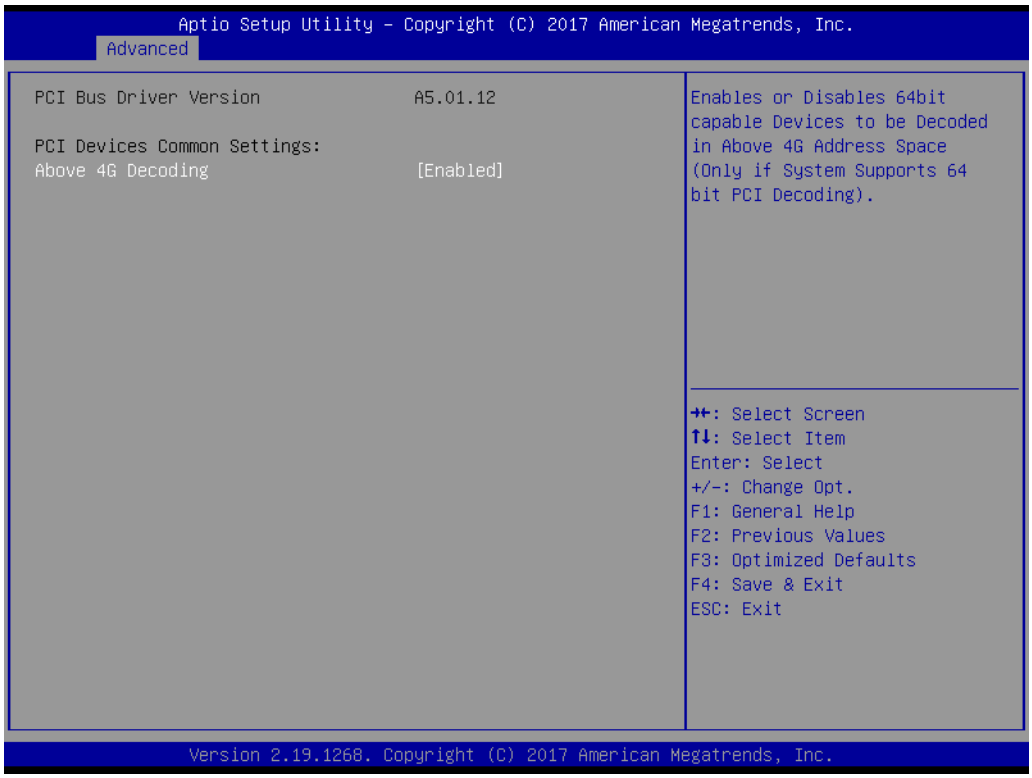


– Out-of-Band Mgmt Port

Selects the COM port user would like to set for having console redirection feature.

- **Terminal Type**
  Set as "VT100", "VT100+", "VT-UTF8", or "ANSI". "VT-UTF8" is the default setting.
- **Bits Per Second**
  To select serial port transmission. Speed must be matched on the other side. It can be set as "9600", "19200", "57600", or "115200". "115200" is the default setting.
- **Flow Control**
  Flow control can prevent data loss from buffer overflow. It can be set as "None",
  "Hardware RTS/CTS", or "Software Xon/Xoff". "None" is the default setting.
- **Data Bits**
- **Parity**
- **Stop Bits**

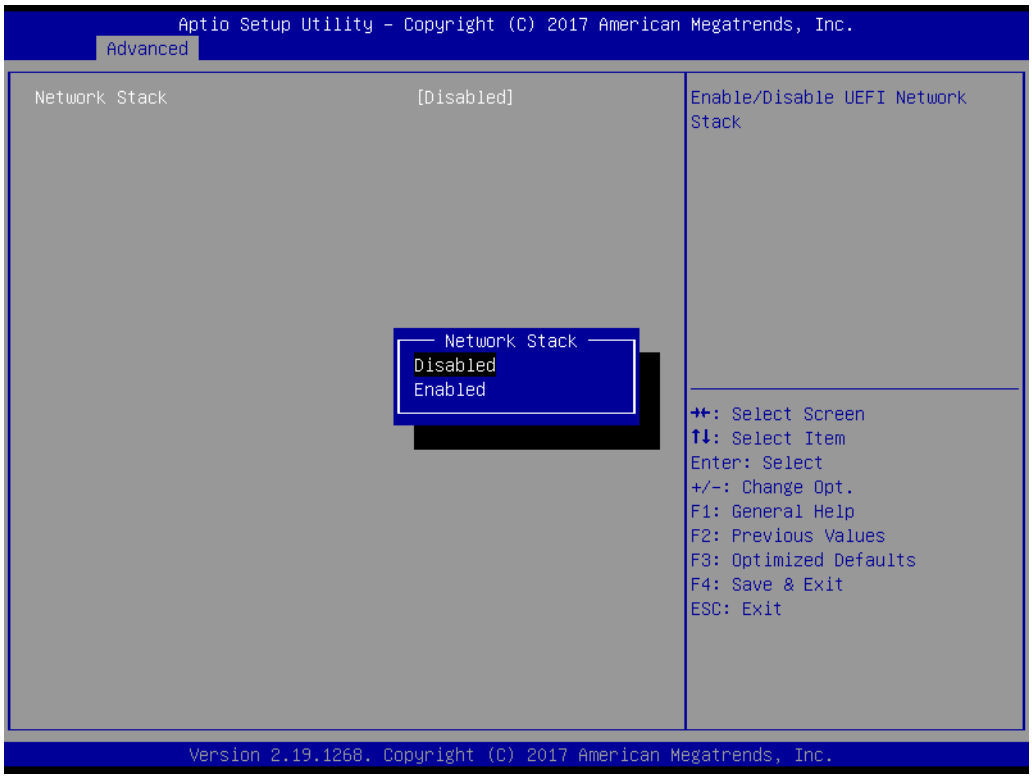
3.2.2.8 PCI Subsystem Settings



- **Above 4G Decoding**
  Enable or disable 64-bit capable devices to be decoded in above 4G address space (only if system supports 64-bit PCI decoding).

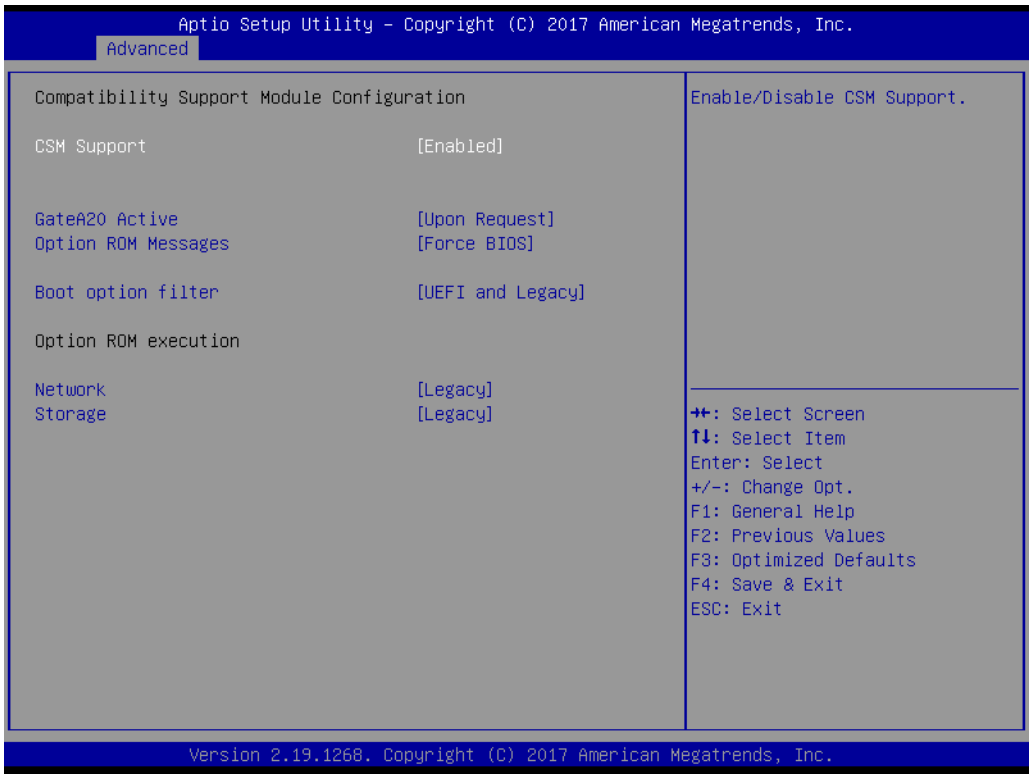
Note! *Some graphics or GPU cards need to enable 4G decoding.*

3.2.2.9 UEFI Network Stack Configuration

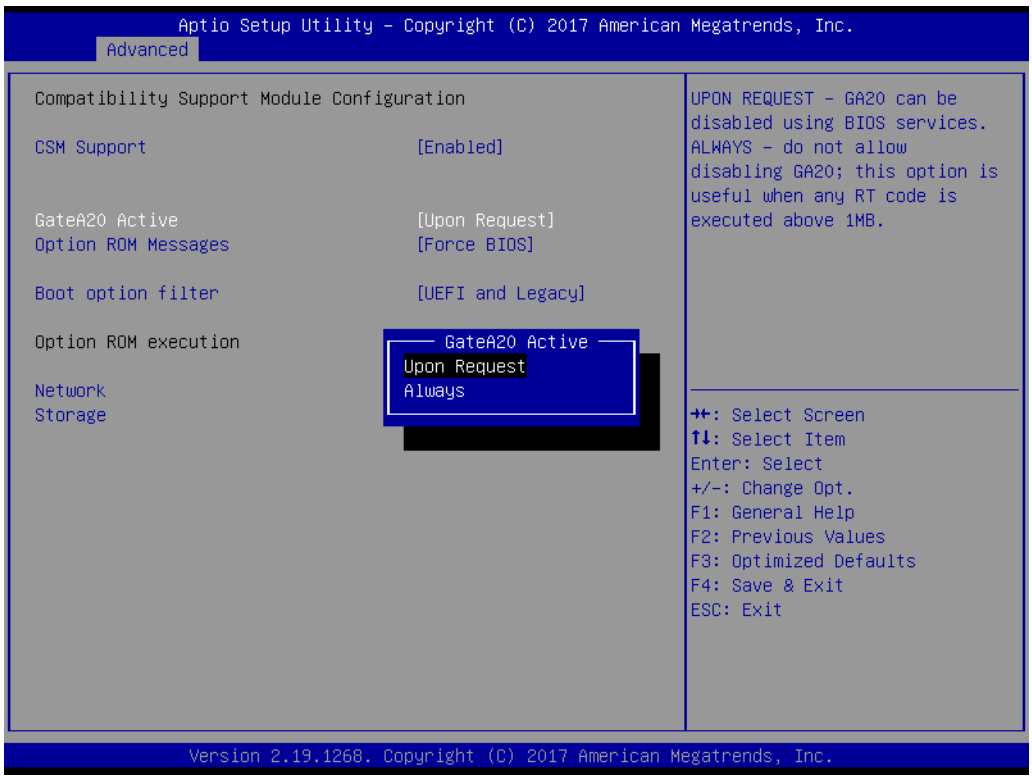


Enable or disable UEFI network stack function.

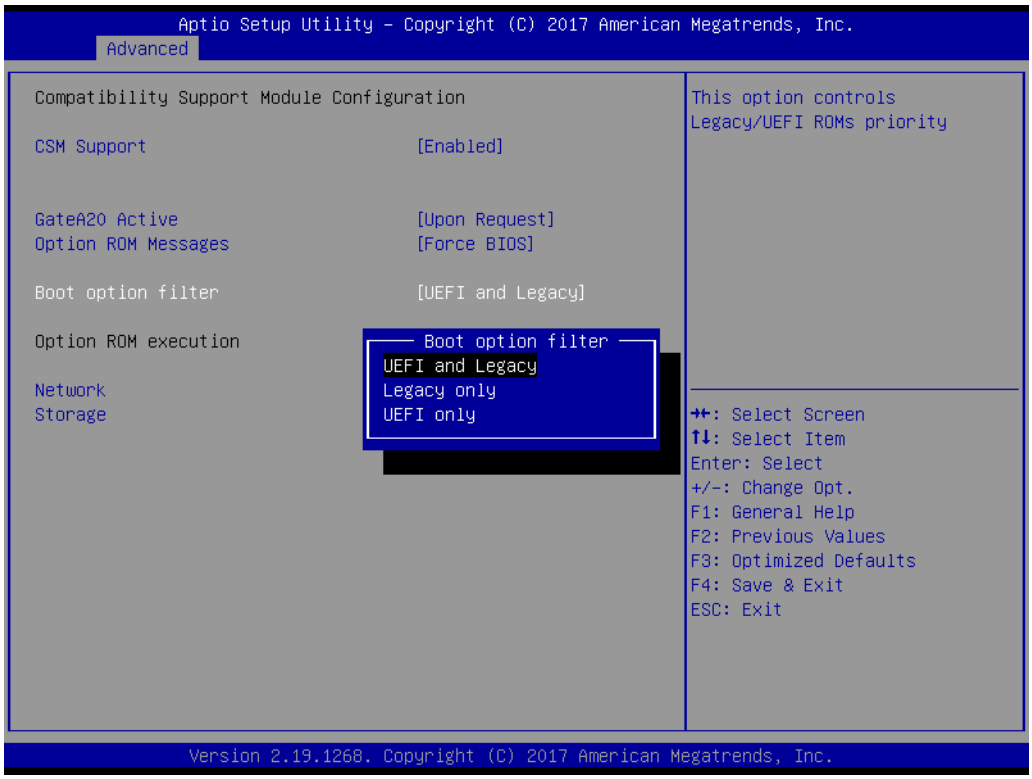
3.2.2.10 CSM Configuration



- **CSM Support**
  Enable or disable UEFI CSM (Compatibility Support Module) to support a legacy PC boot process.
- **GateA20 Active**
  This items is useful when RT code is executed above 1MB. When it's set as "Upon Request", GA20 can be disabled using BIOS services. When it's set as "Always", it does not allow disabling GA20.

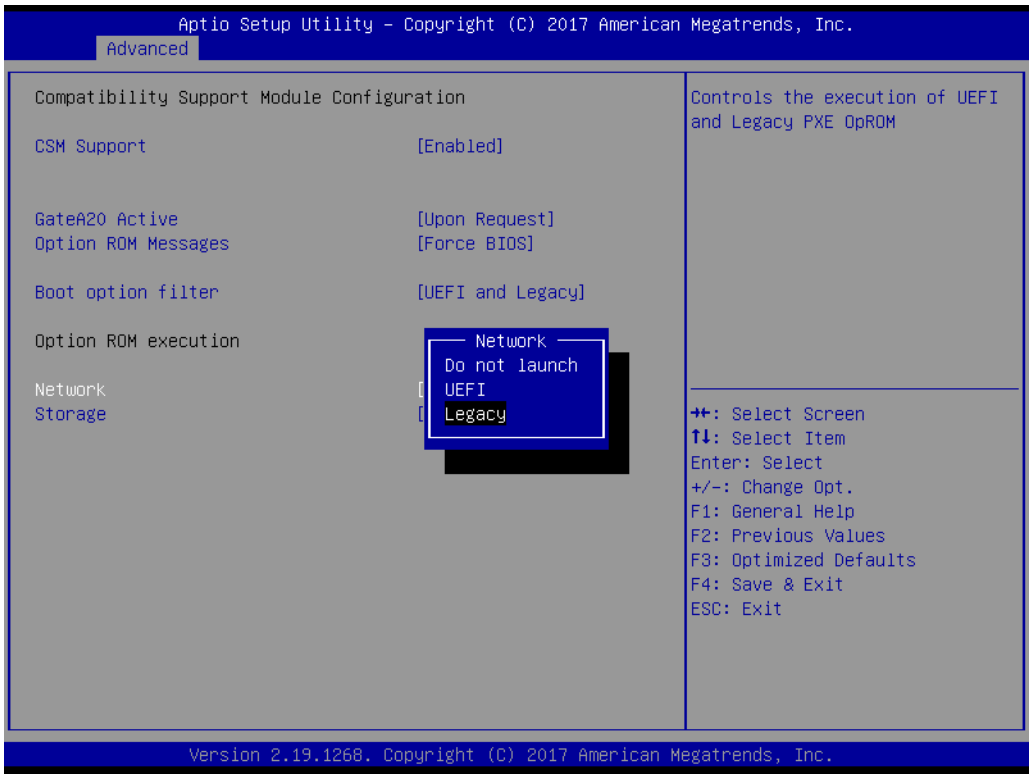


- **Option ROM Messages**
  To "Force BIOS or keep current" to set the display mode for Option ROM.
- **Boot option filter**
  Change UEFI/legacy ROM priority for boot option.



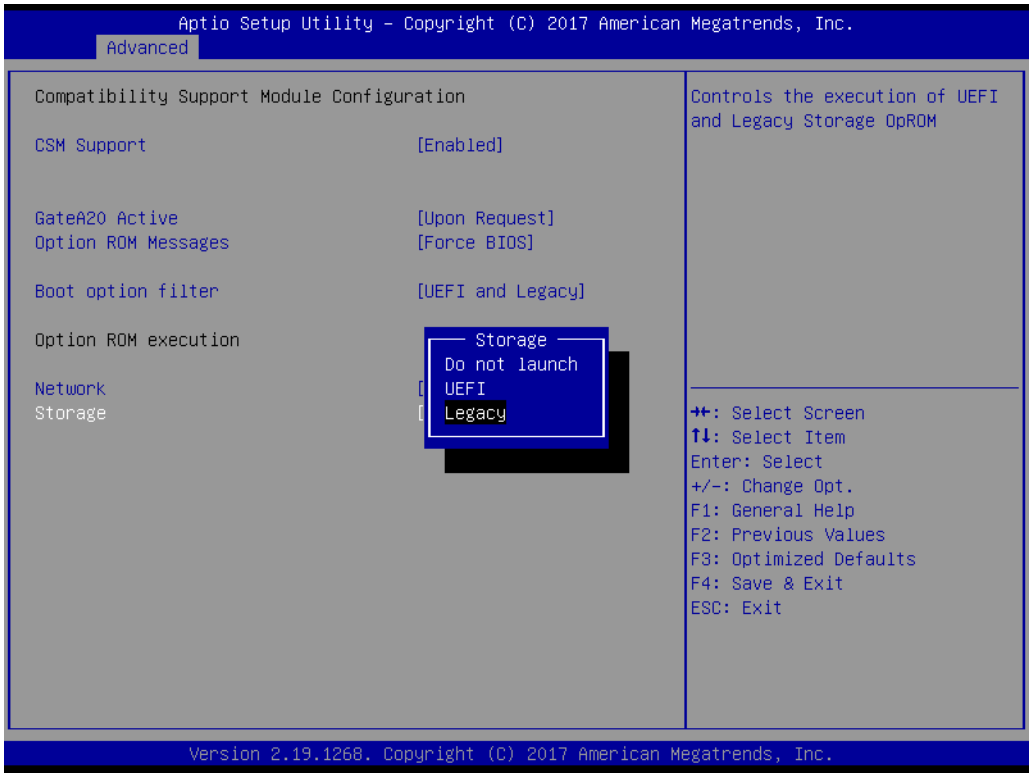
- **Network**

  Control the execution of UEFI and legacy PXE OpROM.



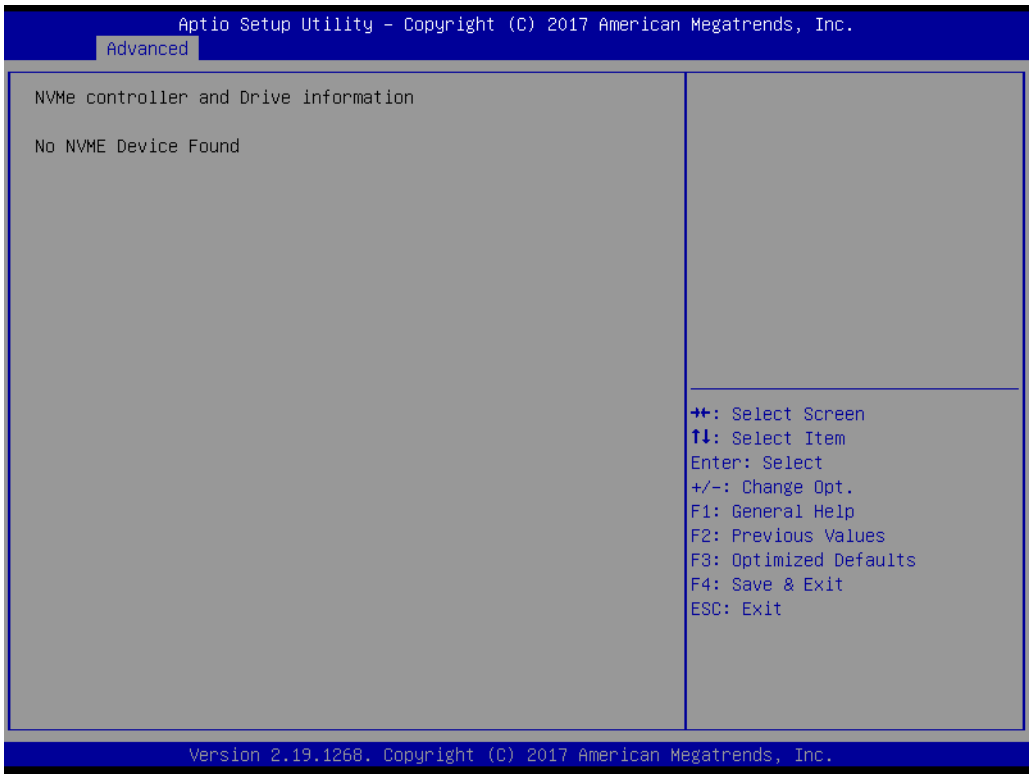
- **Storage**

  Control the execution of UEFI and legacy storage OpROM.



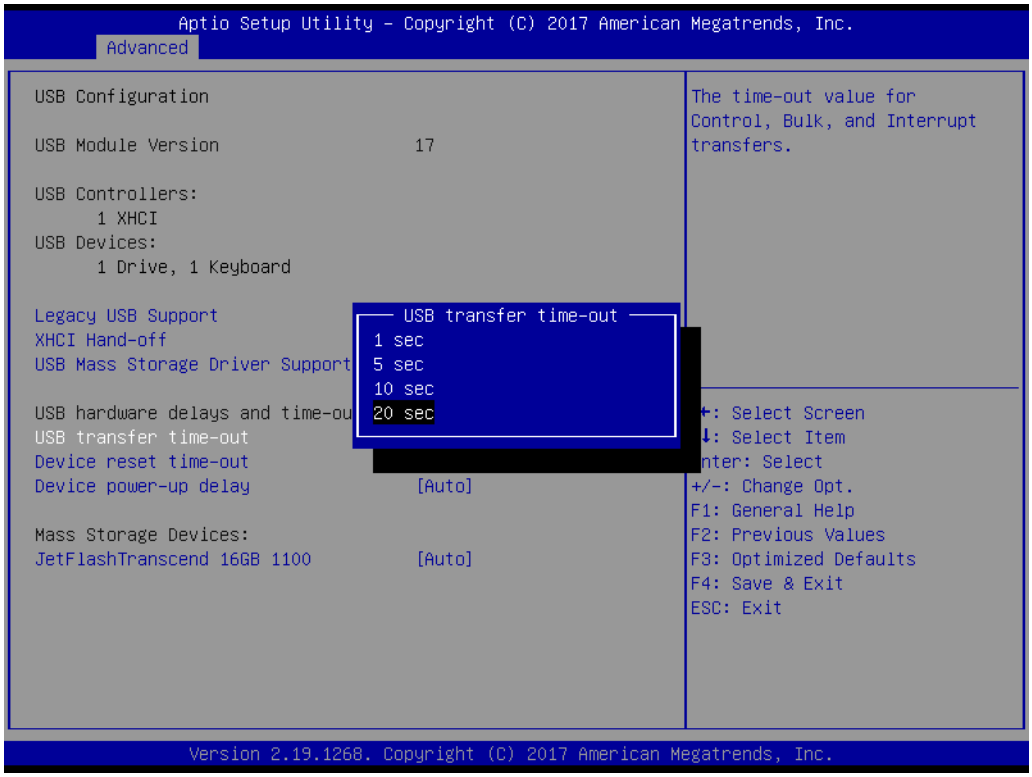
3.2.2.11 NVMe Configuration

Set NVMe device options.



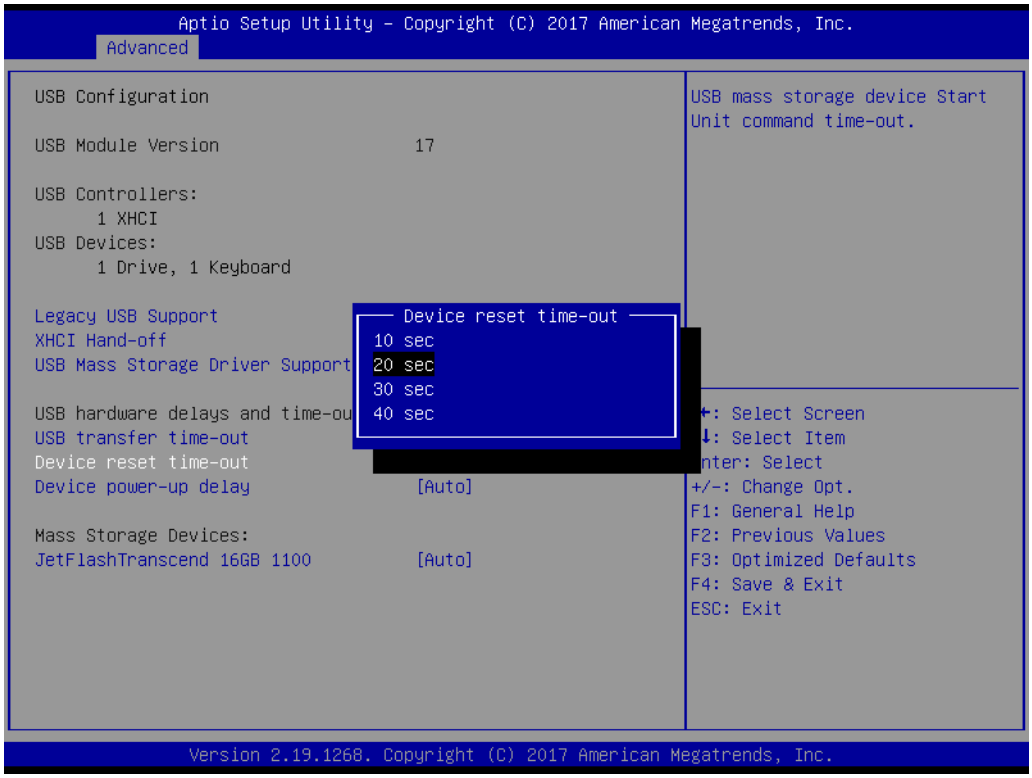
3.2.2.12 USB Configuration

- **Legacy USB Support**
  This is for supporting USB device under a legacy OS such as DOS. When choosing "Auto", the system will automatically detect if any USB device is plugged into the computer and enable USB legacy mode when a USB device is plugged, or disable USB legacy mode when no USB device is attached.
- **XHCI Hand-off**
  This is a workaround for OS without XHCI hand-off support.
  The XHCI ownership change should be claimed by XHCI driver.
- **USB Mass Storage Driver Support**
  Enable or disable USB mass storage driver support.
- **USB Transfer Time-out**
  Selects the USB transfer time-out value. [1,5,10,20sec]



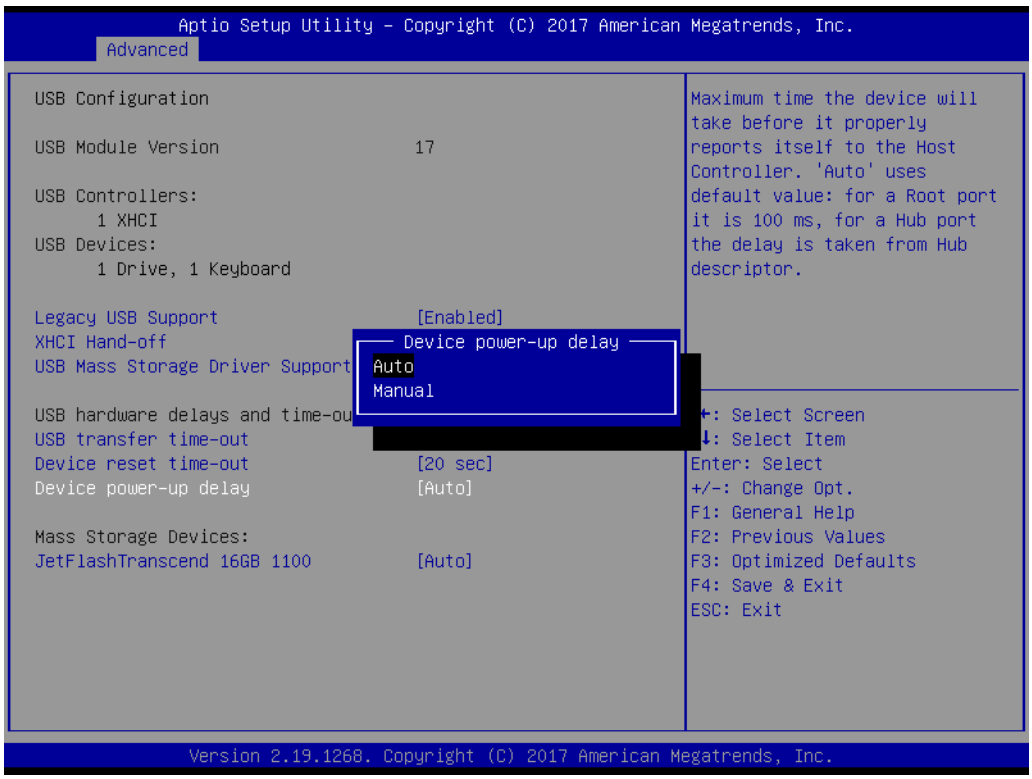
- **Device Reset Time-out**

  Selects the USB device reset time-out value. [10,20,30,40 sec]

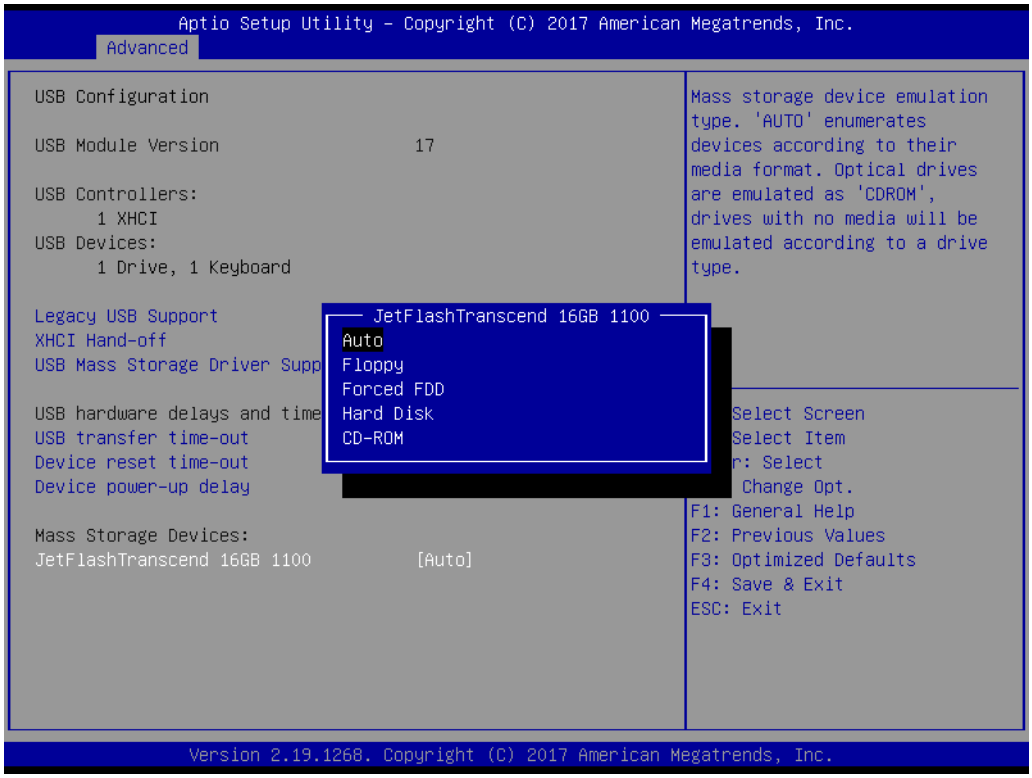


- **Device Power-up Delay**

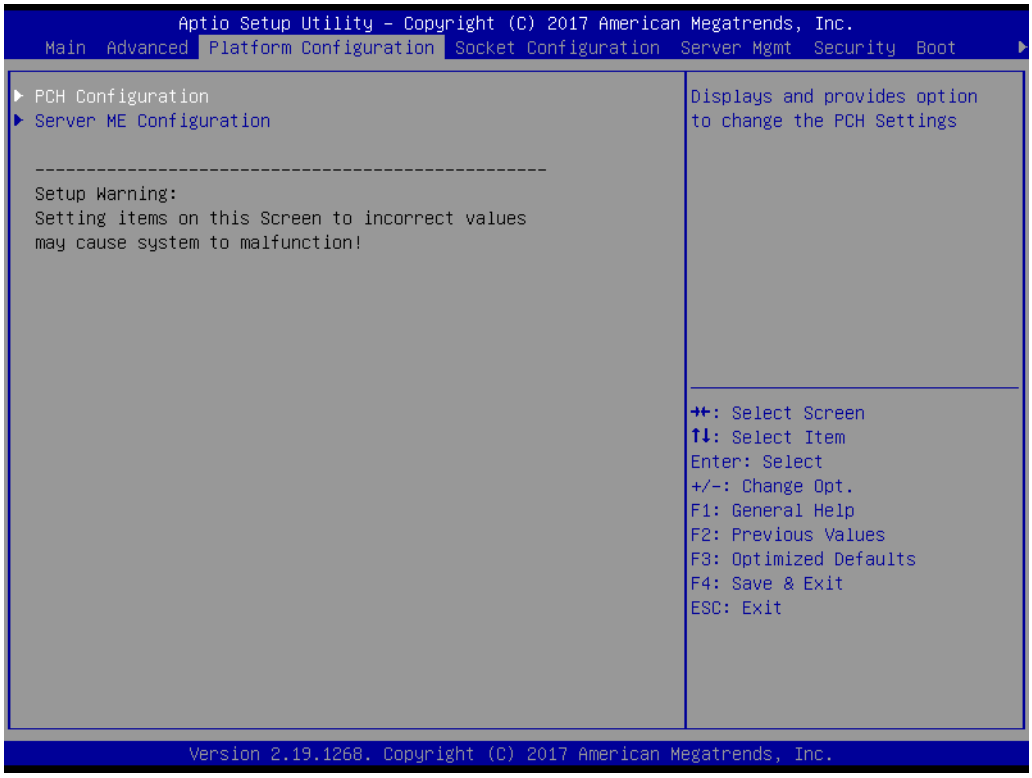
  This item appears only when Device power-up delay item is set to [manual].



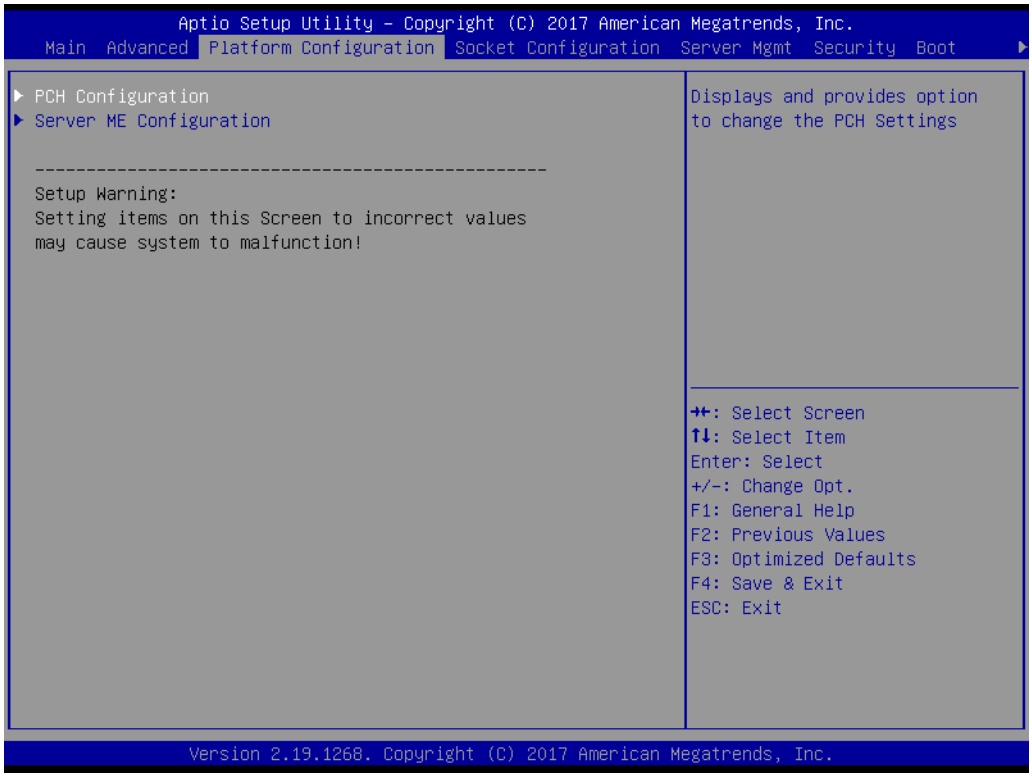
- **Mass Storage Devices**
  Default is "Auto" to enumerate mass storage devices according to media format.

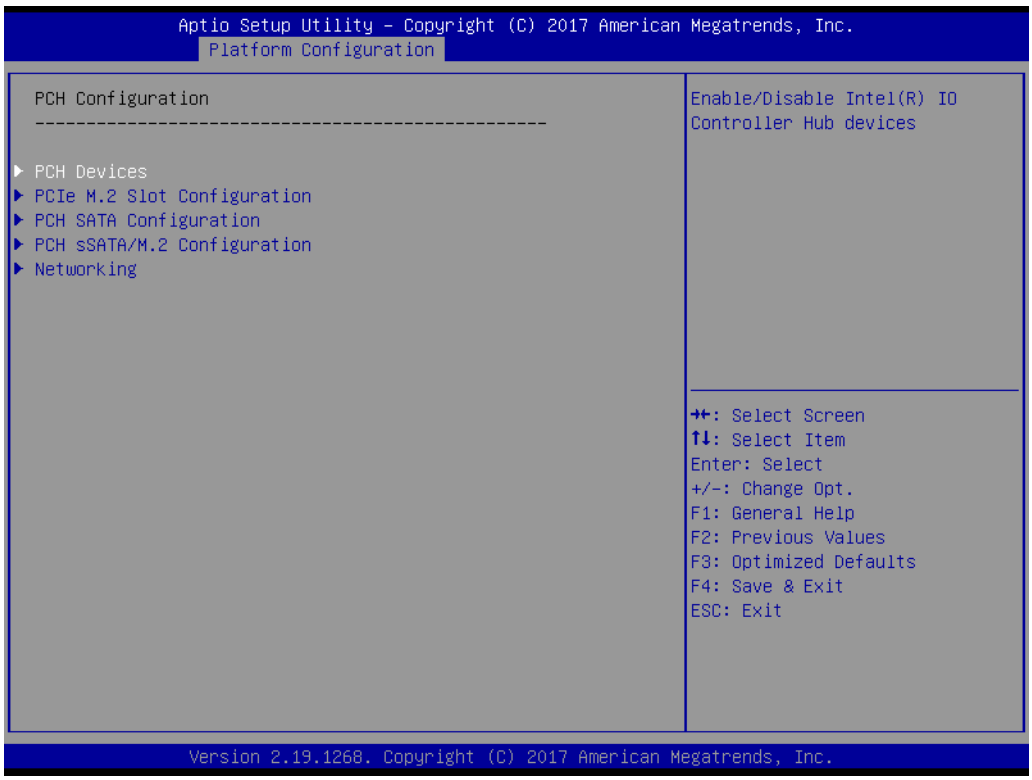


3.2.3 Platform Configuration



3.2.3.1 PCH Configuration



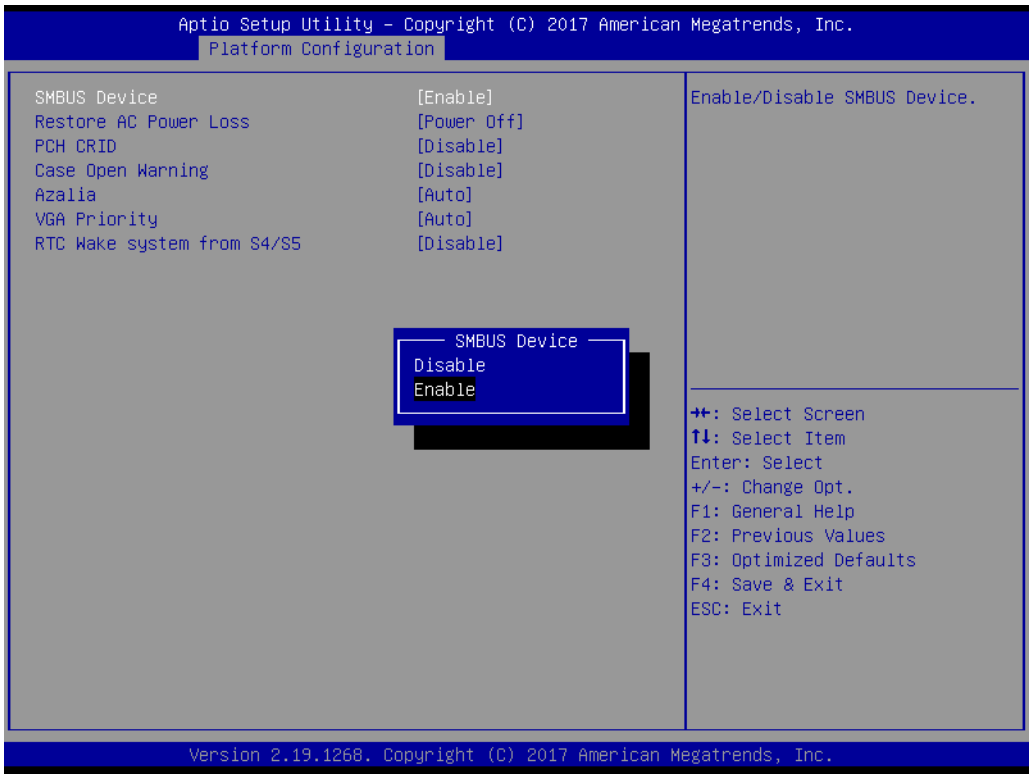


- **PCH Devices**

  This item is to set up IO controller Hub devices.

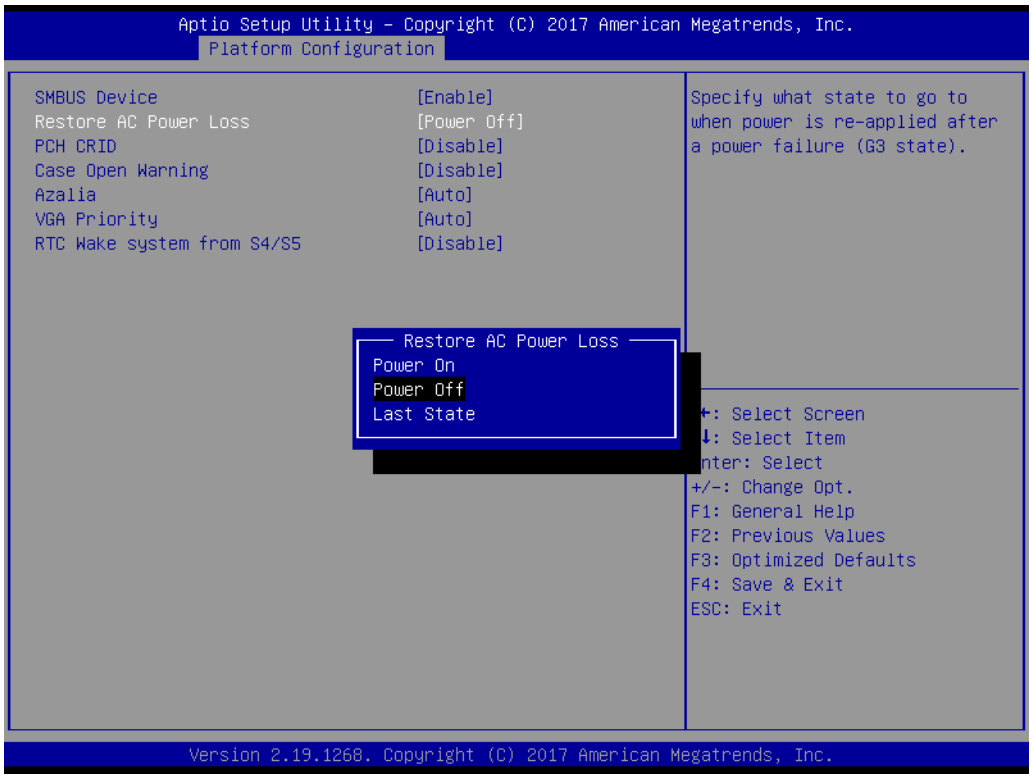
  – **SMBus controller**

  Enable or disable SMBus controller.



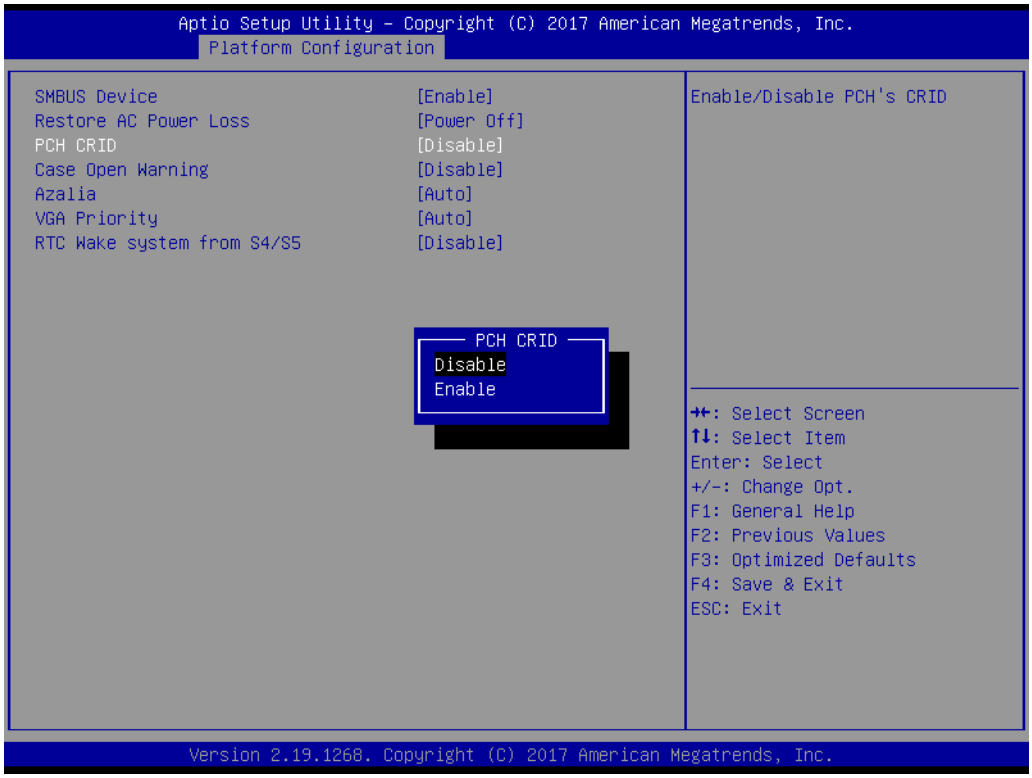
– **Restore AC Power Loss**

Specify what state to go to when power is re-applied after a power failure (G3 state).



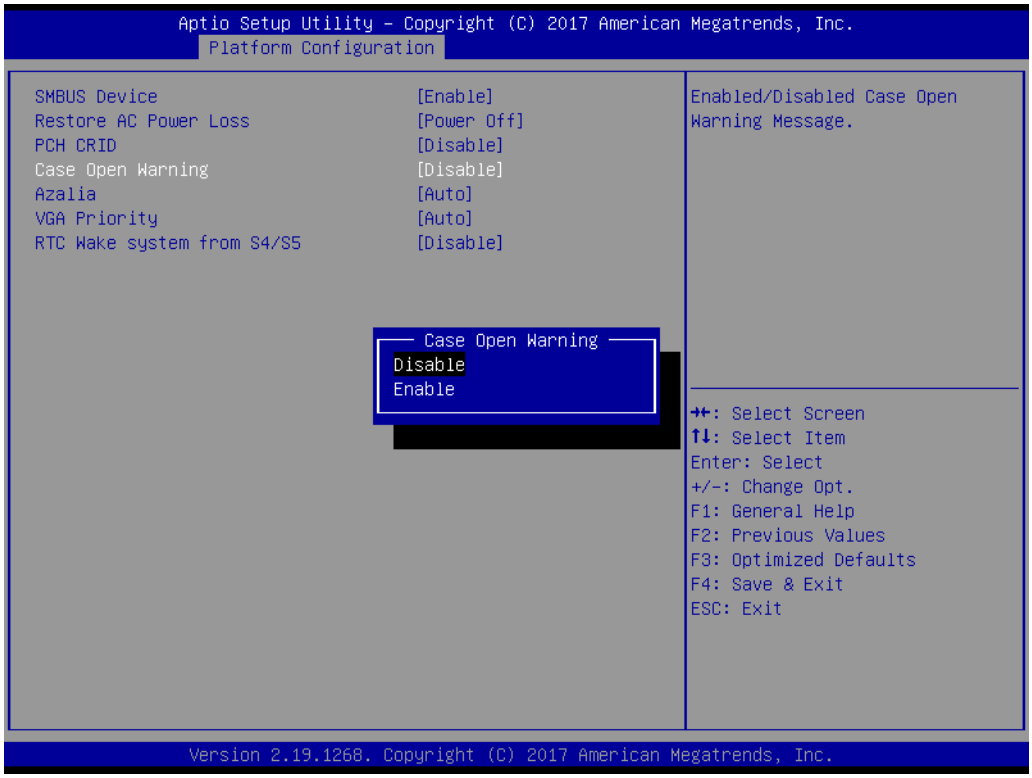
– **PCH CRID**

Enable or disable PCH compatibility revision ID (CRID) functionality.



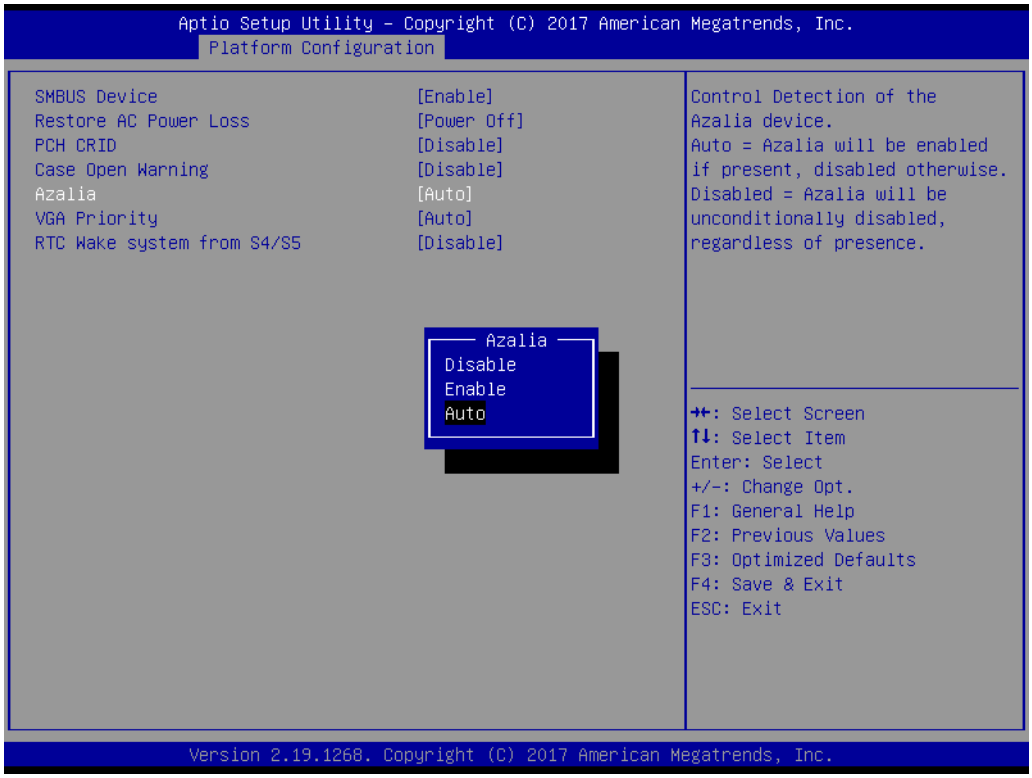
– **Case Open Warning**

Enable or disable the chassis intrusion monitoring function. When enabled and the case is opened, the warning message will show in POST screen.

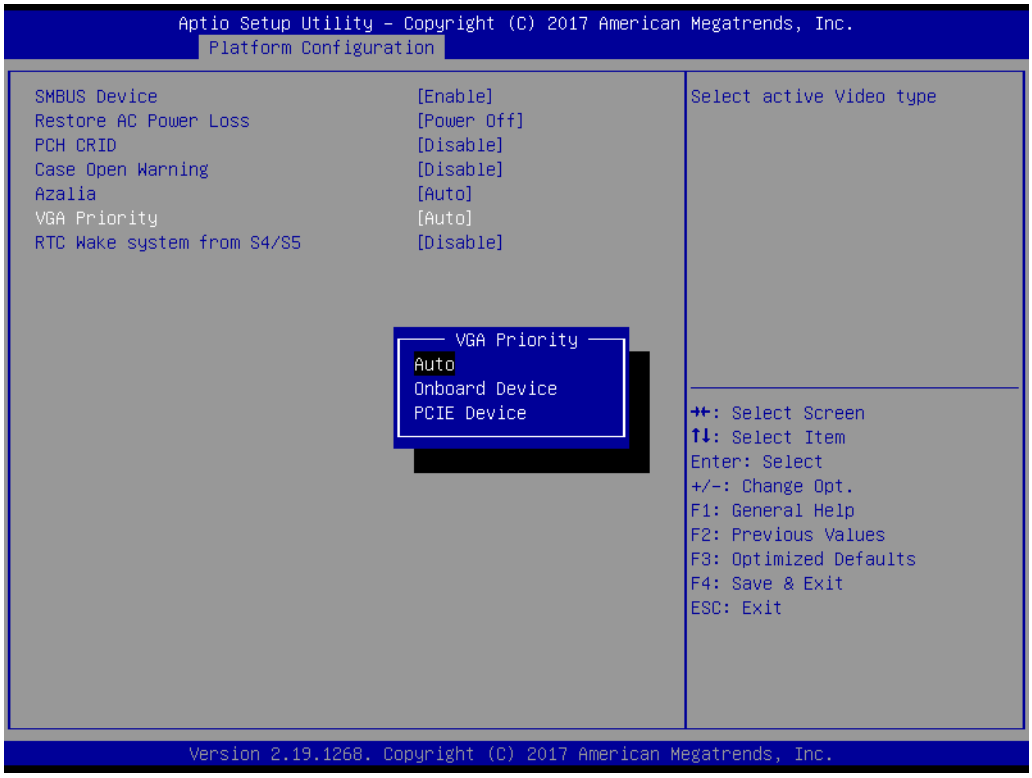


– **Azalia**

Enable or disable Azalia device.

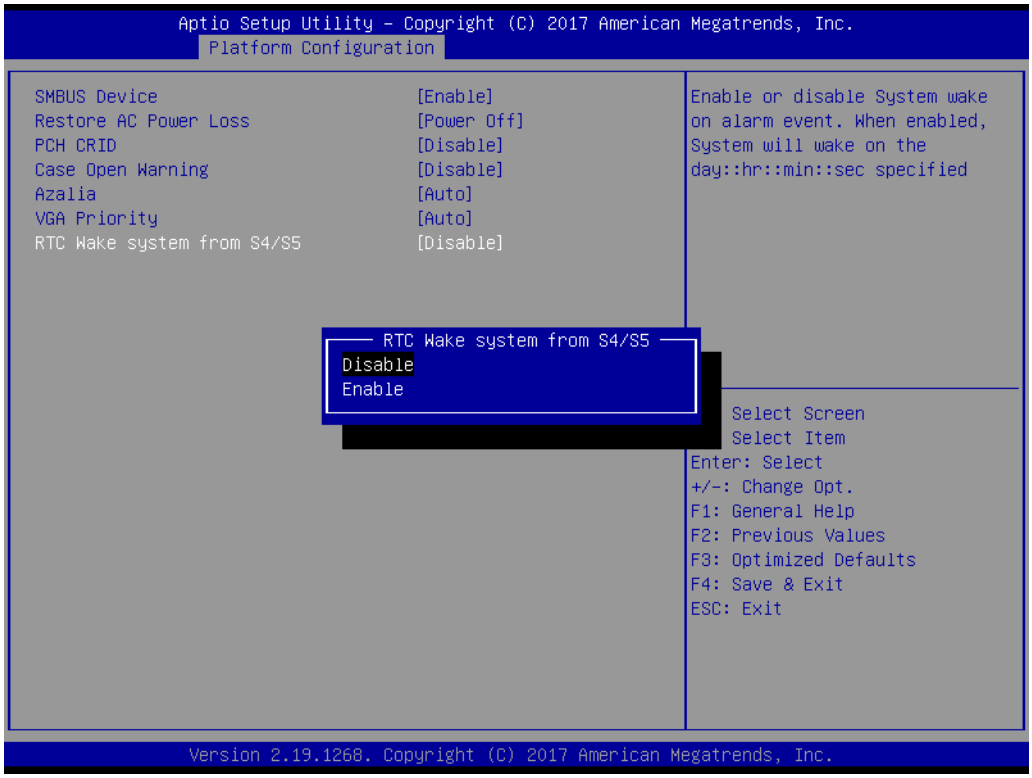


– **VGA Priority**
Determines priority between onboard and 1st off-board video device found.



– **RTC Wake system from S4/S5**

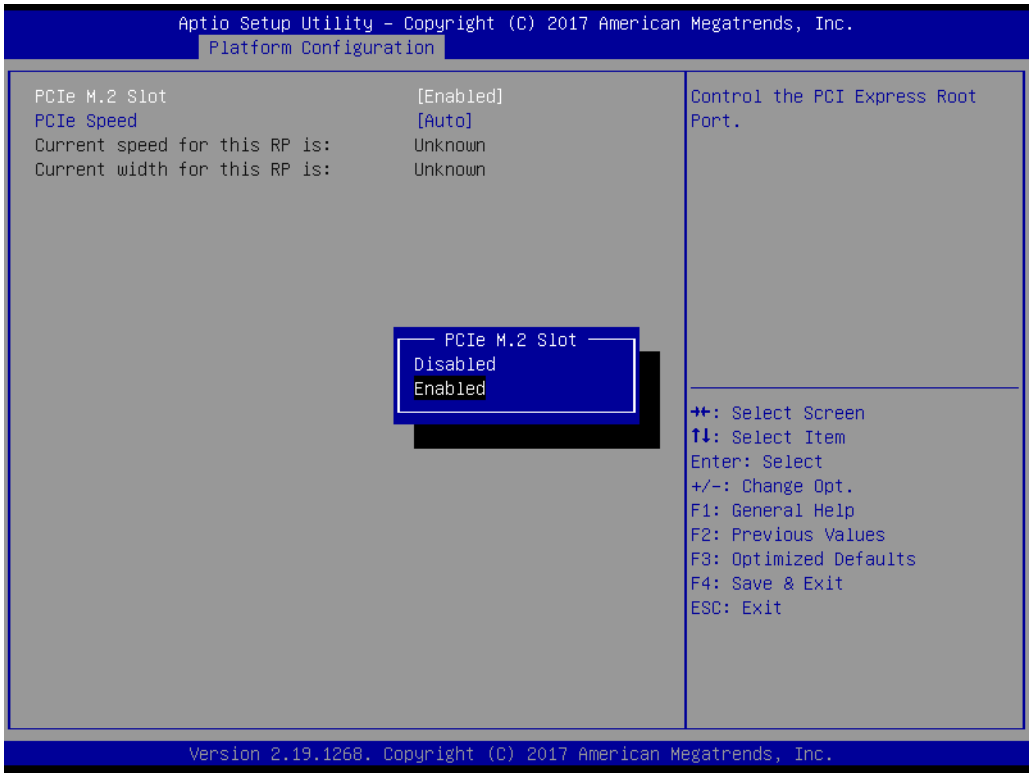
Enable or disable system wake on alarm event.



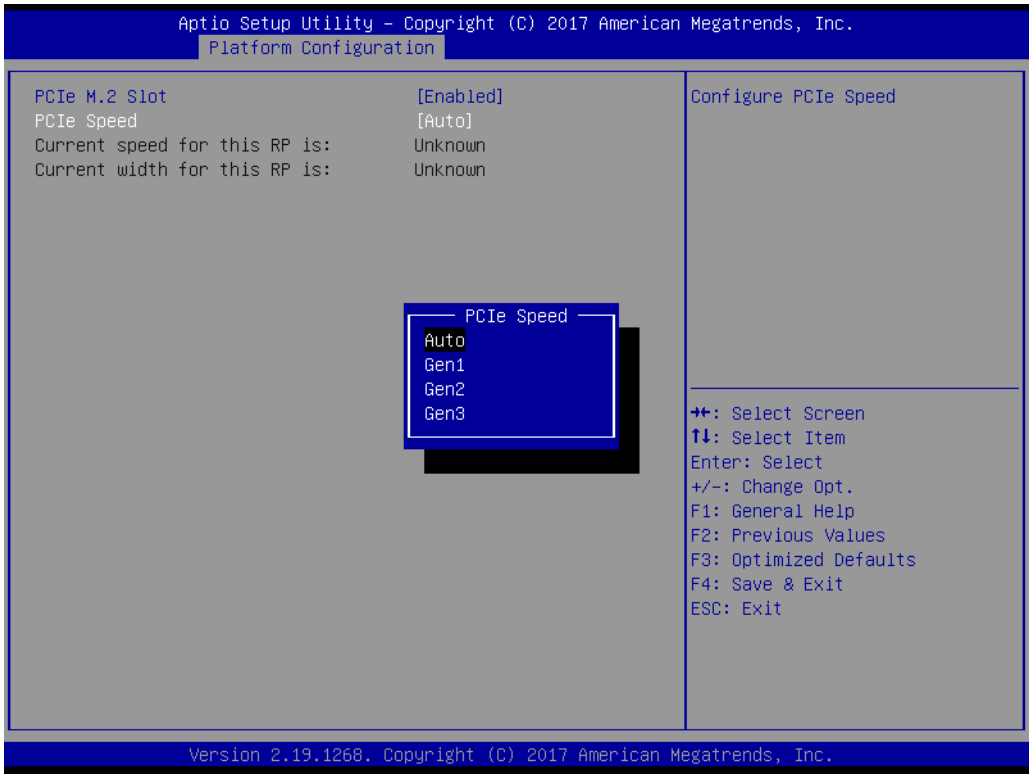
■ **PCIe M.2 Slot Configuration**

– **PCIe M.2 Slot**

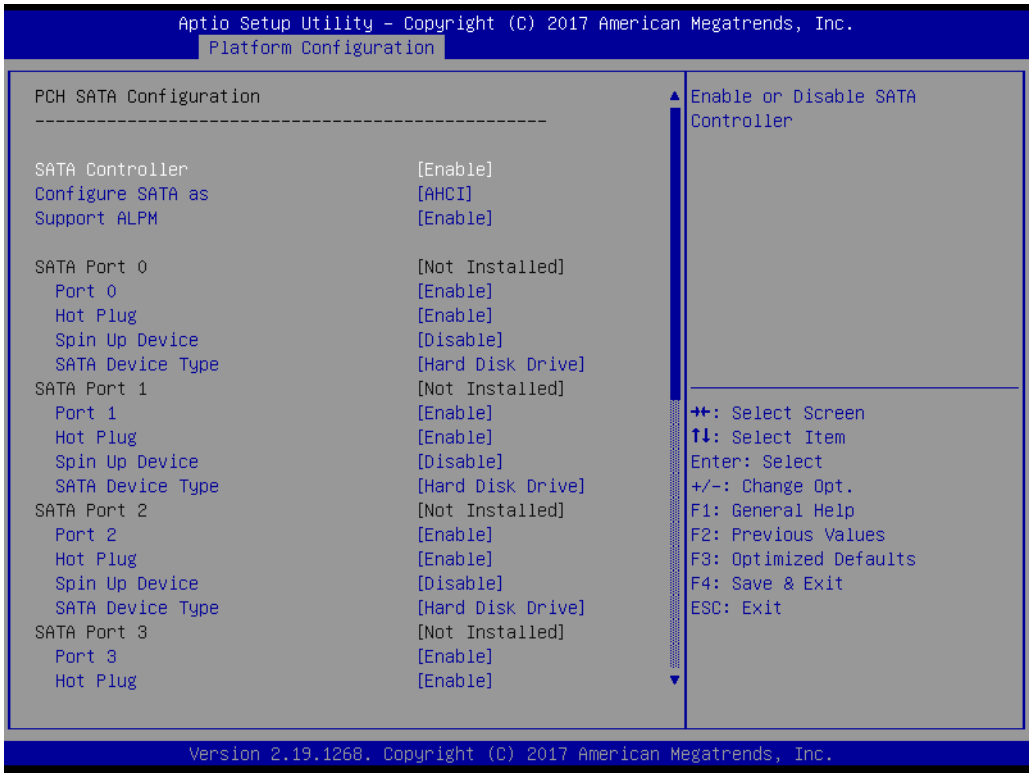
Enable or disable the PCI Express root port.

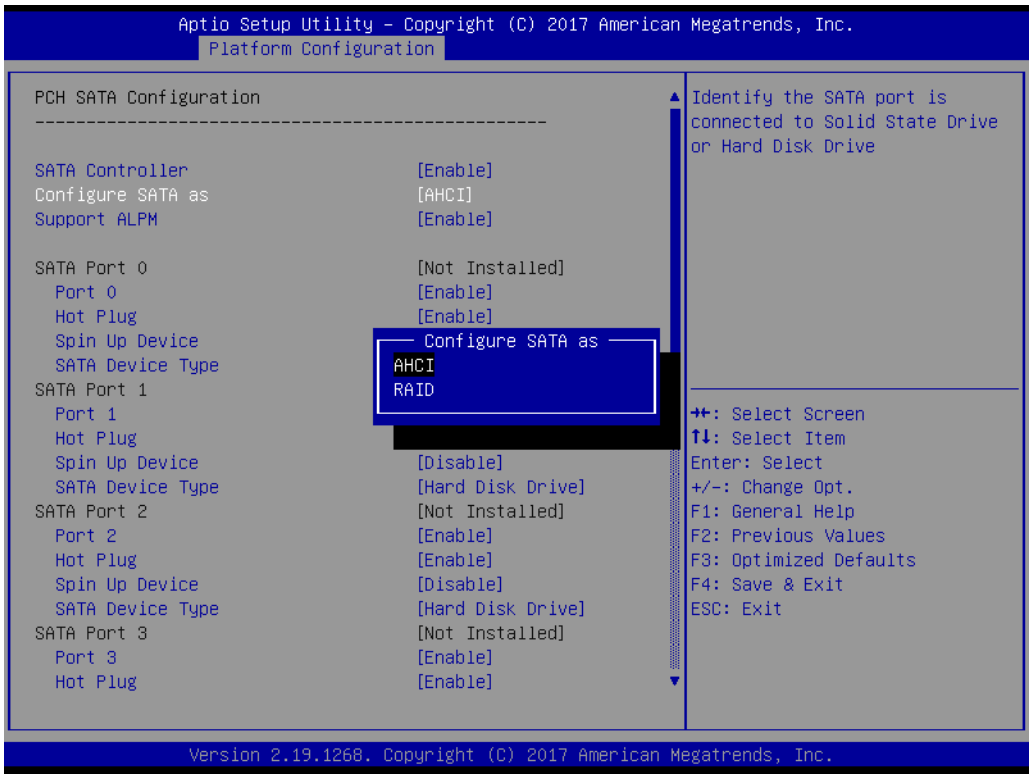


– **PCIe Speed**
  Configure PCI Express speed.



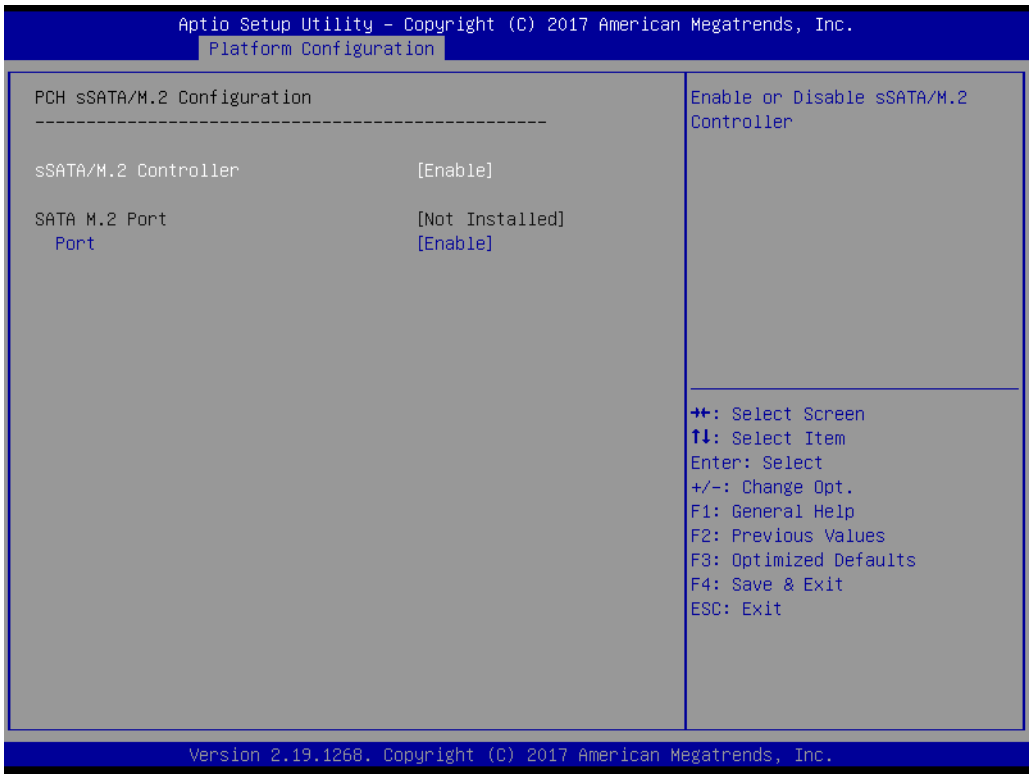
■ PCH SATA Configuration





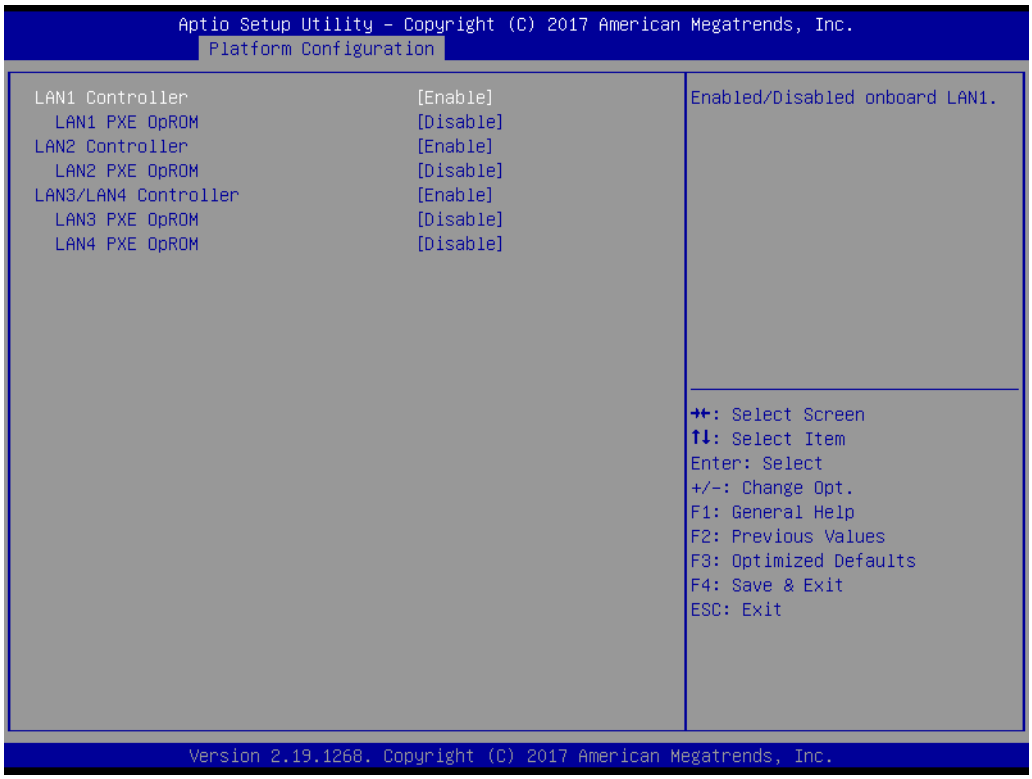
- **SATA controller**
  Enable or disable SATA devices.
- **Configure SATA as**
  Set as AHCI or RAID when SATA controllers are enabled.
- **Support ALPM**
  Enable or disable Aggressive Link Power Management (ALPM) protocol for Advanced Host controller Interface-compliant (AHCI) Serial ATA (SATA) devices.
- **SATA Port 0~7**
  Enable or disable SATA port 0~7.
- **Hot Plug Port 0~7**
  Designates SATA port 0~7 as hot pluggable.
- **SATA Port 0~7 Spin Up Device**
  On an edge detect from 0 to 1, the PCH starts a COMRESET initialization sequence to the device.
- **SATA Port 0~7 Device Type**
  To identify the SATA is connected to Solid State Drive or Hard Disk Drive.

■ **PCH sSATA/M.2 Configuration**



– **sSATA/M.2 controller**
  Enable or disable sSATA/M.2 controller.
– **SATA M.2 Port**
  Enable or disable SATA port.

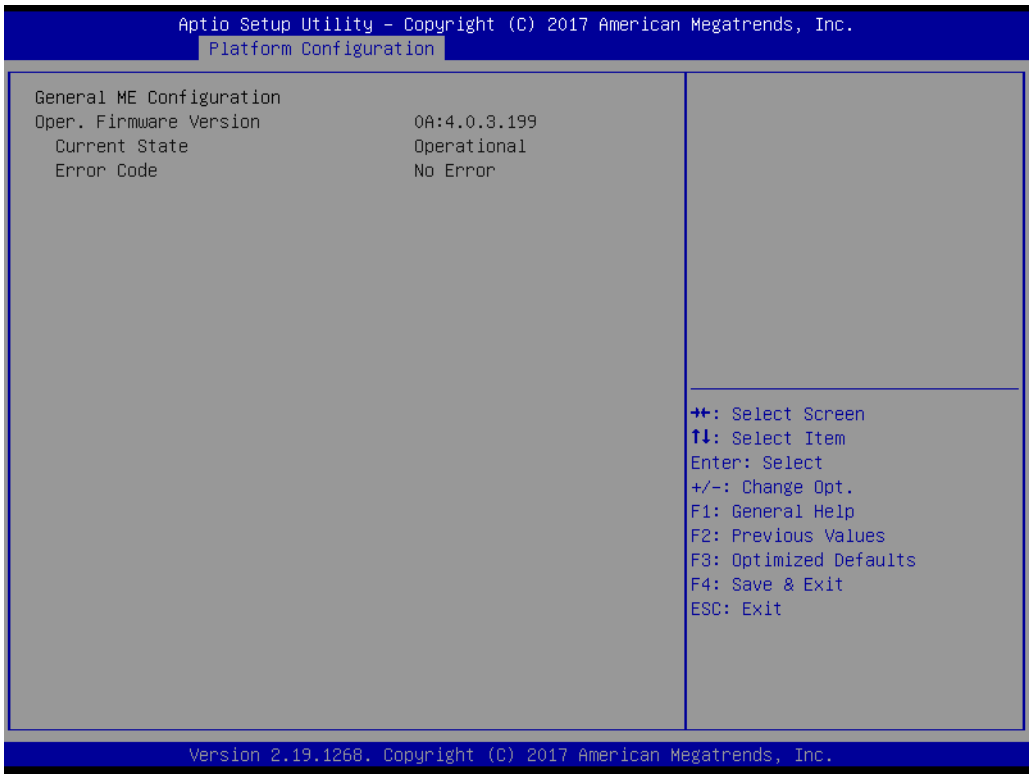
■ Networking



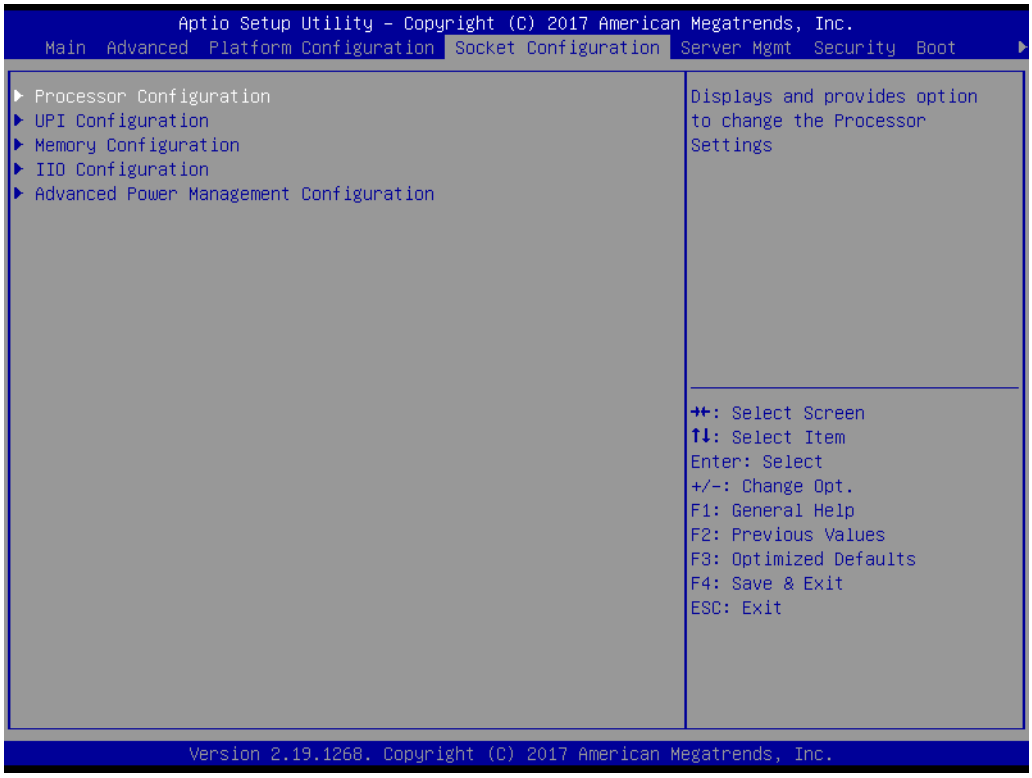
- **LAN1 controller**
  Enable or disable Intel I210 controller support.
- **LAN1 PXE OpROM**
  Enable or disable Boot option for Intel I210 controller.
- **LAN2 controller**
  Enable or disable Intel I210 controller support.
- **LAN2 PXE OpROM**
  Enable or disable Boot option for Intel I210 controller.
- **LAN3/LAN4 controller**
  Enable or disable Intel X557 controller support.
- **LAN3 PXE OpROM**
  Enable or disable boot option for Intel X557 controller.
- **LAN4 PXE OpROM**
  Enable or disable boot option for Intel X557 controller.

3.2.3.2 Server ME Configuration

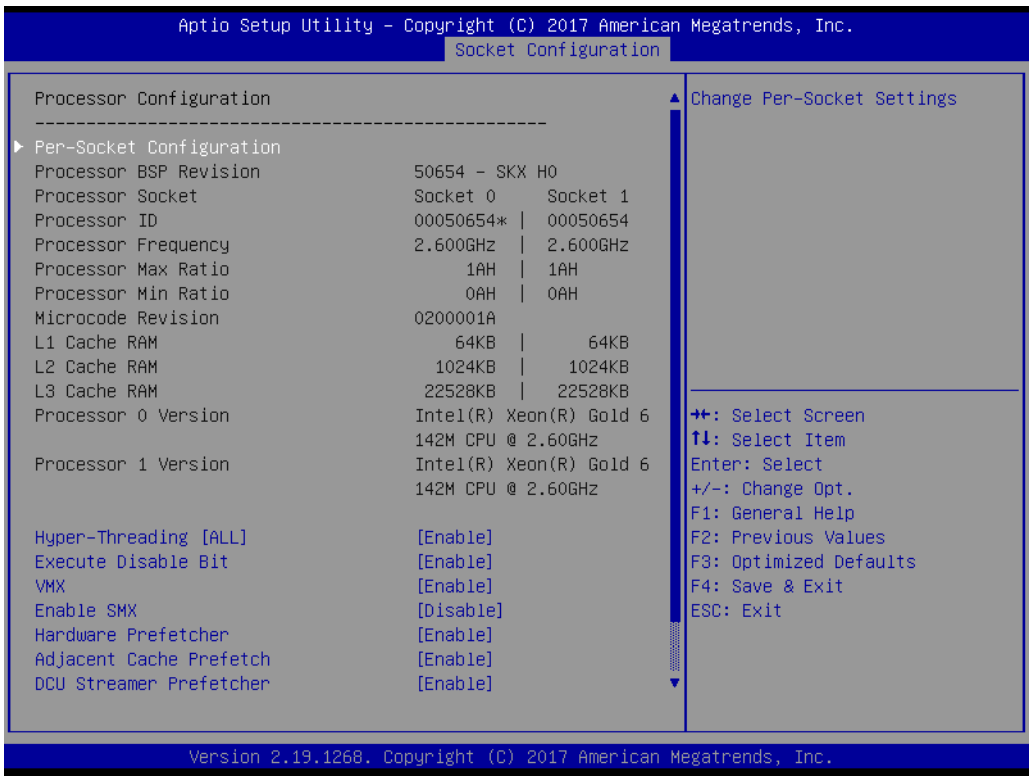
This page shows the Server ME configuration information.

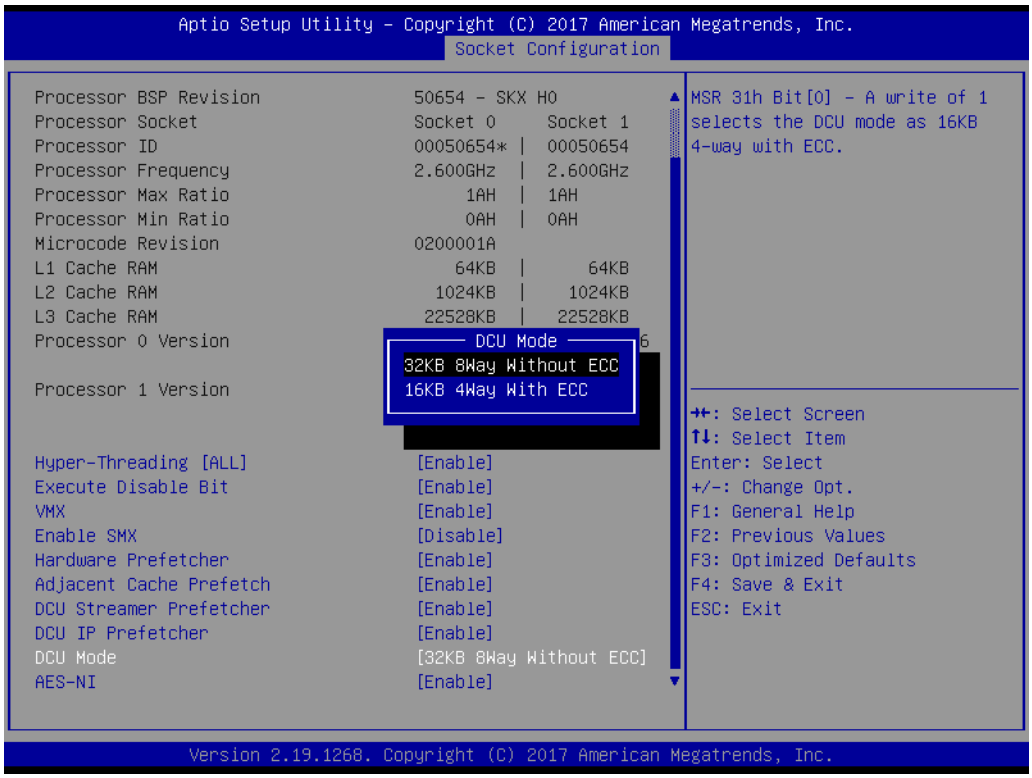


3.2.4 Socket Configuration



3.2.4.1 Processor Configuration

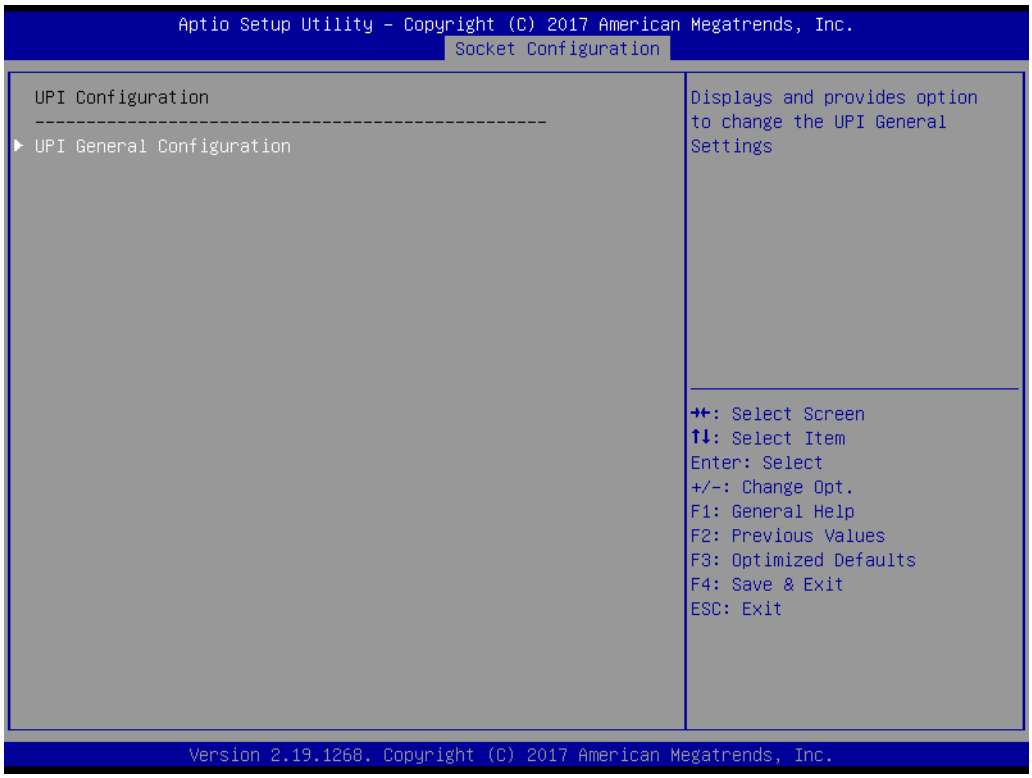




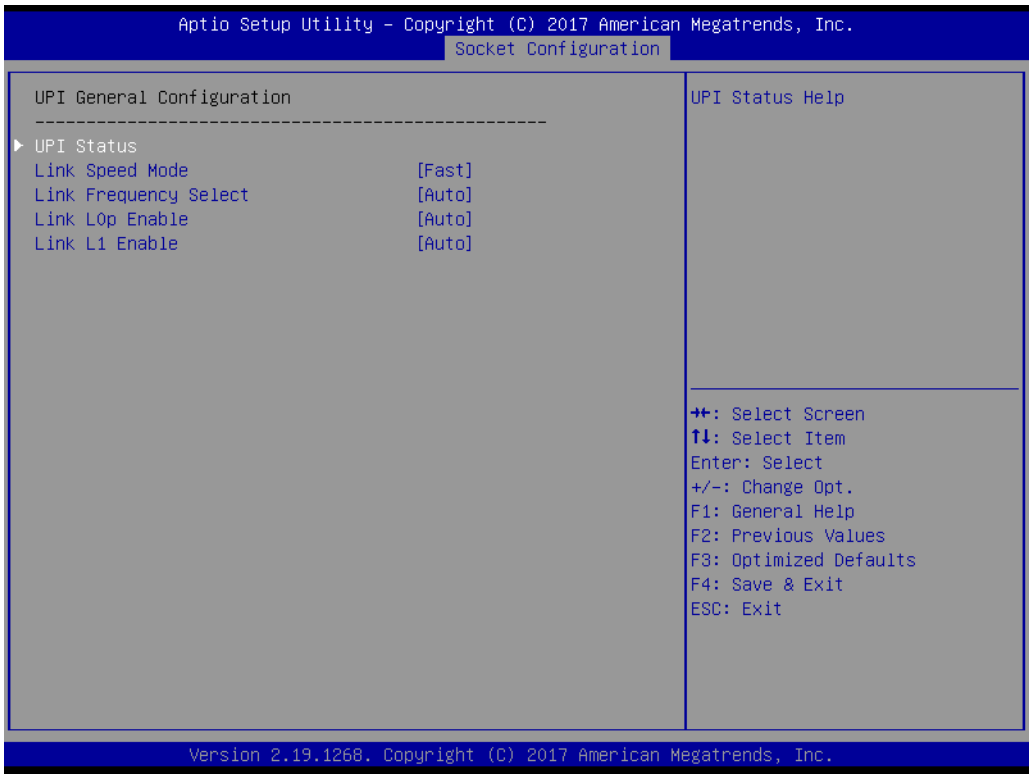
- **Per-Socket Configuration**
  Use this to select how many processor cores you want to activate when you are using a dual or quad core processor.
- **Hyper-threading [All]**
  Enable or disable Intel Hyper Threading technology.
- **Execute disable Bit**
  Enable or disable the Execute disable Bit feature. The Optimal and Fail-Safe default setting is enabled. If disable is selected, the BIOS forces the XD feature flag to always return to 0.
- **VMX**
  Enable or disable Intel Virtual Machine Extensions (VMX) for IA-32 processors that support Intel® Vanderpool Technology
- **Enable SMX**
  Enable or disable Safer Mode Extensions. Safer Mode Extensions (SMX) provide a means for system software to launch an MLE and establish a measured environment within the platform to support trust decisions by end users.
- **Hardware Prefetcher**
  Hardware Prefetcher is a technique that fetches instructions and/or data from memory into the CPU cache memory well before the CPU needs it, so that it can improve the load-to-use latency.
- **Adjacent Cache Prefetch**
  The Adjacent Cache-Line Prefetch mechanism, like automatic hardware prefetch, operates without programmer intervention. When enabled through the BIOS, two 64-byte cache lines are fetched into a 128-byte sector, regardless of whether the additional cache line has been requested or not.
- **DCU Streamer Prefetcher**
  Enable prefetch of next L1 data line based upon multiple loads in same cache line.

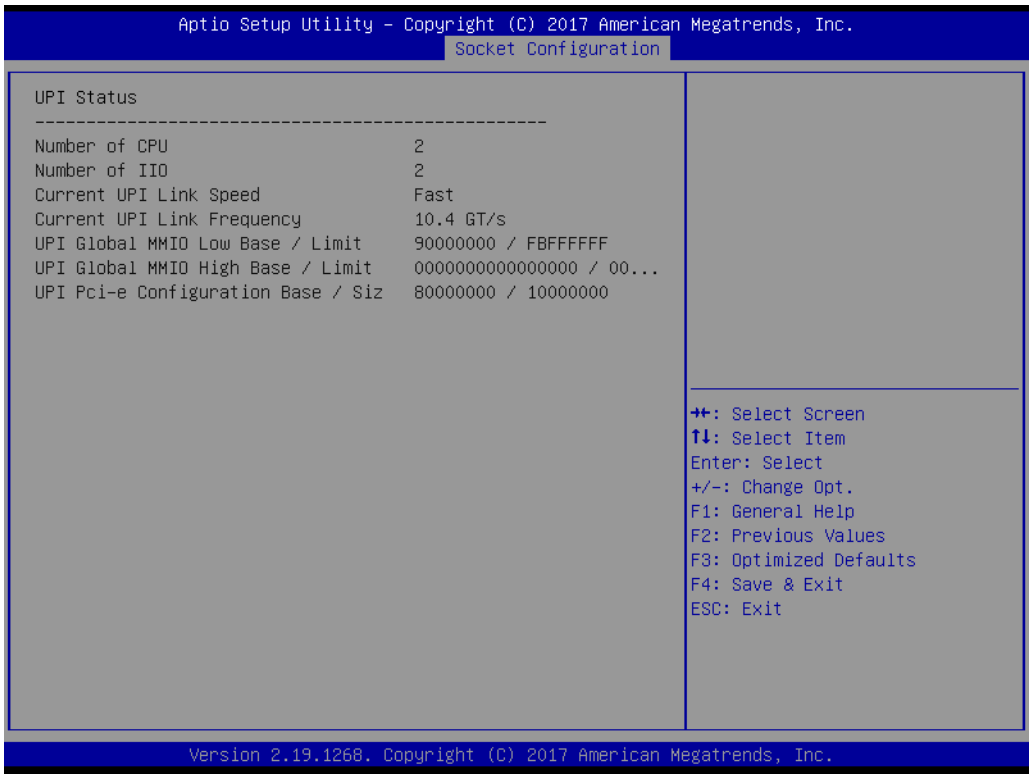
- **DCU IP Prefetcher**
  Enable prefetch of next L1 line based upon sequential load history.
- **DCU Mode**
  Change the data cache unit mode.
- **AES-NI**
  This item is to Enable or disable CPU advanced encryption standard instructions.

3.2.4.2 UPI Configuration

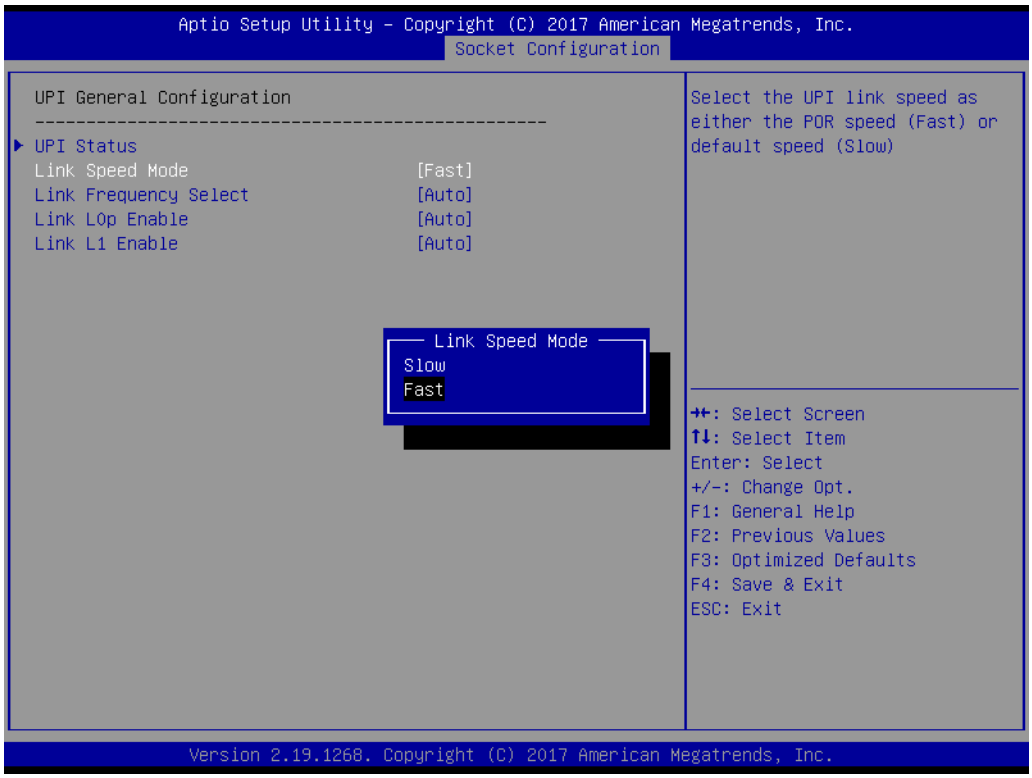


- **UPI Status**
  Display information of Intel UltraPath Interconnect (UPI).

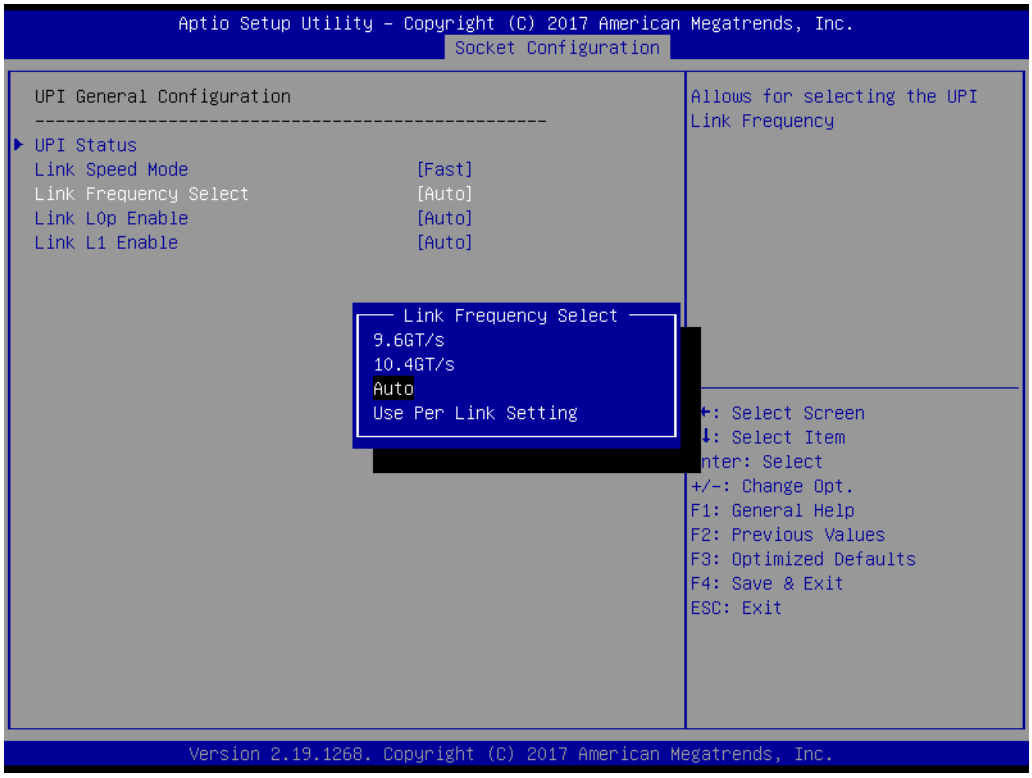




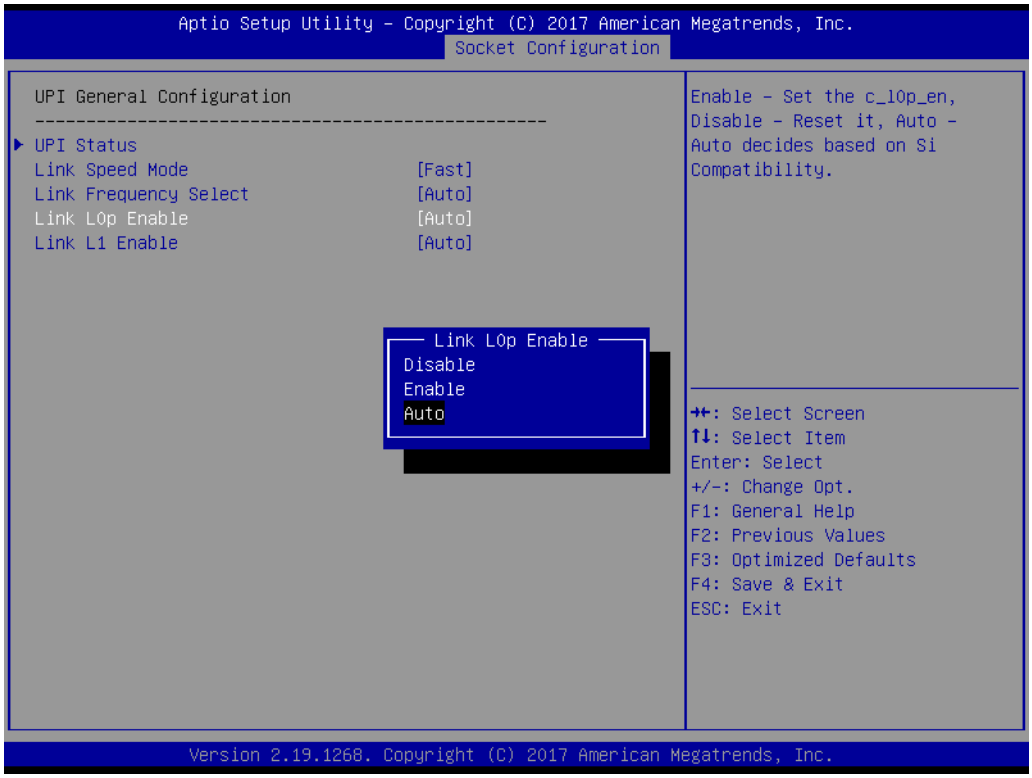
- **Link Speed Mode**
  Select the QPI link speed as either the Fast mode or Slow mode.



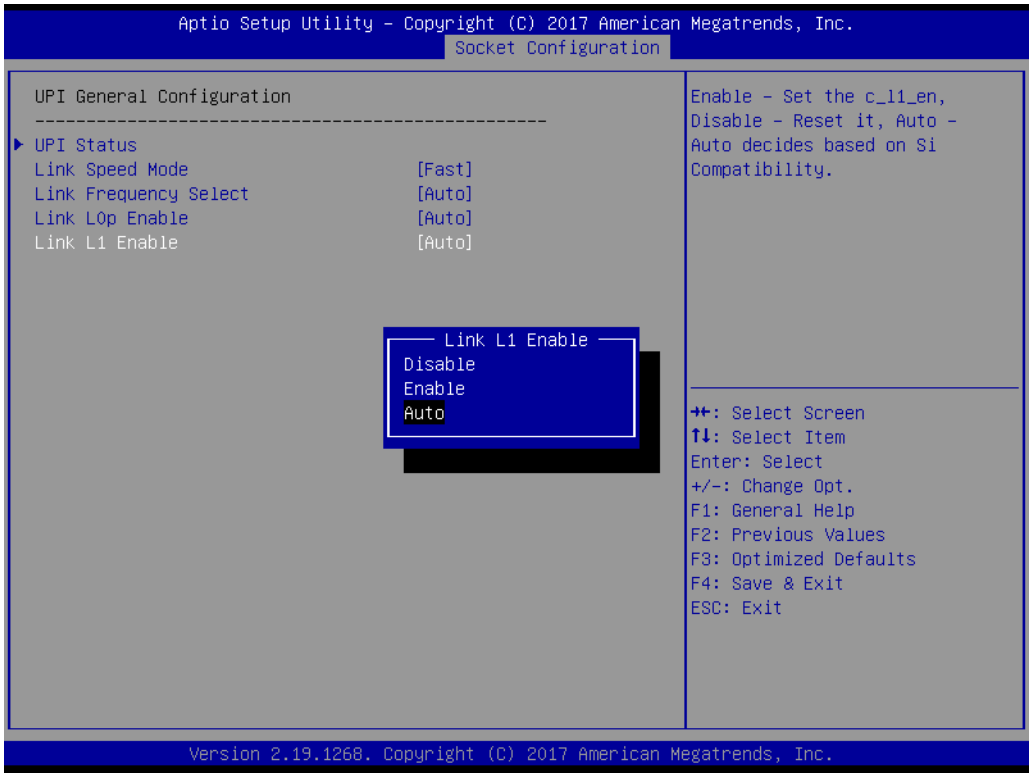
- **Link Frequency Select**
  Allows for selecting the QPI Link frequency.



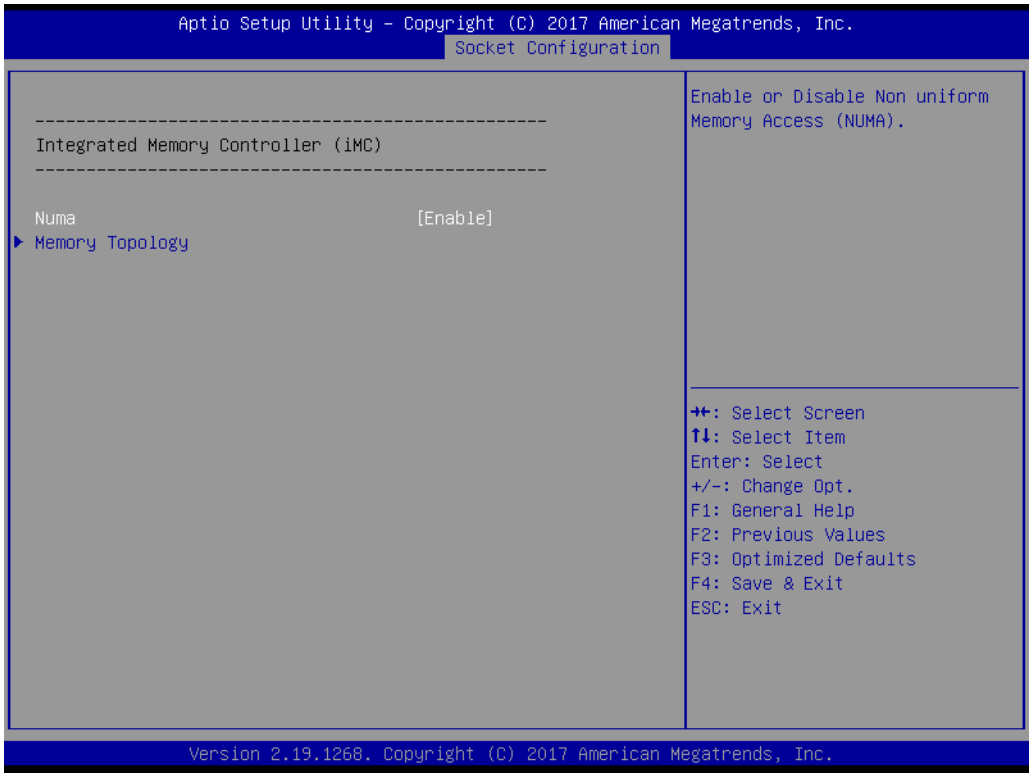
- **Link L0p Enable**
  Enable or disable QPI Link0p.



- **Link L1 Enable**
  Enable or disable QPI Link1.

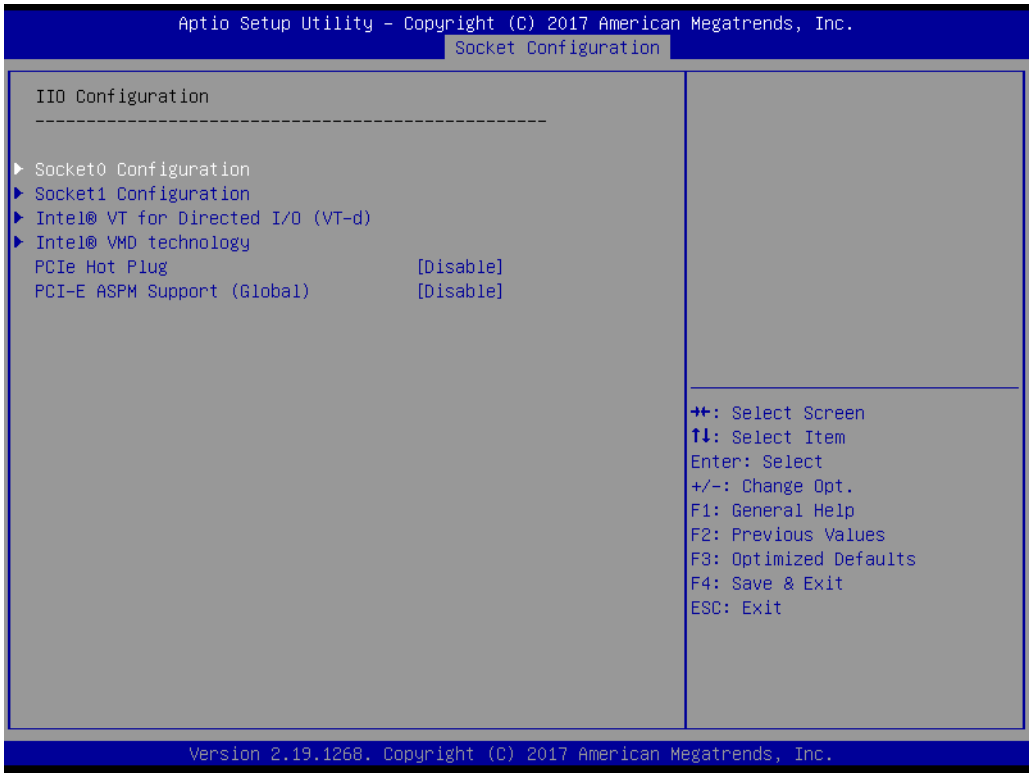


3.2.4.3 Memory Configuration



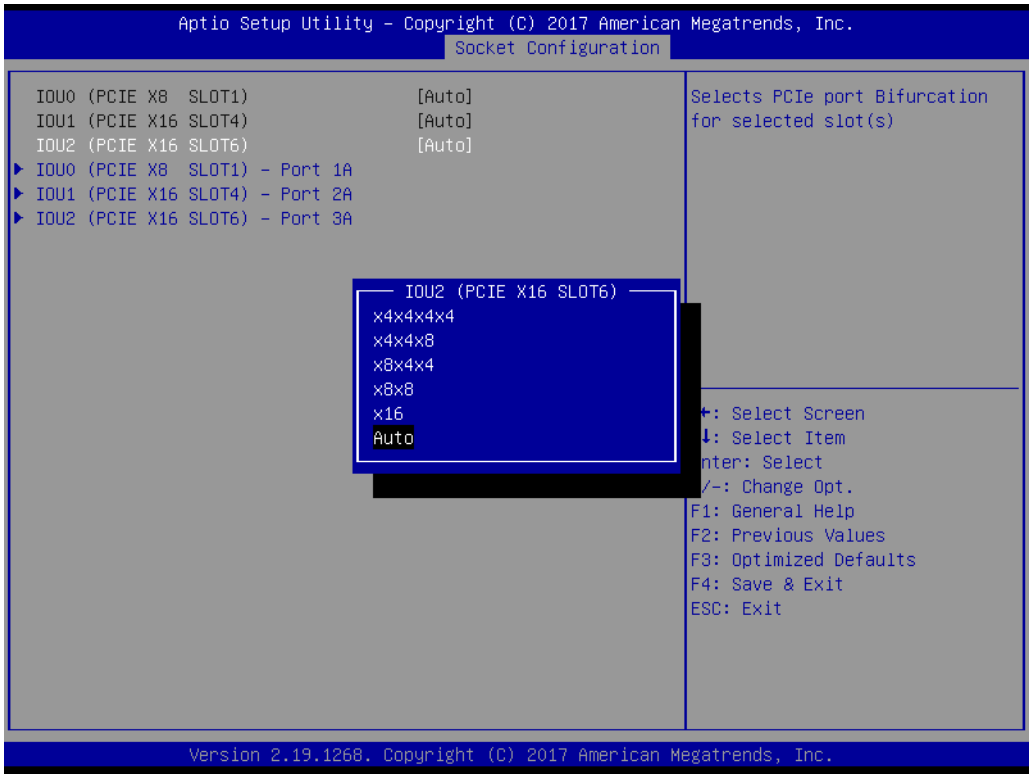
- **NUMA**
  Enable or disable non uniform memory access (NUMA).
- **Memory Technology**
  Display memory topology with DIMM population information.

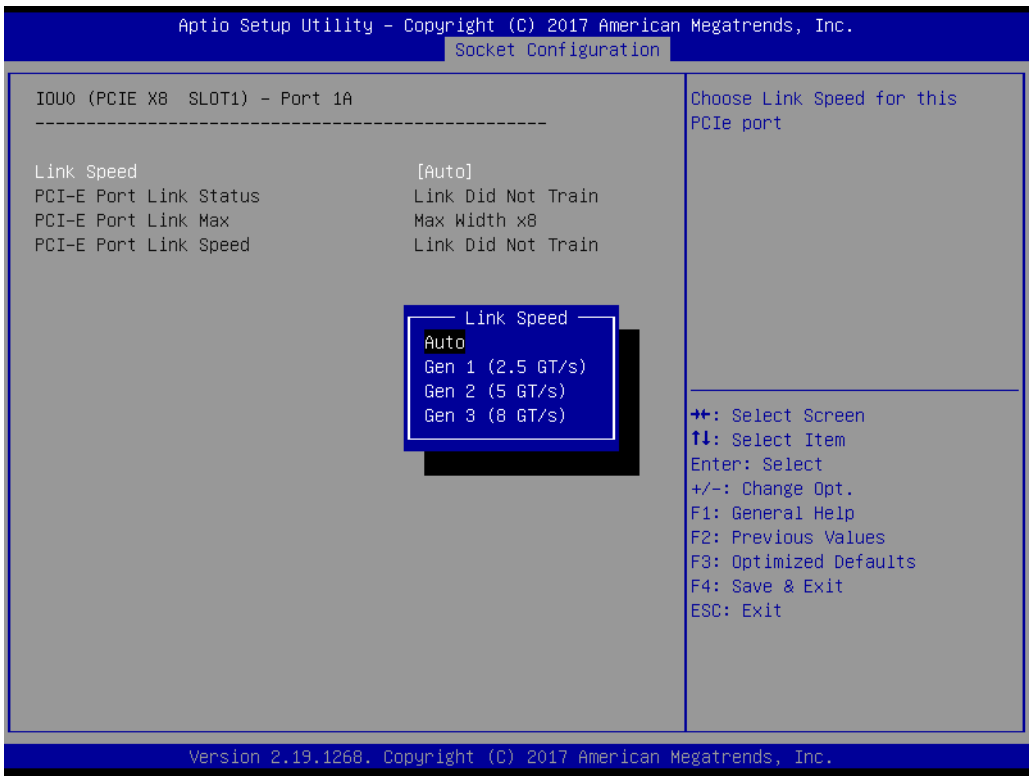
3.2.4.4 IIO Configuration

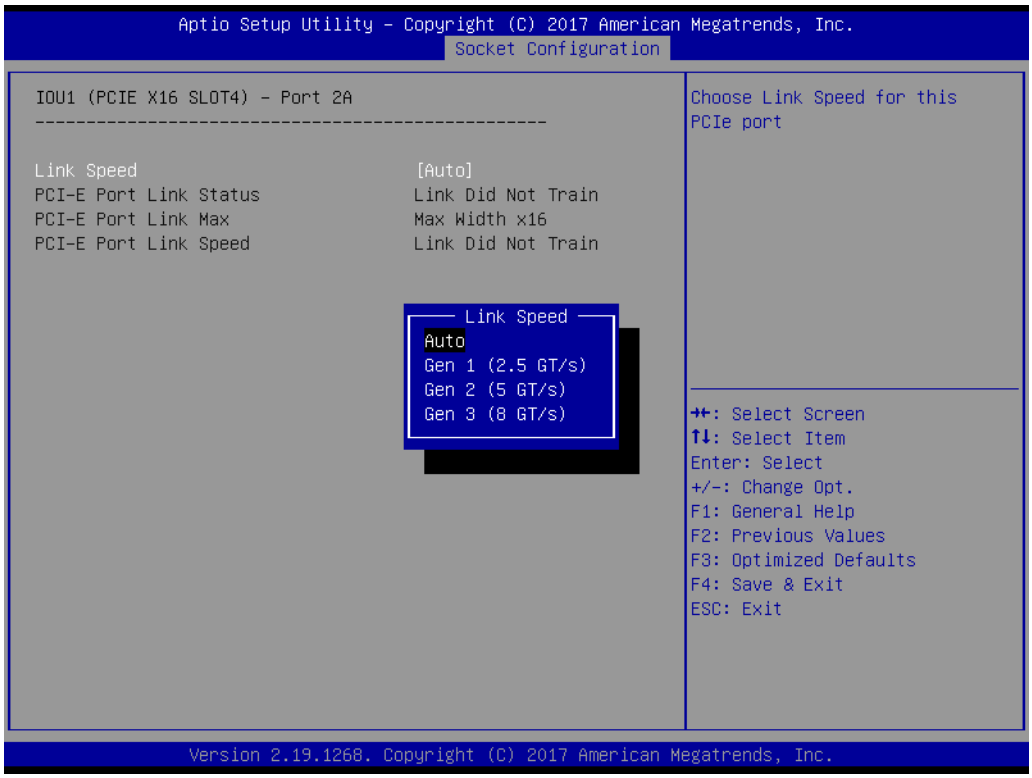


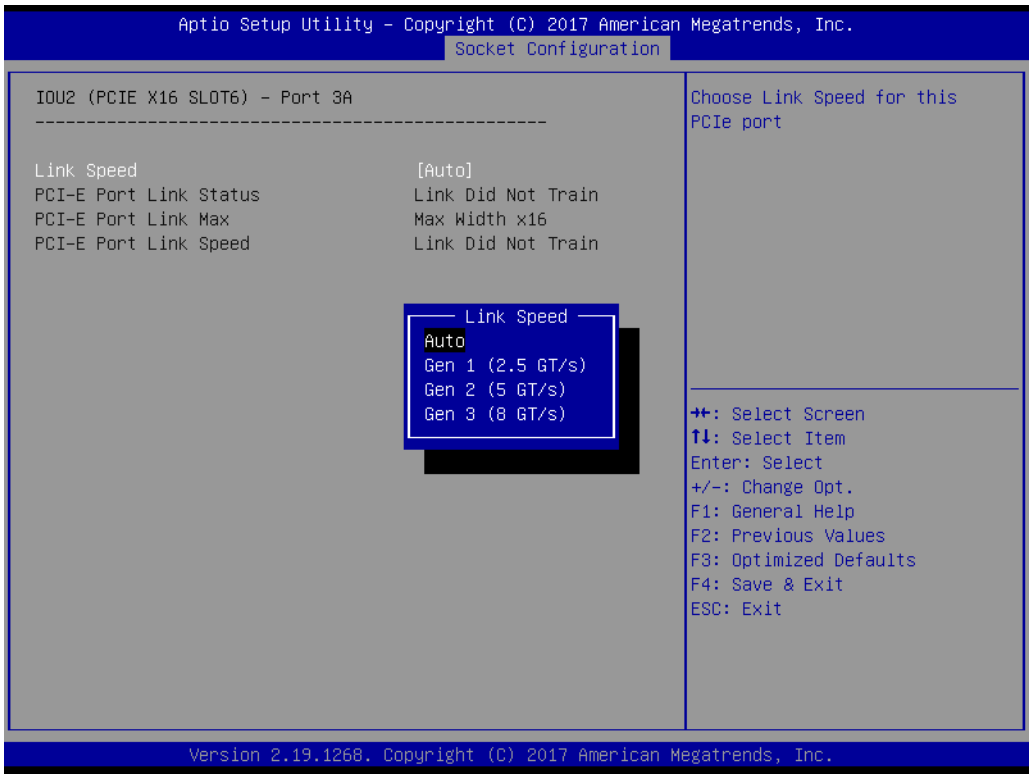
Socket0 PCIe Configuration

PCIe port bifurcation control and select target link speed as Gen1, Gen2, Gen3.



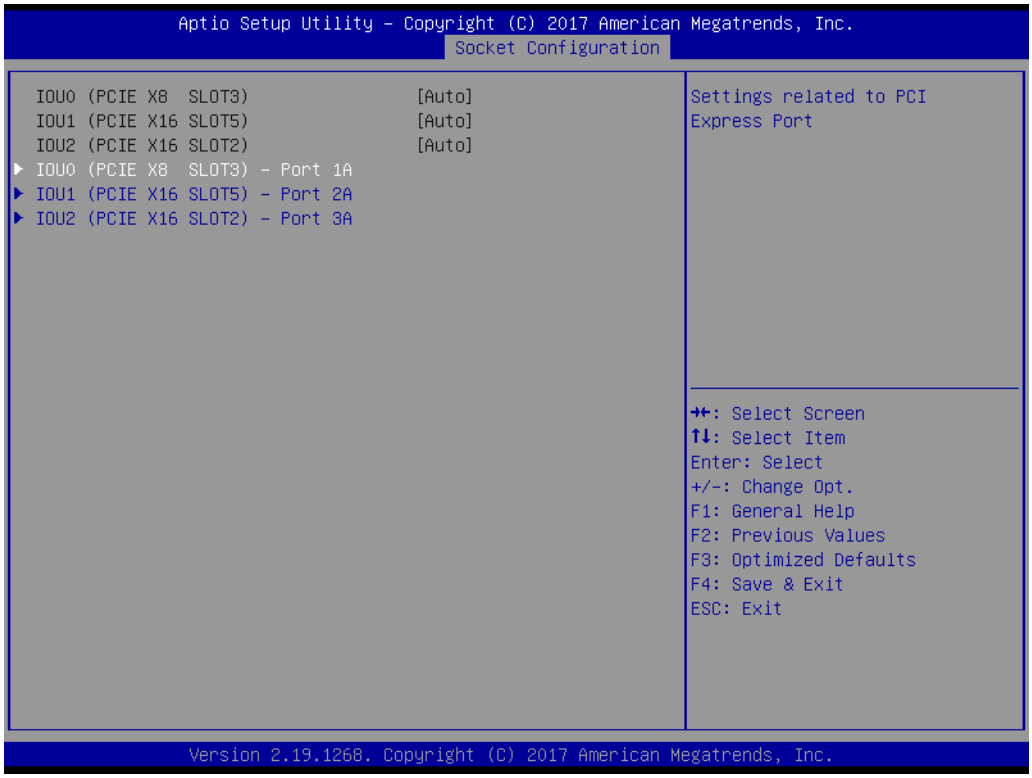


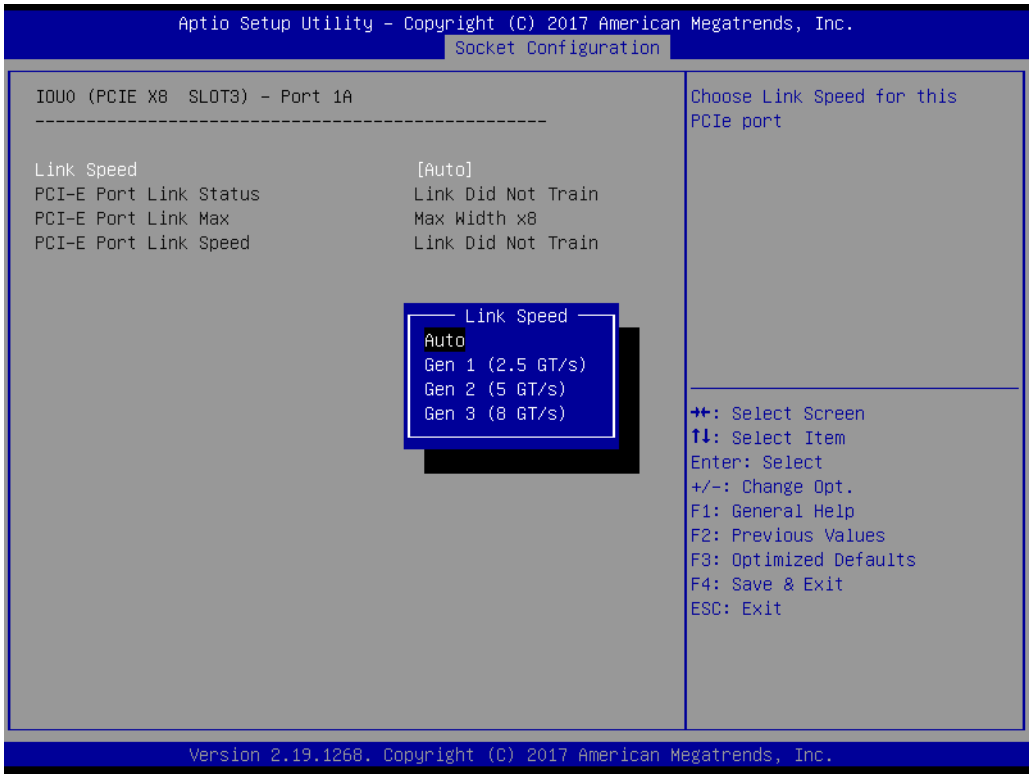

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc.
Socket Configuration
IOU1 (PCIE X16 SLOT4) – Port 2A
Link Speed [Auto]
PCI-E Port Link Status Link Did Not Train
PCI-E Port Link Max Max Width x16
PCI-E Port Link Speed Link Did Not Train
Link Speed
Auto
Gen 1 (2.5 GT/s)
Gen 2 (5 GT/s)
Gen 3 (8 GT/s)
Choose Link Speed for this PCIe port
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc.

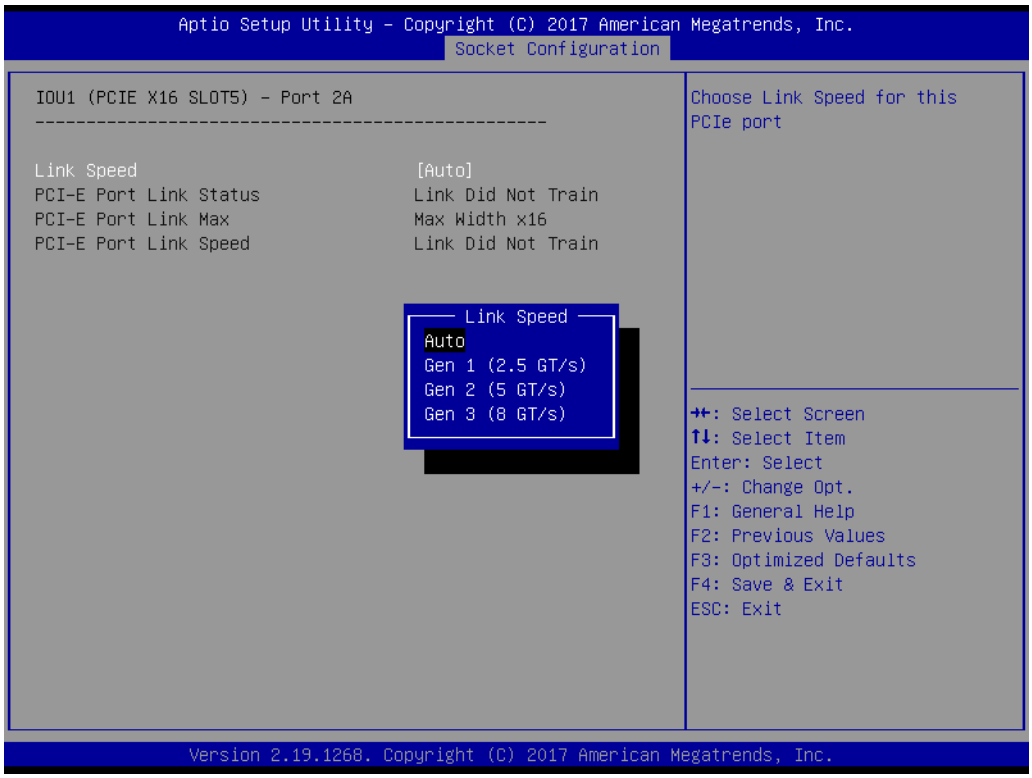

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc.
Socket Configuration
IOU2 (PCIE X16 SLOT6) – Port 3A
Link Speed [Auto]
PCI-E Port Link Status Link Did Not Train
PCI-E Port Link Max Max Width x16
PCI-E Port Link Speed Link Did Not Train
Link Speed
Auto
Gen 1 (2.5 GT/s)
Gen 2 (5 GT/s)
Gen 3 (8 GT/s)
Choose Link Speed for this PCIe port
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc.

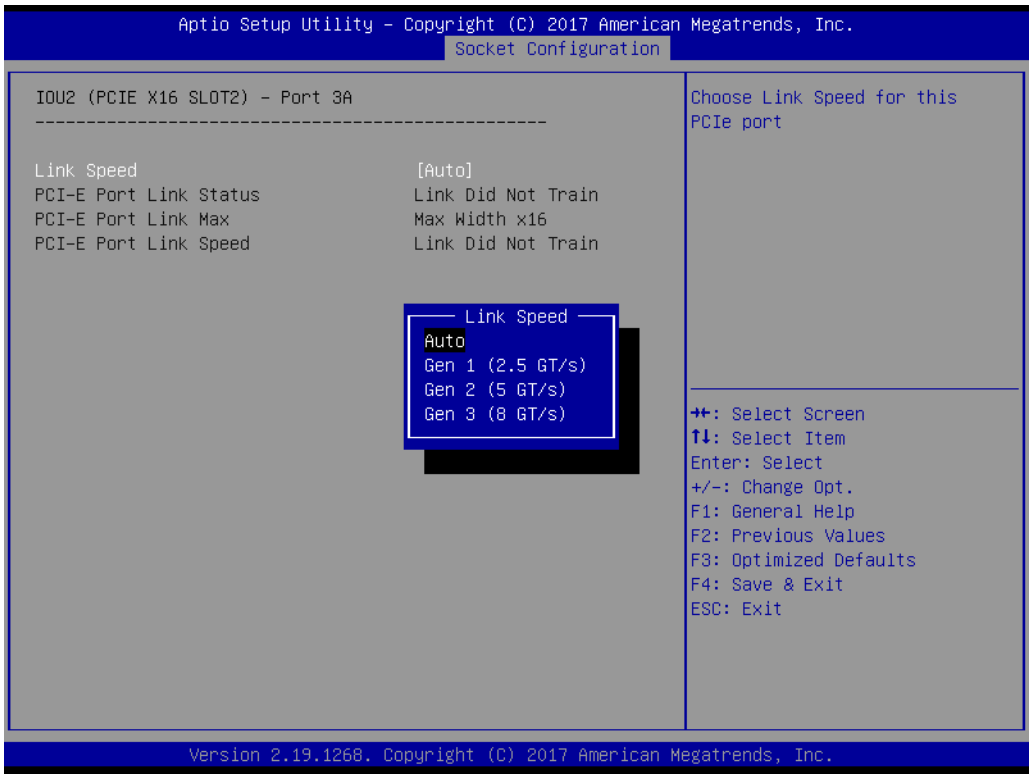
Socket1 PCIe Configuration

PCIe port bifurcation control and select target link speed as Gen1, Gen2, Gen3.

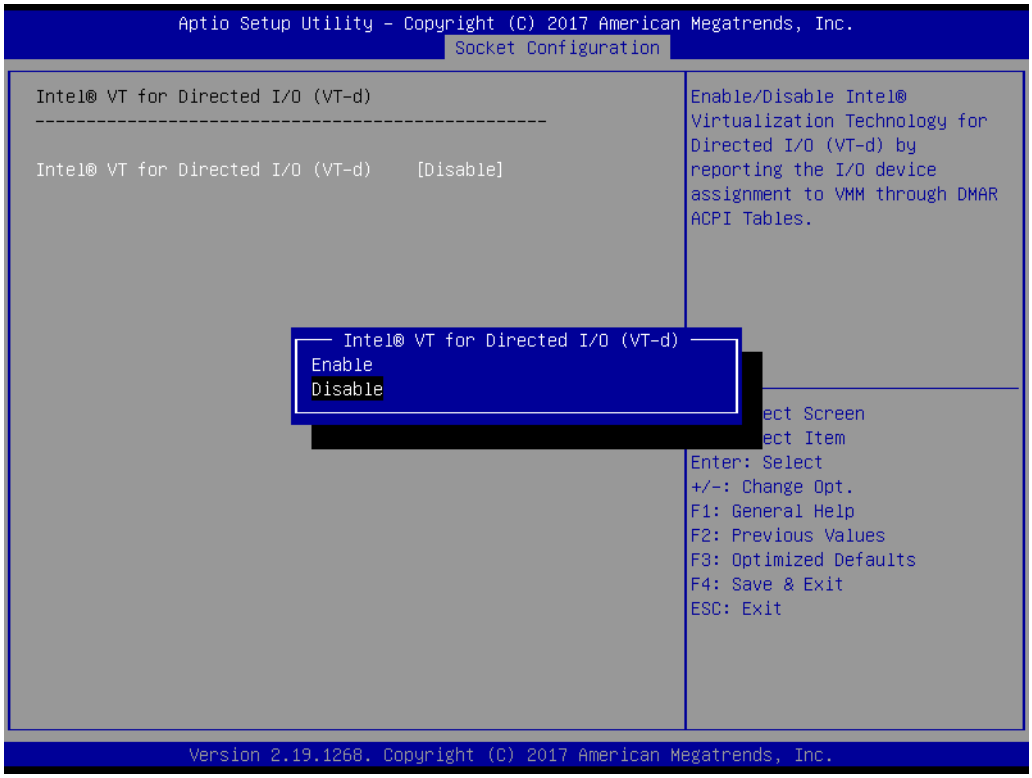




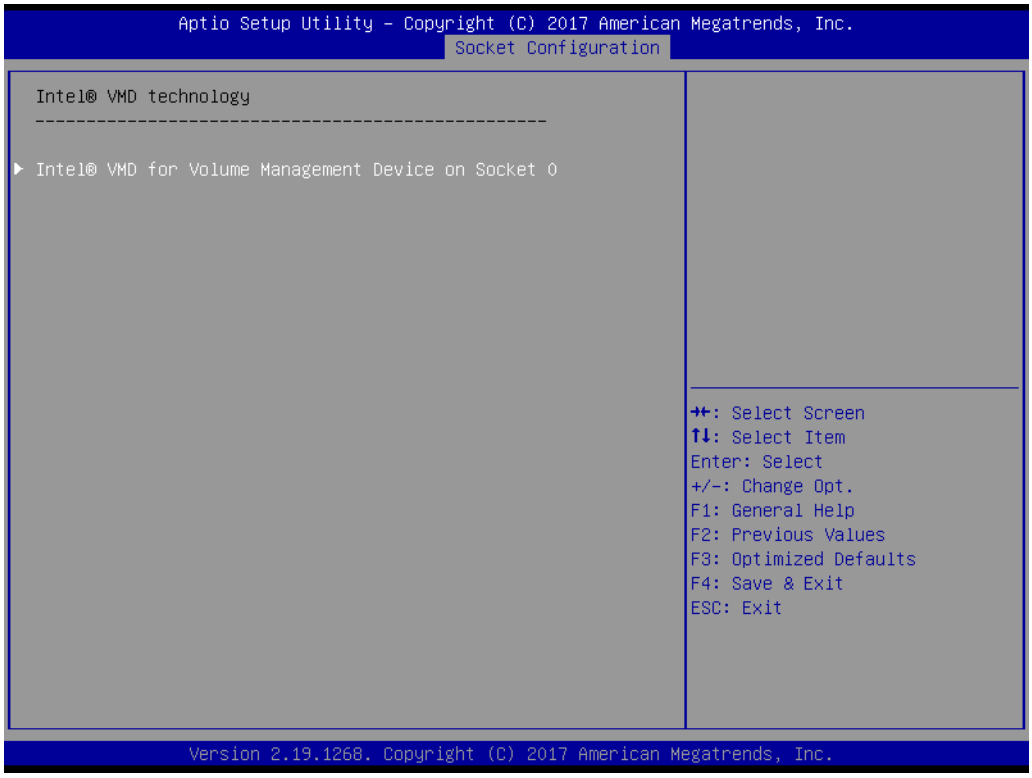

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc.
Socket Configuration
IOU1 (PCIE X16 SLOT5) – Port 2A
Link Speed [Auto]
PCI-E Port Link Status Link Did Not Train
PCI-E Port Link Max Max Width x16
PCI-E Port Link Speed Link Did Not Train
Link Speed
Auto
Gen 1 (2.5 GT/s)
Gen 2 (5 GT/s)
Gen 3 (8 GT/s)
Choose Link Speed for this PCIe port
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc.

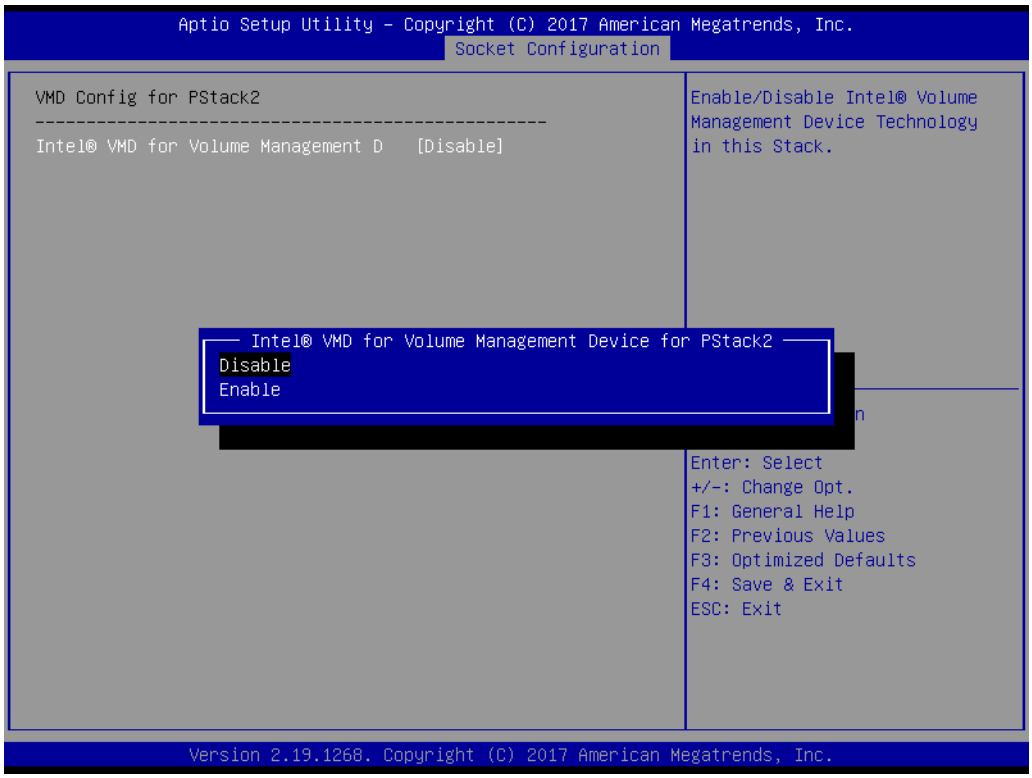

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc.
Socket Configuration
IOU2 (PCIE X16 SLOT2) – Port 3A
Link Speed [Auto]
PCI-E Port Link Status Link Did Not Train
PCI-E Port Link Max Max Width x16
PCI-E Port Link Speed Link Did Not Train
Link Speed
Auto
Gen 1 (2.5 GT/s)
Gen 2 (5 GT/s)
Gen 3 (8 GT/s)
Choose Link Speed for this PCIe port
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc.

- **Intel VT for Directed I/O (VT-d)**
  Enable or disable Intel Virtualization Technology for Directed I/O.

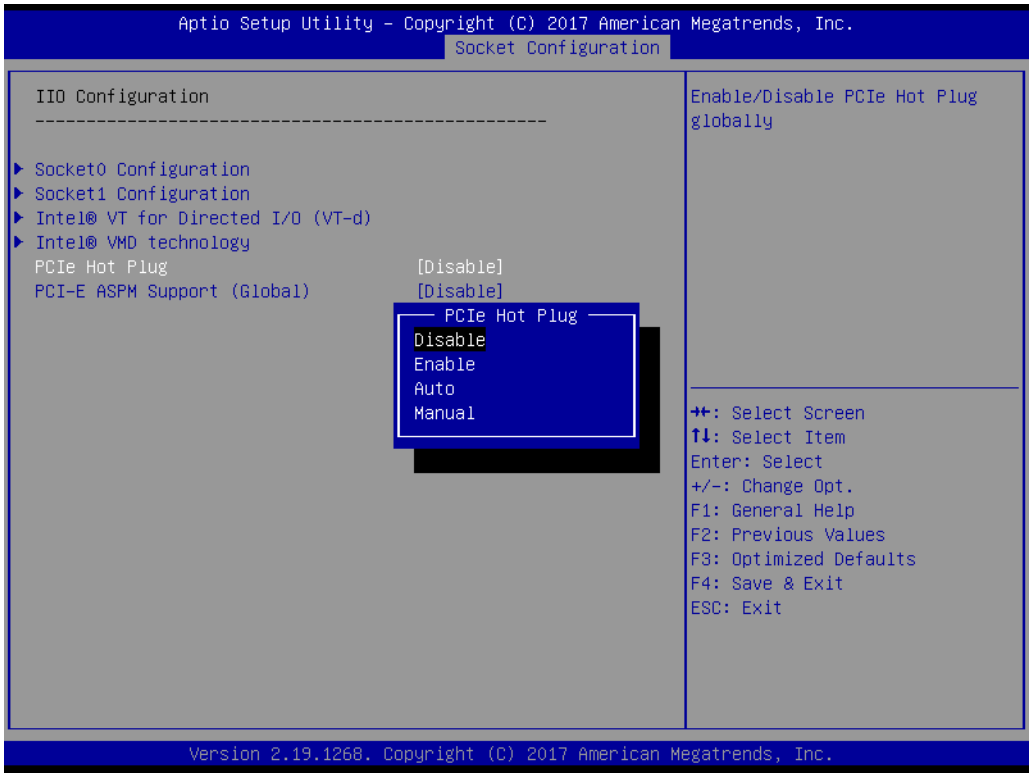


- **Intel VMD technology**
  Enable or disable Intel Volume Management Device Technology.

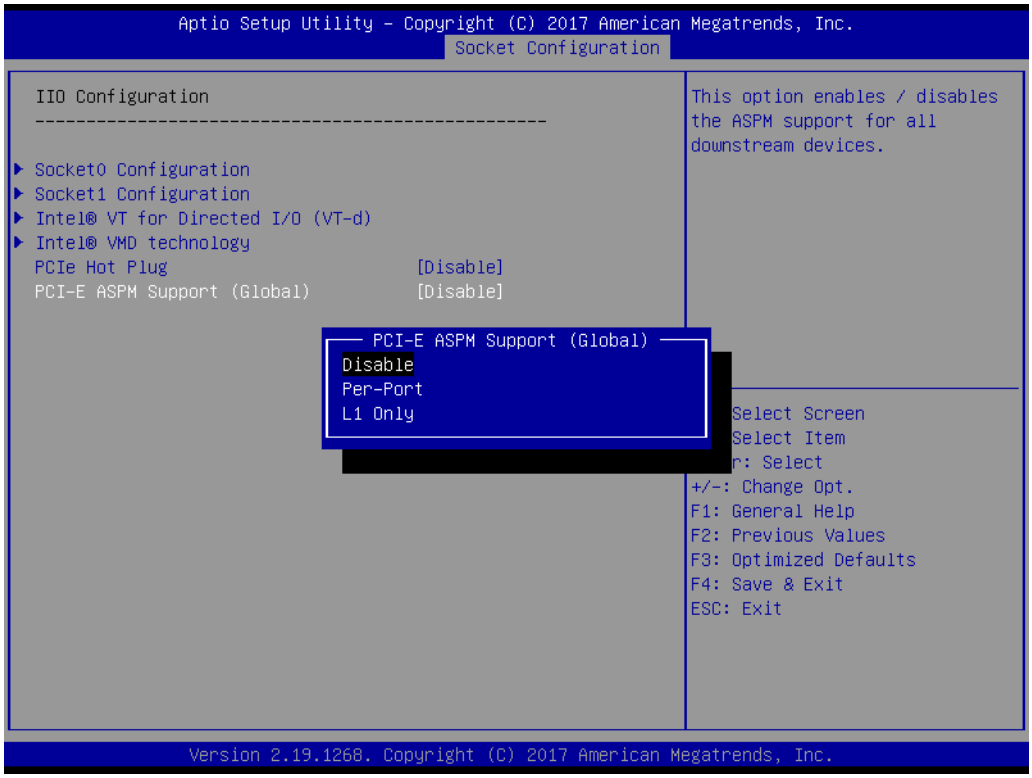




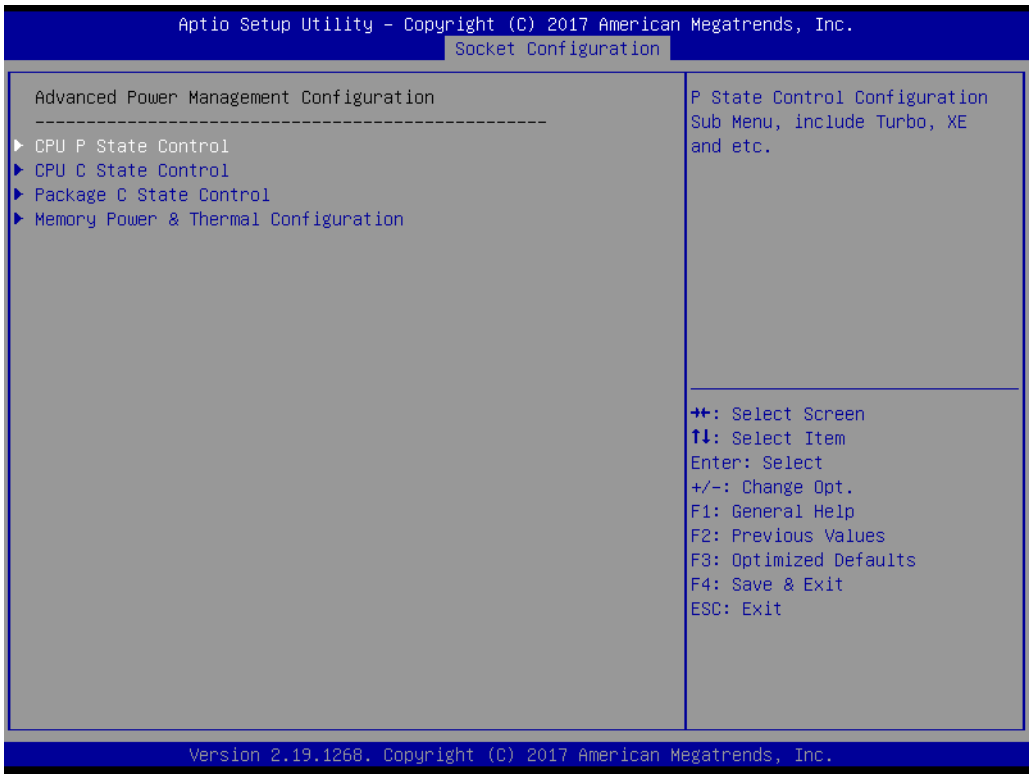
- **PCIe Hot Plug**
  Enable or disable PCIe hot plug globally.



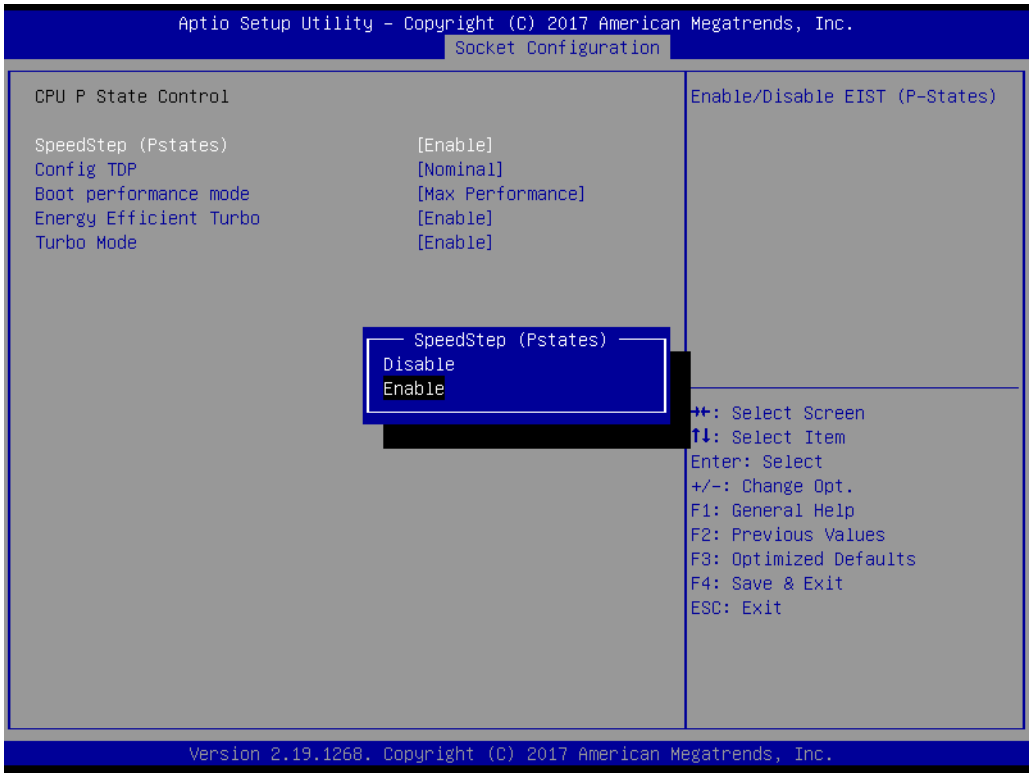
- **PCI-E ASPM Support (Global)**
  Set the ASPM level to Disable, Per-Port or L1 state only.

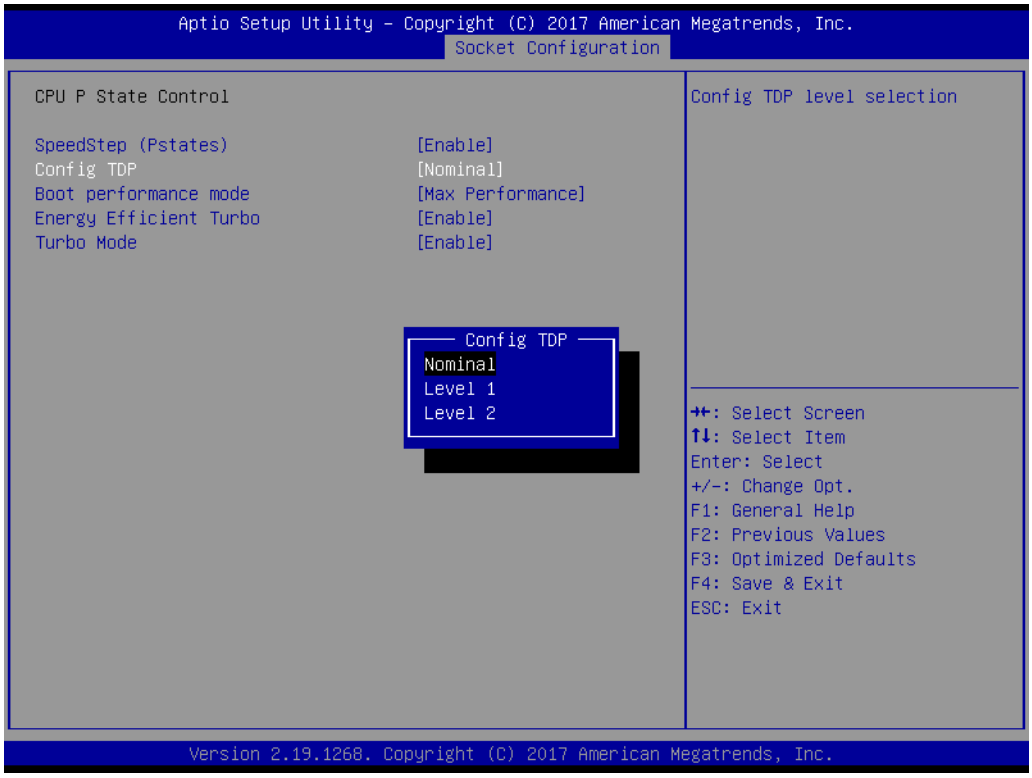


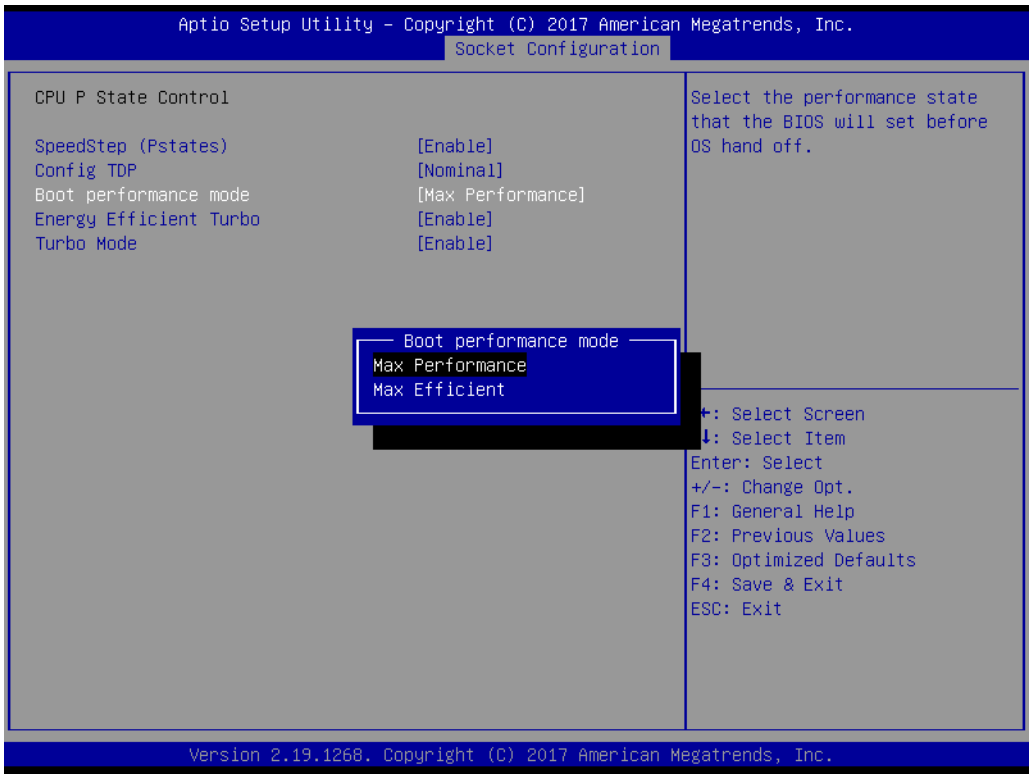
3.2.4.5 Advanced Power Management Configuration

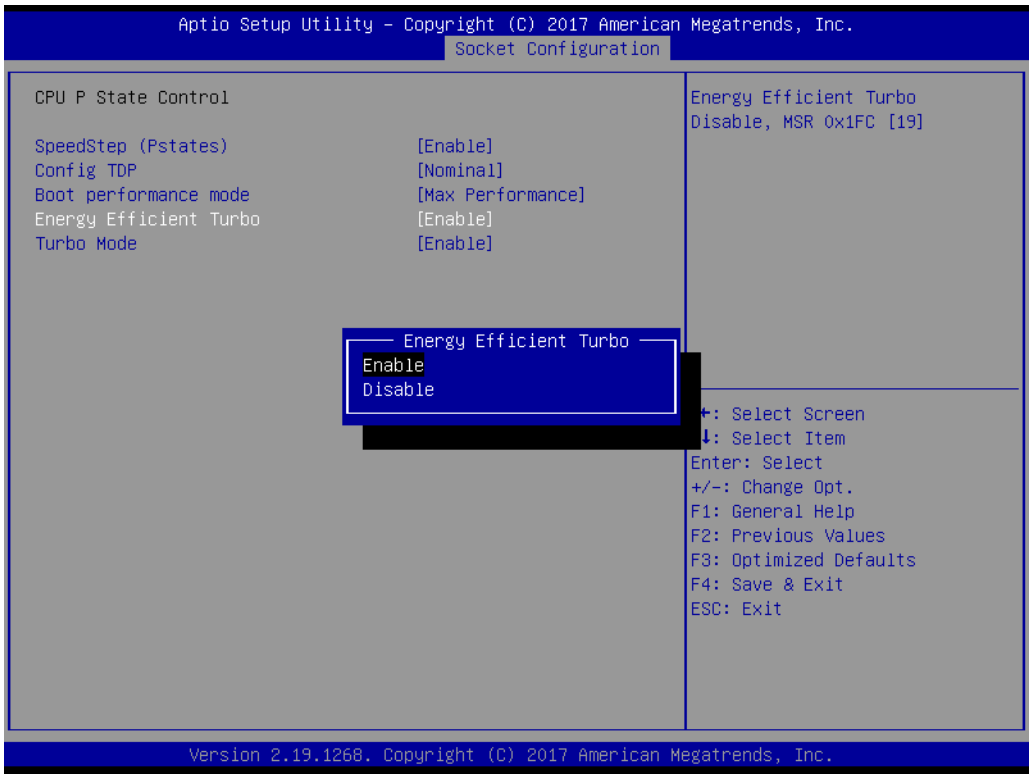


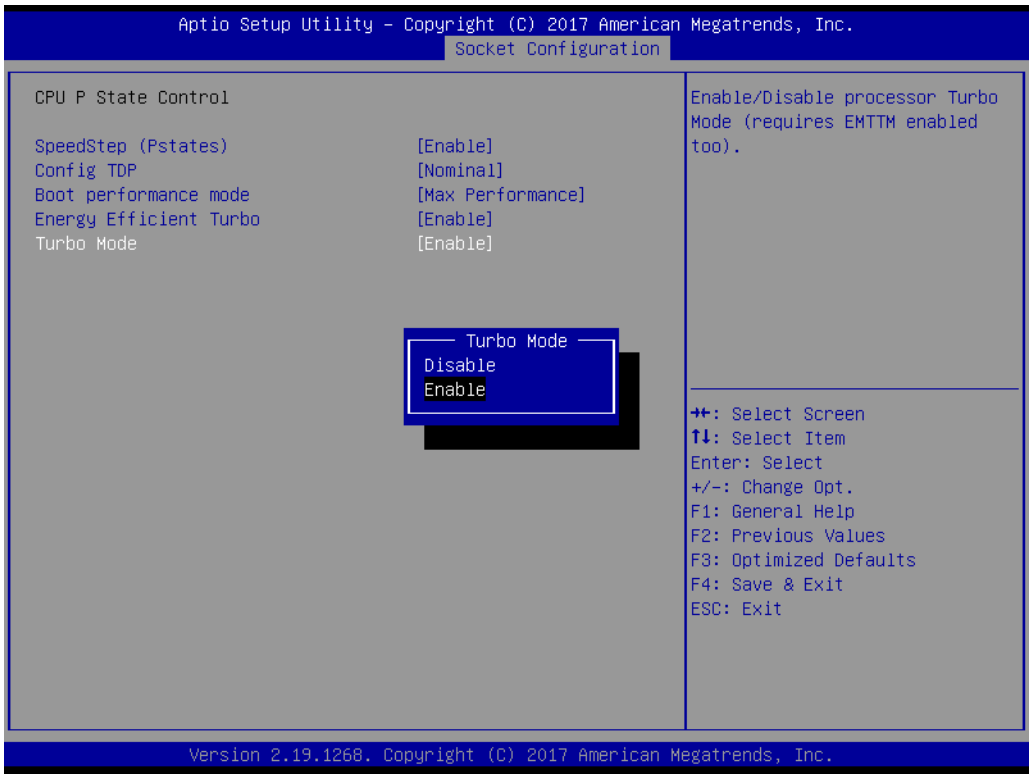
CPU P State Control



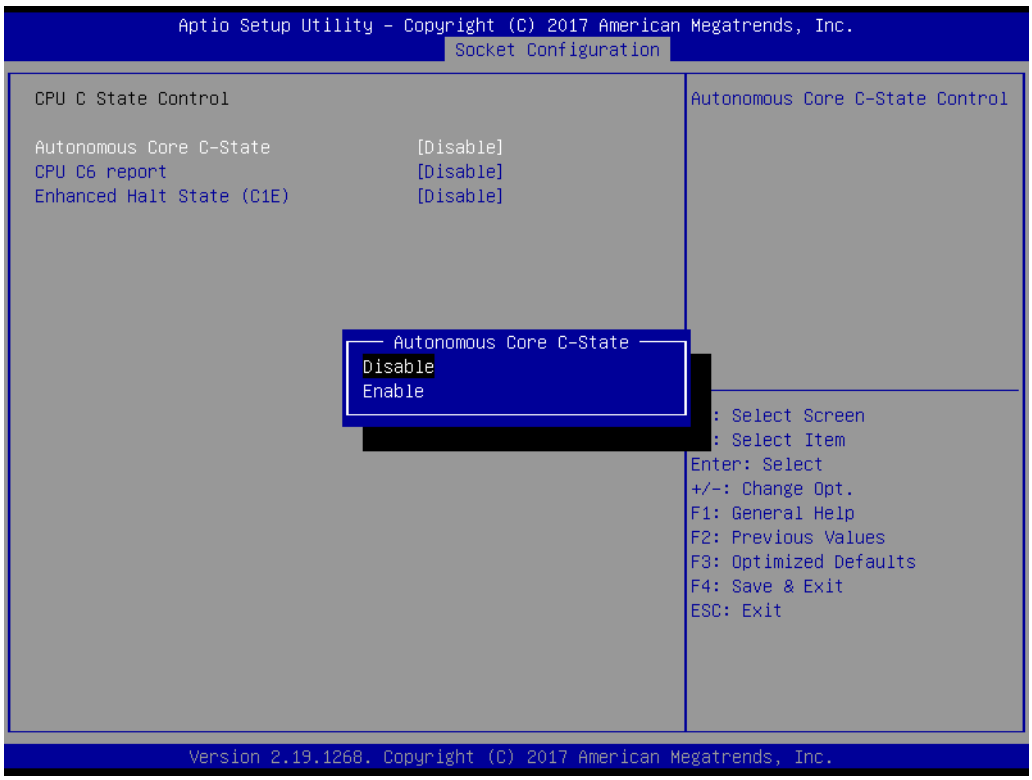


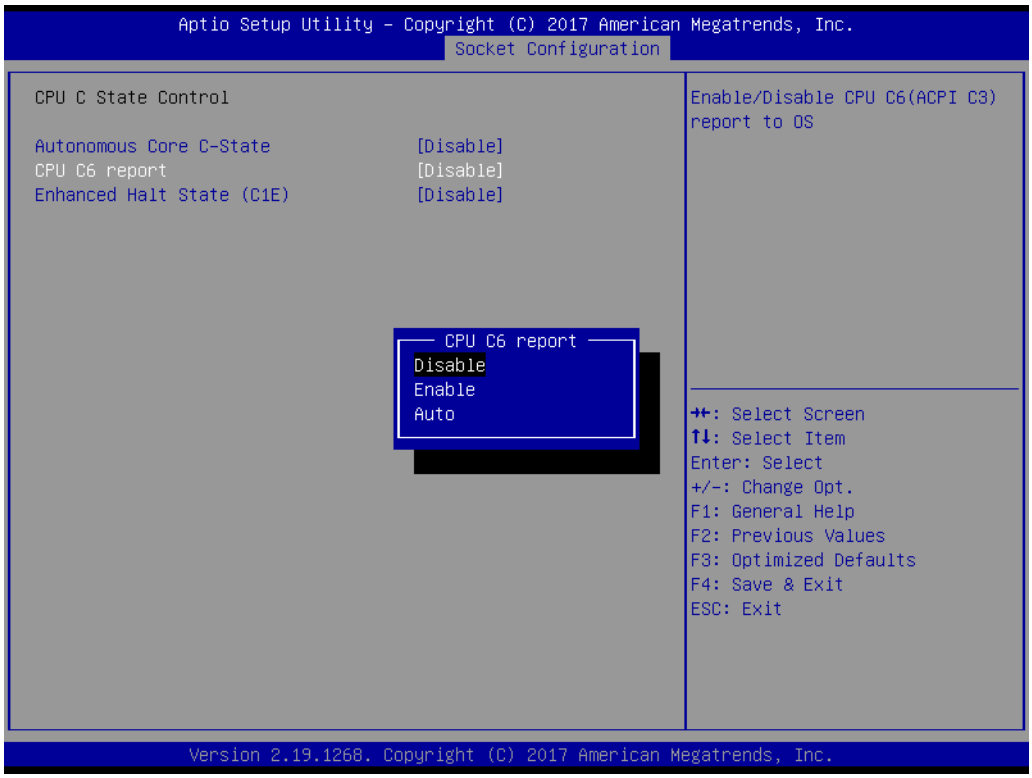

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc.
Socket Configuration
CPU P State Control
SpeedStep (Pstates) [Enable]
Config TDP [Nominal]
Boot performance mode [Max Performance]
Energy Efficient Turbo [Enable]
Turbo Mode [Enable]
Boot performance mode
Max Performance
Max Efficient
Select the performance state that the BIOS will set before OS hand off.
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc.

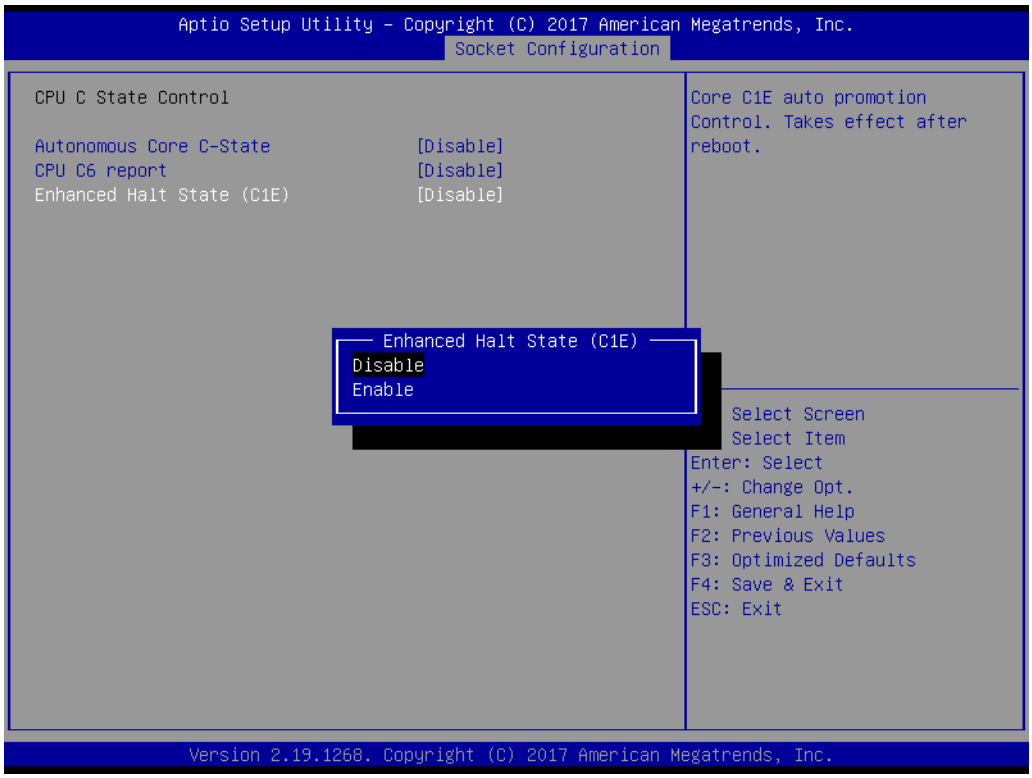

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc.
Socket Configuration
CPU P State Control
SpeedStep (Pstates) [Enable]
Config TDP [Nominal]
Boot performance mode [Max Performance]
Energy Efficient Turbo [Enable]
Turbo Mode [Enable]
Energy Efficient Turbo
Enable
Disable
Energy Efficient Turbo
Disable, MSR 0x1FC [19]
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc.



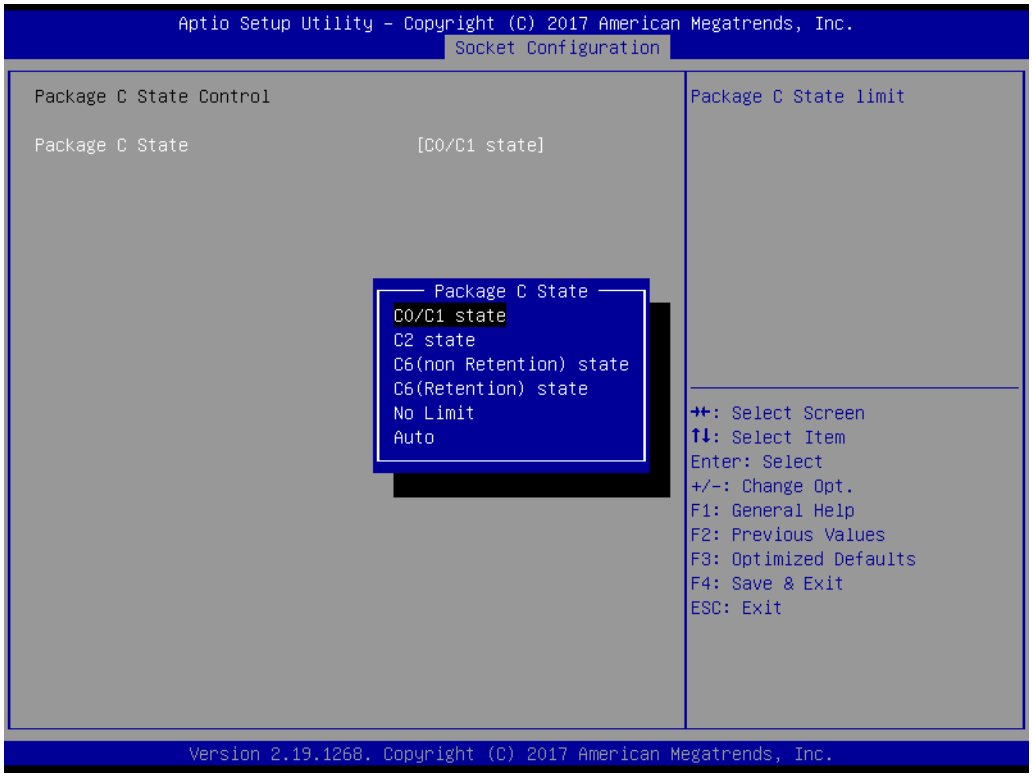
CPU C State Control



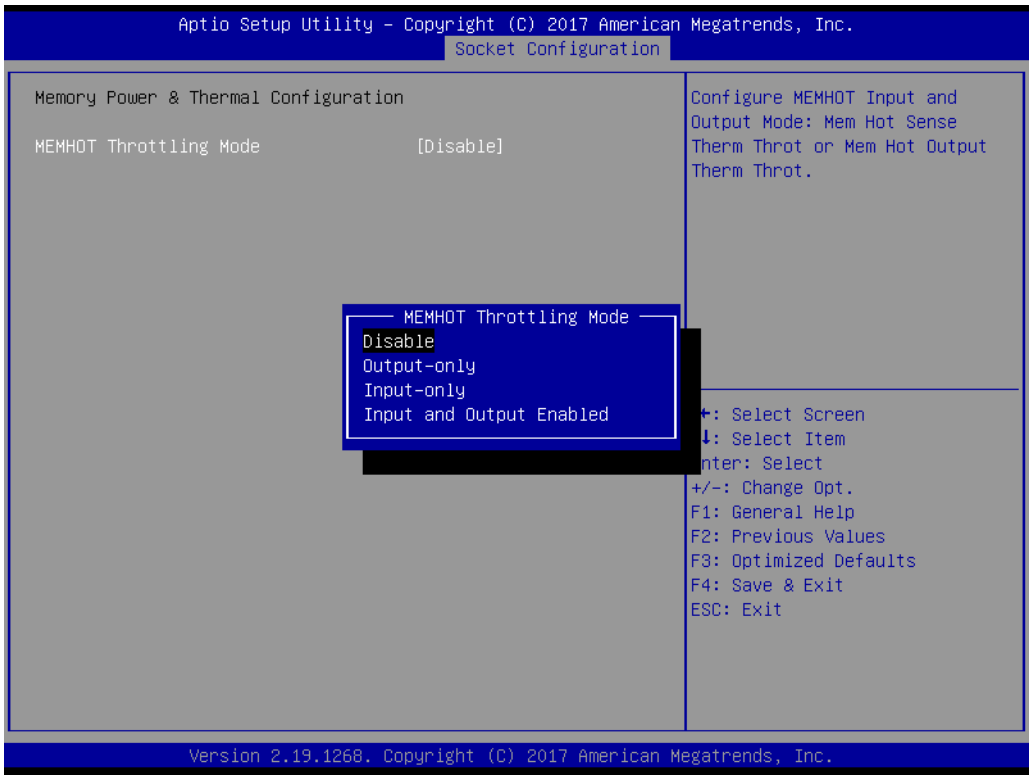

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc.
Socket Configuration
CPU C State Control
Autonomous Core C-State [Disable]
CPU C6 report [Disable]
Enhanced Halt State (C1E) [Disable]
CPU C6 report
Disable
Enable
Auto
Enable/Disable CPU C6(ACPI C3) report to OS
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc.


Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc.
Socket Configuration
CPU C State Control
Autonomous Core C-State [Disable]
CPU C6 report [Disable]
Enhanced Halt State (C1E) [Disable]
Enhanced Halt State (C1E)
Disable
Enable
Core C1E auto promotion Control. Takes effect after reboot.
Select Screen
Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc.

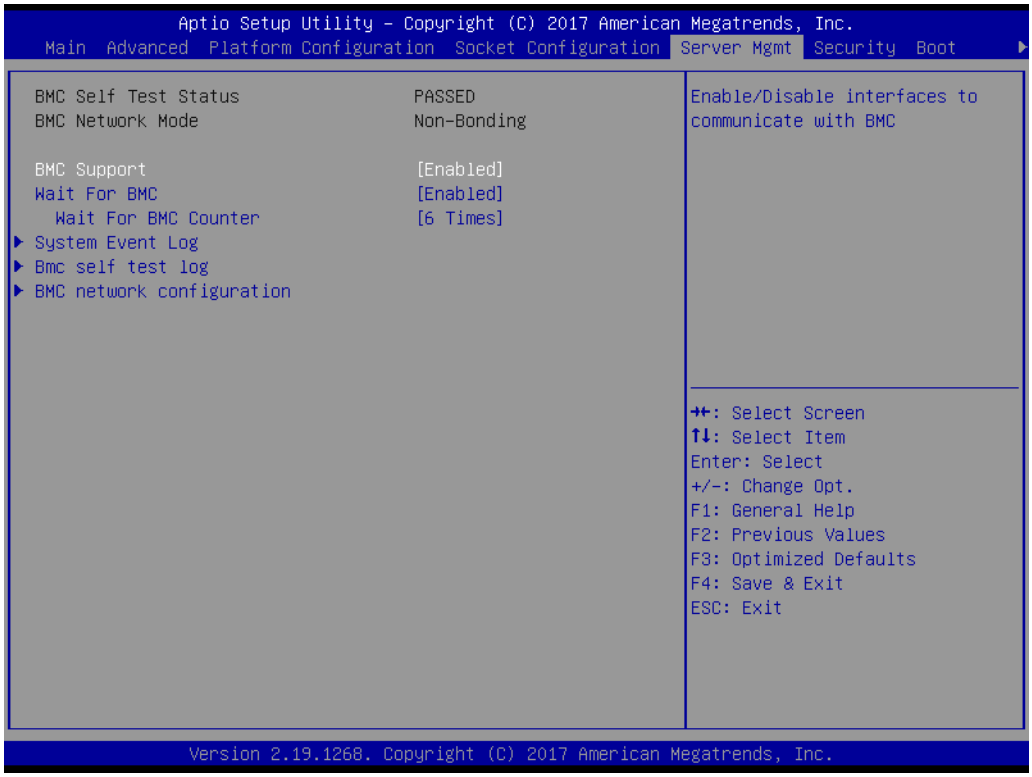
Package C State Control



Memory Power & Thermal Configuration

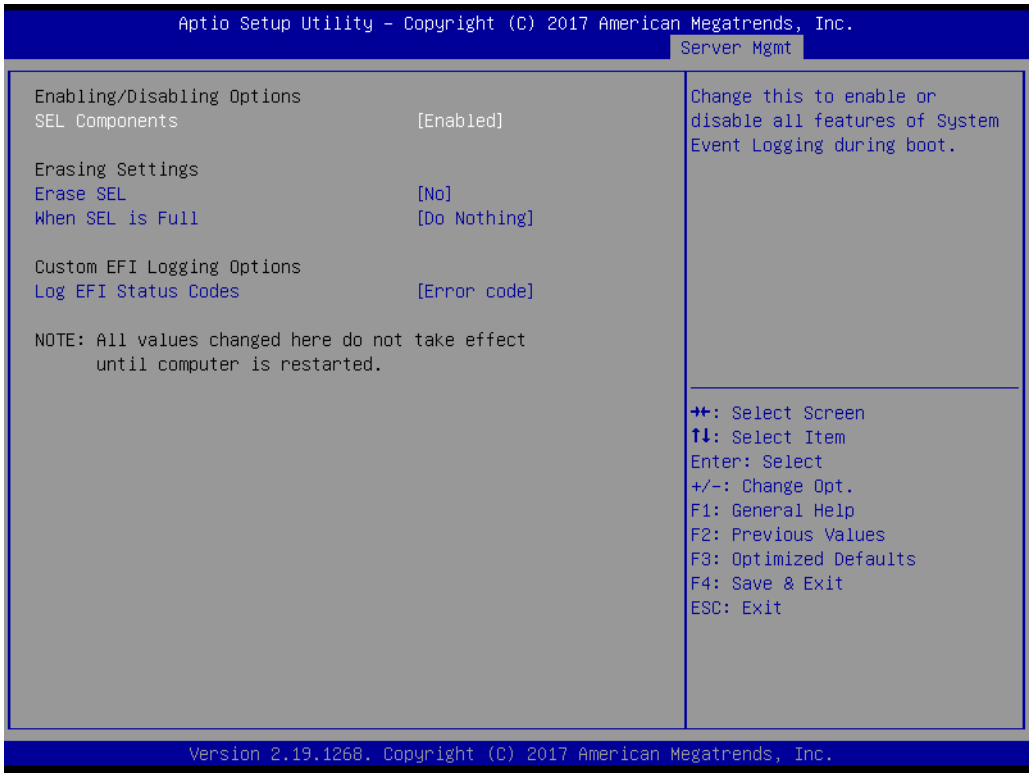


3.2.5 Server Management



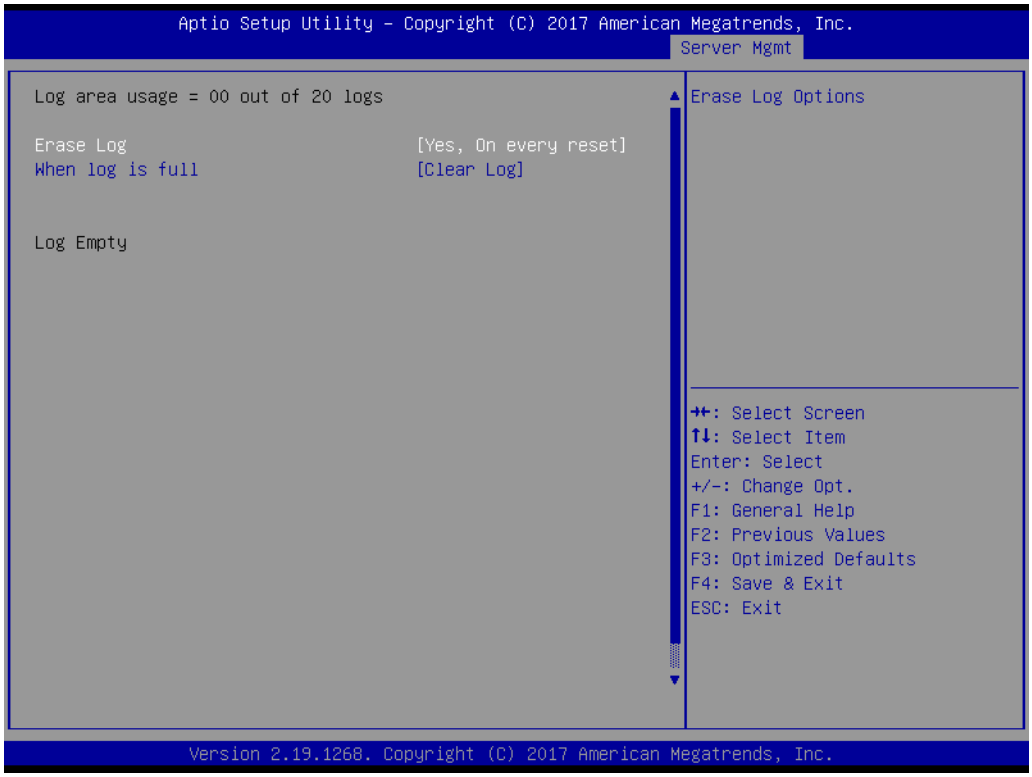
- **BMC Support**
  Enable or disables interfaces to communicate with BMC.
- **Wait for BMC**
  If enabled, motherboard will wait 30 ~ 60 seconds until BMC module boots up completely. After that, the normal BIOS post screen will be displayed.
  If disabled, motherboard will not wait for the BMC module's response.
- **Wait for BMC counter**
  Initializes host to BMC interfaces.
  The MB beeps per 5 seconds to check it.

3.2.5.1 System Event Log



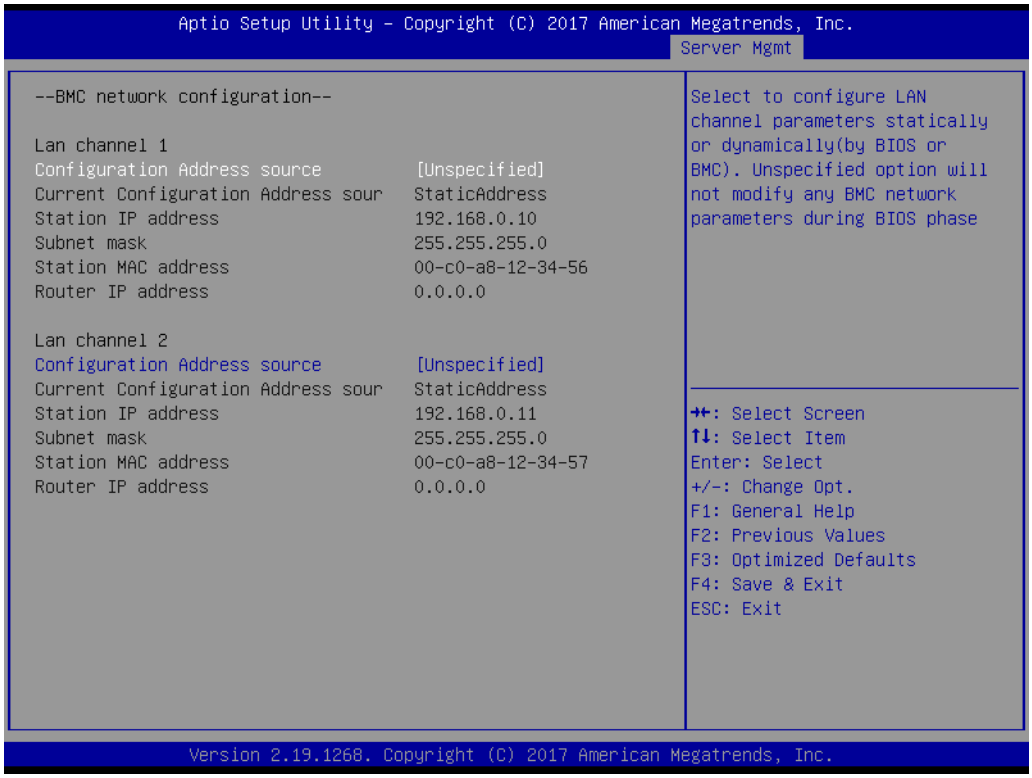
- **SEL Components**
  Enable/disable all features of system event logging during boot.
- **Erase SEL**
  Choose options for erasing SEL.
- **When SEL is Full**
  Choose options for reactions to a full SEL.
- **Log EFI Status Codes**
  Disable the logging of EFI status codes or log only error code or only progress code or both.

3.2.5.2 BMC Self Test Log



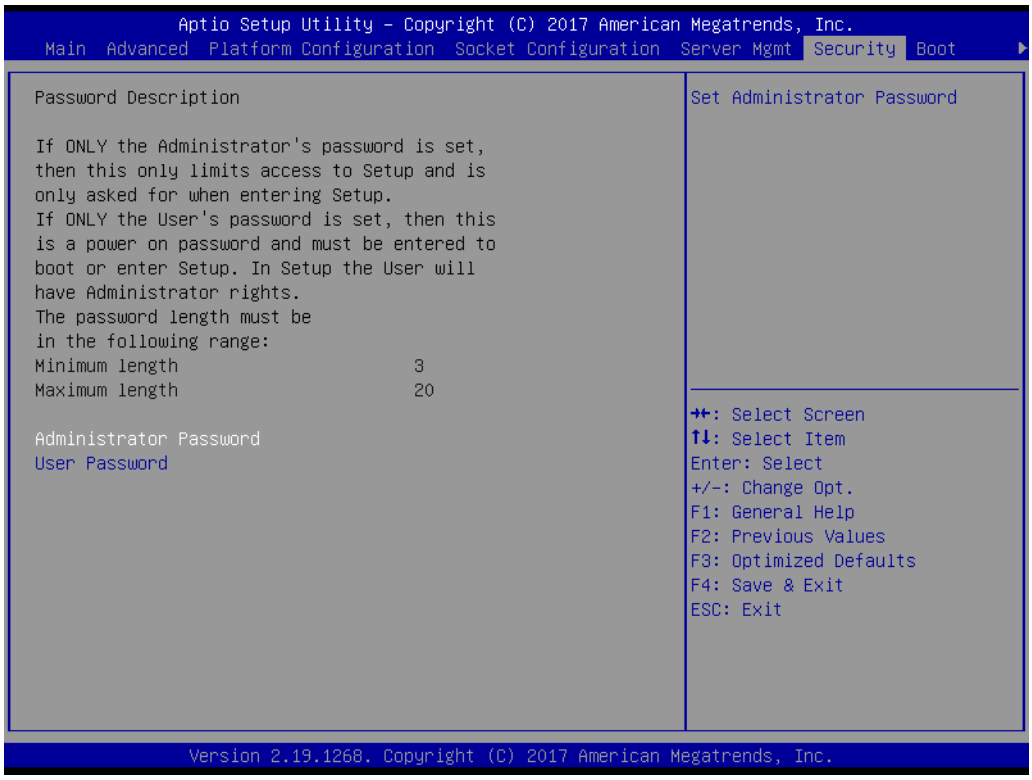
- **Erase Log**
  Erase log options.
- **When Log is Full**
  Select the action to be taken when the log is full.

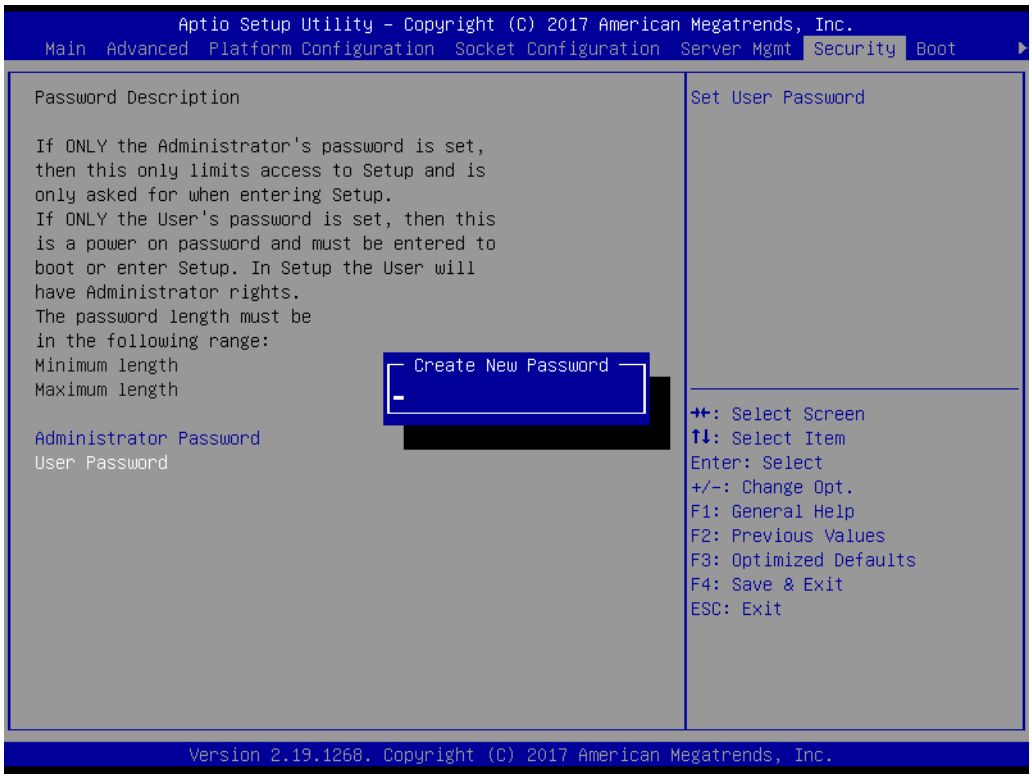
3.2.5.3 BMC Network Configuration



- **Configuration Address Source**
  Select to configure LAN channel parameters statically or dynamically (by BMC). Unspecified option will not modify any BMC network parameters during BIOS phase.

3.2.6 Security





Note! ***With AC power & Battery. Short CMOS1 Jumper:***

Date/Time & Password: Keep

Setting: reset to default

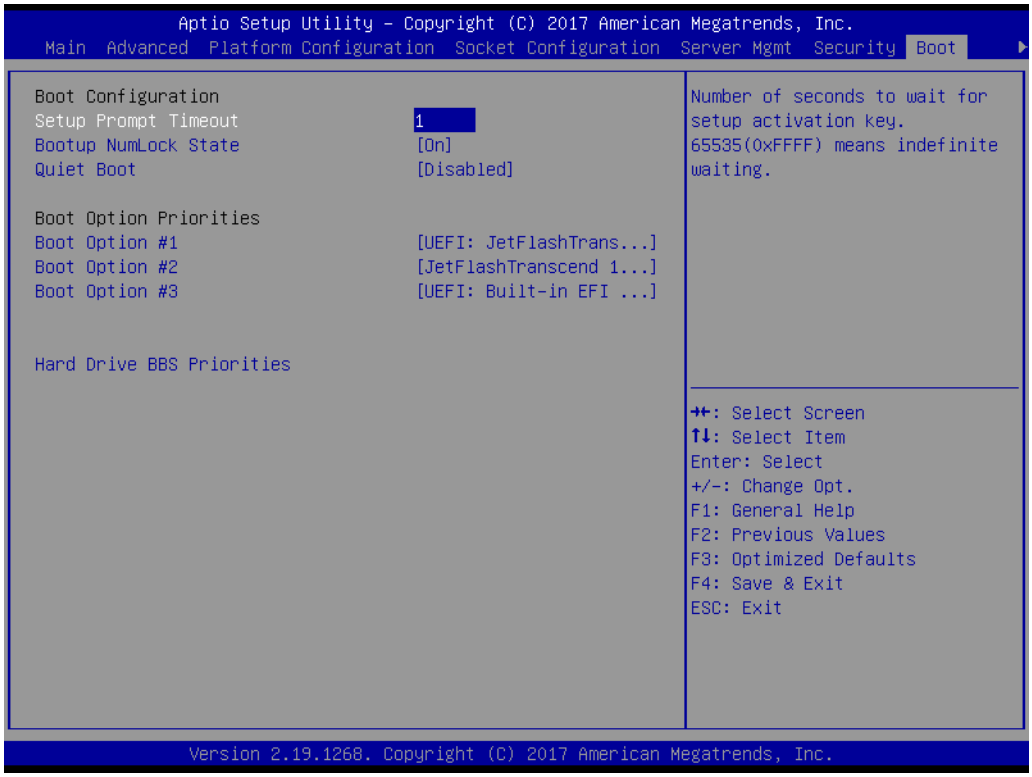
AC power and CMOS battery are removed. Short CMOS1 Jumper:

Date/Time: reset to default

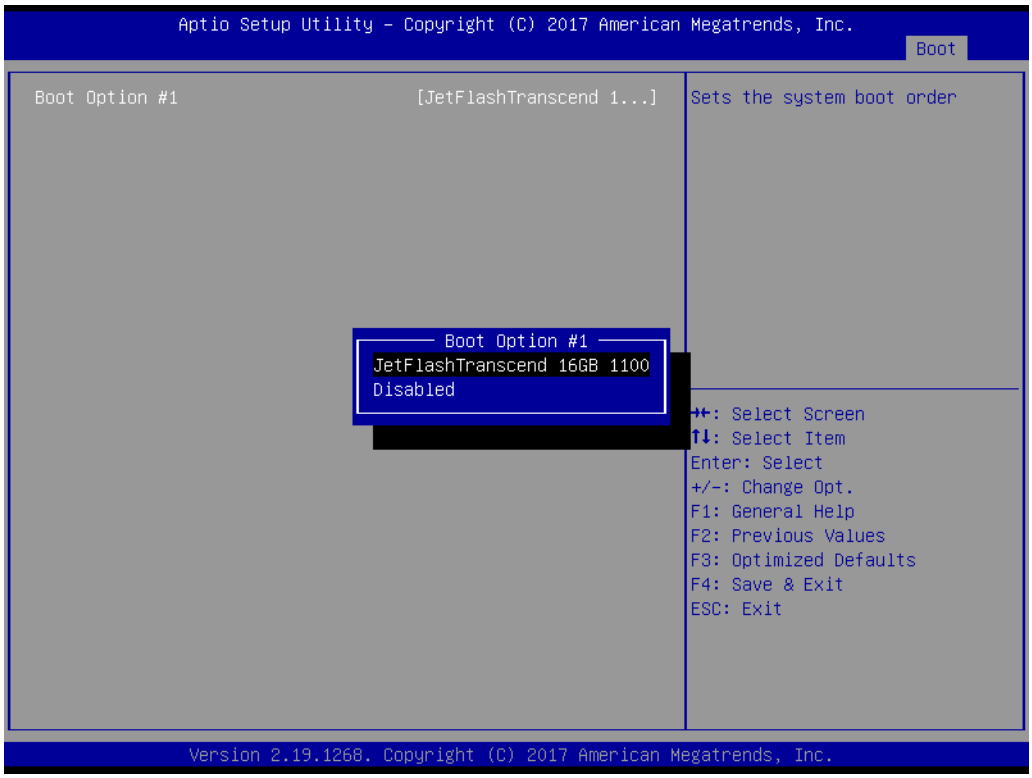
Password: keep

Setting: reset to default

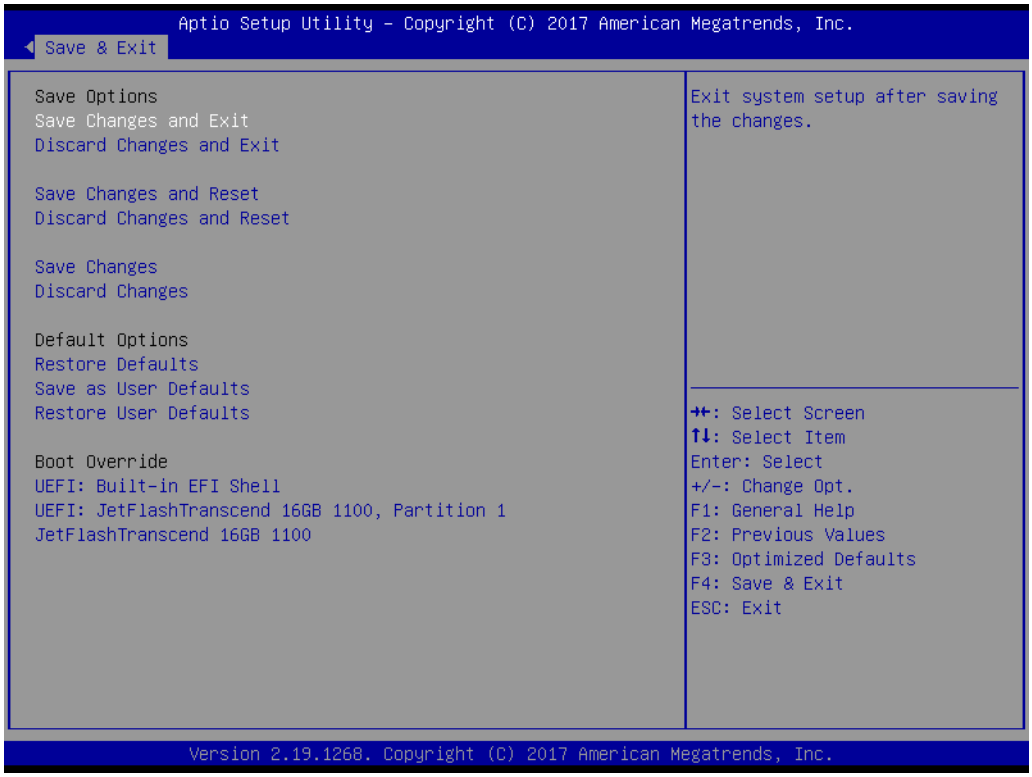
3.2.7 Boot



- **Setup Prompt Timeout**
  Number of seconds to wait for setup activation key.
- **Bootup NumLock State**
  Select the keyboard NumLock state as "On" or "Off".
- **Quiet Boot**
  Enable or disable quiet boot option.
- **Boot Option Priorities**
  Sets the system boot priorities.
- **Hard Drive BBS Priorities**
  Display this item when external legacy devices are plugged in to set boot priorities.



3.2.8 Save & Exit



- **Save Changes and Exit**
  Exit system setup after saving the changes.
- **Discard Changes and Exit**
  Exit system setup without saving any changes.

- **Save Changes and Reset**
  Reset the system after saving changes.
- **Discard Changes and Reset**
  Reset system setup without saving any changes.
- **Save Changes**
  Save changes done so far to any of the setup options.
- **Discard Changes**
  Discard changes done so far to any of the setup options.
- **Restore Defaults**
  Restore/Load default values for all the setup options.
- **Save as User Defaults**
  Save the changes done so far as user defaults.
- **Restore User Defaults**
  Restore the user defaults to all the setup options.

Chapter 4

Chipset Software Installation Utility

4.1 Before Beginning

To facilitate the installation of the enhanced display drivers and utility software, read the instructions in this chapter carefully. The drivers for the ASMB-975 are located on the software installation CD.

Before beginning, it is important to note that most display drivers need to have the relevant software application already installed on the system prior to installing the enhanced display drivers. In addition, many of the installation procedures assume that you are familiar with both the relevant software applications and operating system commands. Review the relevant operating system commands and the pertinent sections of your application software's user manual before performing the installation.

4.2 Introduction

4.2.1 Main Menu

The Intel Chipset Software Installation (CSI) utility installs the Windows INF files that outline to the operating system how the chipset components will be configured. This is needed for the proper functioning of the following features:

- Core PCI PnP services
- Serial ATA interface support
- USB 1.1/2.0/3.0 support
- Identification of Intel chipset components in the Device Manager

Note! *The files on the software installation CD are compressed. Do not attempt to install the drivers by copying the files manually. You must use the supplied SETUP program to install the drivers.*

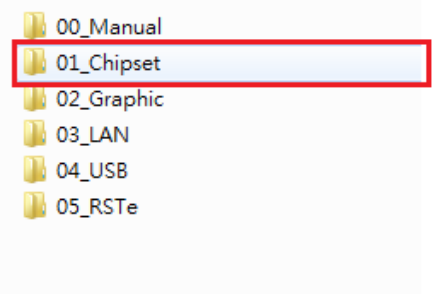
Note! *The chipset driver is used for the following versions of Windows, and it has to be installed before installing all the other drivers:*

Windows Server 2016 Standard	*x64*
Windows Server 2012 R2 Standard	*x64*
Windows 10 Ultimate	*x64*

Note! *It is necessary to update all the latest Microsoft hot fix files when using this OS.*

4.3 Windows 10/ Windows server 2012 R2 & 2016

1. Insert the driver CD into your system's CD-ROM drive. When the folder is displayed, move the mouse cursor over the folder 01_Chipset. Find the executable in this folder, click to install the driver.



Chapter 5

Graphic Setup

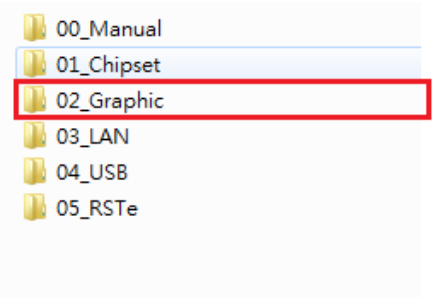
5.1 Introduction

Install the ASPEED VGA driver to enable this function, which includes the following features:

- 32-bit 2D graphics engine on board for normal use.
- 64 MB RAM for this chip, the highest resolution is 1920x1200.

5.2 Windows Series Driver Setup

Insert the driver CD into your system's CD-ROM drive. When the folder is displayed, navigate to the "Graphic" folder and click the executable file to complete the installation of the drivers for the OS that you need.



Note!

1. *If ASMB-975 Series carries an additional graphics card for VGA output, please set this additional graphic card as "major output" under the "Display properties" of OS.*
2. *The WDDM driver can support for the following OS versions:*
   - *Windows 8 x86/x64 version*
   - *Windows 8.1 x86/x64 version*
   - *Windows Server 2012 version (WHQL)*
   - *Windows Server 2012R2 version (WHQL)*
   - *Windows 10 x86/x64 version*
   - *Windows Server 2016 version (WHQL)*
3. *ASPEED Graphics WDDM Driver Limitation on Microsoft Windows OS.*
   - *It is non-WHQL certified driver because ASPEED VGA is a 2D VGA, it cannot meet the WHQL requirement of WDDM driver which requires 3D VGA function.*
   - *Because it is non-WHQL certified driver, it may meet some compatible issues with some specific applications.*
   - *Does not support modes with different display frequencies.*

Chapter 6

LAN Configuration & USB 3.0

6.1 LAN Configuration

6.1.1 Introduction

The ASMB-975T2 Series has two Gigabit Ethernet LAN connections via dedicated PCI Express x1 lanes: GbE LAN1 - Intel I210; GbE LAN2 - I210; two 10G Base-T LAN connectors LAN3 and LAN4 - Intel X557 PHY. They eliminate bottlenecks of network data flow and incorporate Gigabit Ethernet at 10 Gbps.

6.1.2 Features

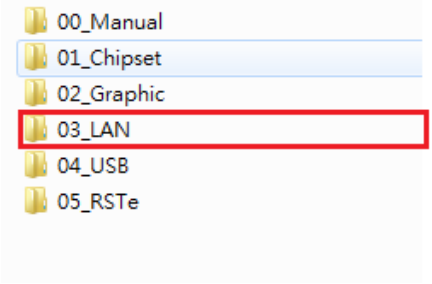
- 10/100/1000&10G Base-T Ethernet controller
- 10/100/1000&10G Base-T triple-speed MAC
- Full duplex at 10/100/1000 Mbps or 10 Gbps and half duplex at 10/100/1000 Mbps
- Wake-on-LAN (WOL) support
- PCIe x1 host and PHY interface

6.1.3 Installation

The integrated Intel gigabit Ethernet controller supports all major network operating systems. However, the installation procedure varies with different operating systems. In the following sections, refer to the one that provides the driver setup procedure for the operating system you are using.

6.1.4 Windows Series Driver Setup (LAN)

1. Insert the driver CD into your system's CD-ROM drive. Select folder "03_LAN" then click the proper LAN driver for the OS.



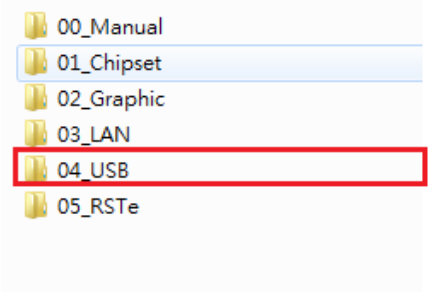
6.2 USB 3.0

6.2.1 Introduction

ASMB-975 offers eight USB 3.0 ports, four in rear side and four via onboard header. The USB 3.0 could provide the bandwidth up to 500MB/s to shorter the time for data transmission.

6.2.2 Windows Series Driver Setup

Insert the driver CD into your system's CD-ROM drive. Select folder "04_USB" then click the Setup.exe file for the installation.

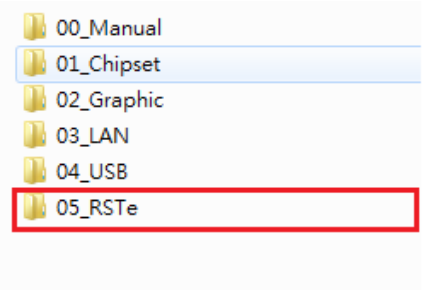


6.3 SATA & PCIe SSD RAID

Intel C622/C621 PCH chip offers SATA & PCIe SSD RAID under Windows operating system.

Note! *1.Please visit the Intel download center for "Intel Rapid Storage Technology enterprise for Microsoft Windows Operating System Software User's Guide" file download,*

2.For the hotfix file download, please visit Microsoft website.



Appendix A

Programming the Watchdog Timer

The ASMB-925 Series watchdog timer can be used to monitor system software operation and take corrective action if the software fails to function within the programmed period. This section describes the operation of the watchdog timer and how to program it.

A.1 Watchdog Timer Overview

The watchdog timer is built in to the EC controller IT8528E. It provides the following functions for user programming:

- Can be enabled and disabled by user program
- Timer can be set from 1 to 255 seconds
- Generates an interrupt or resets signal if the software fails to reset the timer before time-out

A.2 Programming the Watchdog Timer

The I/O port address of the watchdog timer is as below:

Address	Description	
0x57	Event - Warm Reset: 0x04	
0x5E	Warm Reset Timer (High BYTE)	Based 100ms
0x5F	Warm Reset Timer (Low BYTE)	

Here is an example to step by step program the Watchdog Timer.

Step	Action	Description
00	Read 0x299 port	Clear I/O port
	Wait IBF clear	0x29A, BIT1, = 0
01	Write 0x89 to 0x29A	
	Wait IBF clear	0x29A, BIT1, = 0
02	Write 0x5E to 0x299 port	
	Wait IBF clear	0x29A, BIT1, = 0
03	Write 0x00 to 0x299 port	Set 10 sec (high byte)
	Wait IBF clear	0x29A, BIT1, = 0
04	Write 0x89 to 0x29A	
	Wait IBF clear	0x29A, BIT1, = 0
05	Write 0x5F to 0x299 port	
	Wait IBF clear	0x29A, BIT1, = 0
06	Write 0x64 to 0x299 port	Set 10 sec (low byte)
	Wait IBF clear	0x29A, BIT1, = 0
07	Write 0x89 to 0x29A	
	Wait IBF clear	0x29A, BIT1, = 0

08	Write 0x57 to 0x299 port	Watchdog Event
	Wait IBF clear	0x29A, BIT1, = 0
09	Write 0x04 to 0x299 port	(Warm) Reset event
	Wait IBF clear	0x29A, BIT1, = 0
10	Write 0x28 to 0x29A	Start watchdog
	Wait 1 ~ 9 sec	
	Wait IBF clear	0x29A, BIT1, = 0
11	Write 0x29 to 0x29A	Stop watchdog
	Wait IBF clear	0x29A, BIT1, = 0
12	Go to Step 07	

Appendix B

I/O Pin Assignments

B.1 USB Header (USB9_10)

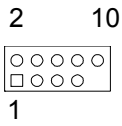


Table B.1: USB Header

Pin	Signal	Pin	Signal
1	USB_VCC5	2	USB_VCC5
3	USB_D-	4	USB_D-
5	USB_D+	6	USB_D+
7	GND	8	GND
9	Key	10	N/C

B.2 USB3.0 Header(USB5_6, USB7_8)

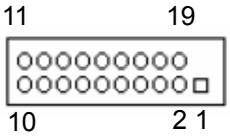


Table B.2: USB Header

Pin	Signal	Pin	Signal
1	+5 V	2	STDA_SSRX-
3	STDA_SSRX+	4	GND
5	STDA_SSRX-	6	STDA_SSRX+
7	GND	8	D-
9	D+	10	N/C (OC pin reserved)
11	D+	12	D-
13	GND	14	STDA_SSRX+
15	STDA_SSRX-	16	GND
17	STDA_SSRX+	18	STDA_SSRX-
19	+5 V	20	

B.3 VGA Connector (VGA1)

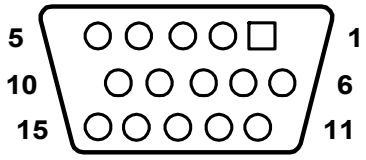


Table B.3: VGA Connector			
Pin	**Signal**	**Pin**	**Signal**
1	RED	9	VCC
2	GREEN	10	GND
3	BLUE	11	N/C
4	N/C	12	SDT
5	GND	13	H-SYNC
6	GND	14	V-SYNC
7	GND	15	SCK
8	GND		

B.4 RS-232 Interface (COM2)

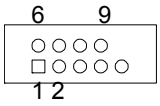


Table B.4: RS-232 Interface	
Pin	**Signal**
1	DCD
2	RXD
3	TXD
4	DTR
5	GND
6	DSR
7	RTS
8	CTS
9	RI

B.5 External Keyboard Connector (KBMS1)

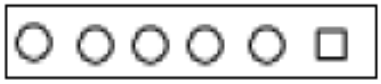


Table B.5: External Keyboard Connector	
Pin	**Signal**
1	KB CLK
2	KB DATA
3	MS DATA
4	GND
5	VCC
6	MS CLK

B.6 CPU and System Fan Power Connector (CPUFAN0~1, SYSFAN0~SYSFAN4)

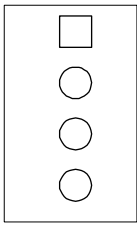


Table B.6: Fan Power Connector	
Pin	**Signal**
1	GND
2	+12 V
3	DETECT
4	PWM

B.7 Power LED (JFP3)

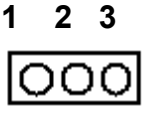


Table B.7: Power LED	
Pin	**Function**
1	LED power (3.3 V)
2	N/C
3	Ground

B.8 External Speaker Connector (JFP2)

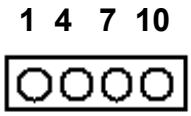


Table B.8: External Speaker Connector	
Pin	**Function**
1	SPK+
4	N/C
7	BZ-
10	SPK-

B.9 Reset Connector (JFP1)

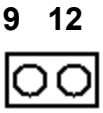


Table B.9: Reset Connector	
Pin	**Signal**
9	RESET
12	GND

B.10 HDD LED Connector (JFP1)

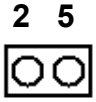


Table B.10: HDD LED Connector	
Pin	**Signal**
2	HDD_LED+
5	HDD_LED-

B.11 ATX Soft Power Switch (JFP1)

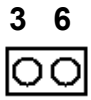
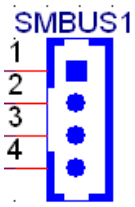


Table B.11: ATX Soft Power Switch

Pin	Signal
3	PWR-BTN
6	GND

B.12 Front panel SMBus Connector (SMBUS1)



1	+V5
2	SMB_CLK_RESUME
3	SMB_DATA_RESUME
4	GND

B.13 USB/LAN Ports (LAN1_USB1_2, LAN2_USB3_4)

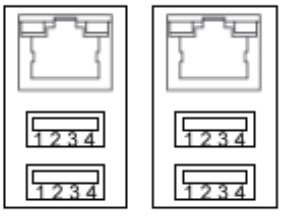


Table B.12: USB Port

Pin	Signal	Pin	Signal
1	VCC_DUAL	3	Data0+
2	Data0-	4	GND

Table B.13: Giga LAN 10/100/1000 Base-T RJ-45 Port

Pin	Signal	Pin	Signal
1	MID0+	4	MID2+
2	MID0-	5	MID2-
3	MID1+	7	MID3+
6	MID1-	8	MID3-

B.14 Audio Connector (HDAUD1)

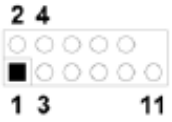


Table B.14: Front Panel Audio Connector			
Pin	**Signal**	**Pin**	**Signal**
1	+5V_AUDIO	2	GND
3	ACZ_SYNC	4	ACZ_BITCLK
5	ACZ_SDOUT	6	ACZ_SDIN0
7	ACZ_SDIN1	8	ACZ_RST
9	+12V_AUDIO	10	GND
11	GND		

B.15 Alarm Board Connector (VOLT1)

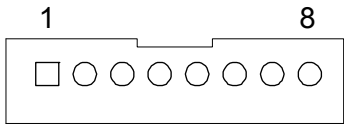


Table B.15: Alarm Board Connector			
Pin	**Signal**	**Pin**	**Signal**
1	5VSB	5	+5V
2	GND	6	+3.3V
3	GND	7	-12V
4	-5V	8	+12V

B.16 Case Open Connector (JCASE1)

Table B.16: Case Open Connector	
Pin	**Signal**
1	CASEOP
2	GND

B.17 Front Panel LAN LED Connector (LANLED1)

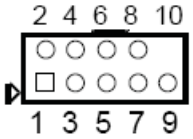
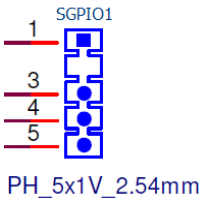


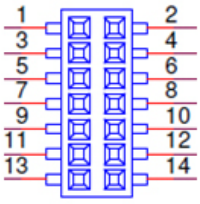
Table B.17: LAN LED Connector			
Pin	**Signal**	**Pin**	**Signal**
1	LAN1_LED_ACT#	2	LAN2_LED_ACT#
3	+V3.3_AUX	4	+V3.3_AUX
5	LAN3_LED_ACT#	6	LAN4_LED_ACT#
7	+V3.3_AUX	8	+V3.3_AUX
9	NC		

B.18 SATA SGPIO (SGPIO1/SGPIO2)



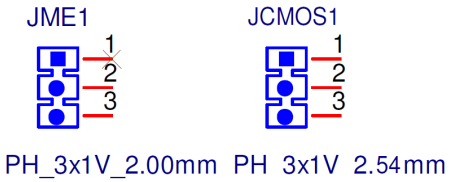
1	SGPIO_SATA_CLOCK
2	N/C
3	SGPIO_SATA_LOAD
4	SGPIO_SATA_DATA0
5	SGPIO_SATA_DATA1

B.19 LPC Connector (LPC1)



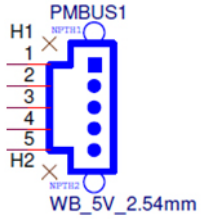
1	CLK_24M_LPCCN	2	LPC_AD1
3	PLTRST_LPC	4	LPC_AD0
5	LPC_FRAME#	6	+3.3 V
7	LPC_AD3	8	GND
9	LPC_AD2	10	SMB_SCL_LPC
11	SERIRQ_PCH	12	SMB_SDA_LPC
13	+5V_AUX	14	+5V

B.20 Clear CMOS and Update ME Connector (JCMOS1, JME1)



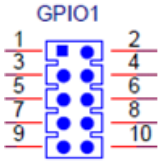
	JCMOS1	JME1
1	N/C	N/C
2	RTC_RST_PCH	PCH_HDA_SDO
3	GND	V3.3_AUX

B.21 PMBUS Connector (PMBUS1)



1	SMB_SCL_PM
2	SMB_SDA_PM
3	SMB_ALERT_PM#
4	GND
5	+V3.3_AUX

B.22 GPIO Connector (GPIO1)



1	EC_GPIO0	2	EC_GPIO4
3	EC_GPIO1	4	EC_GPIO5
5	EC_GPIO2	6	EC_GPIO6
7	EC_GPIO3	8	EC_GPIO7
9	+VCC_GPIO	10	GND

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